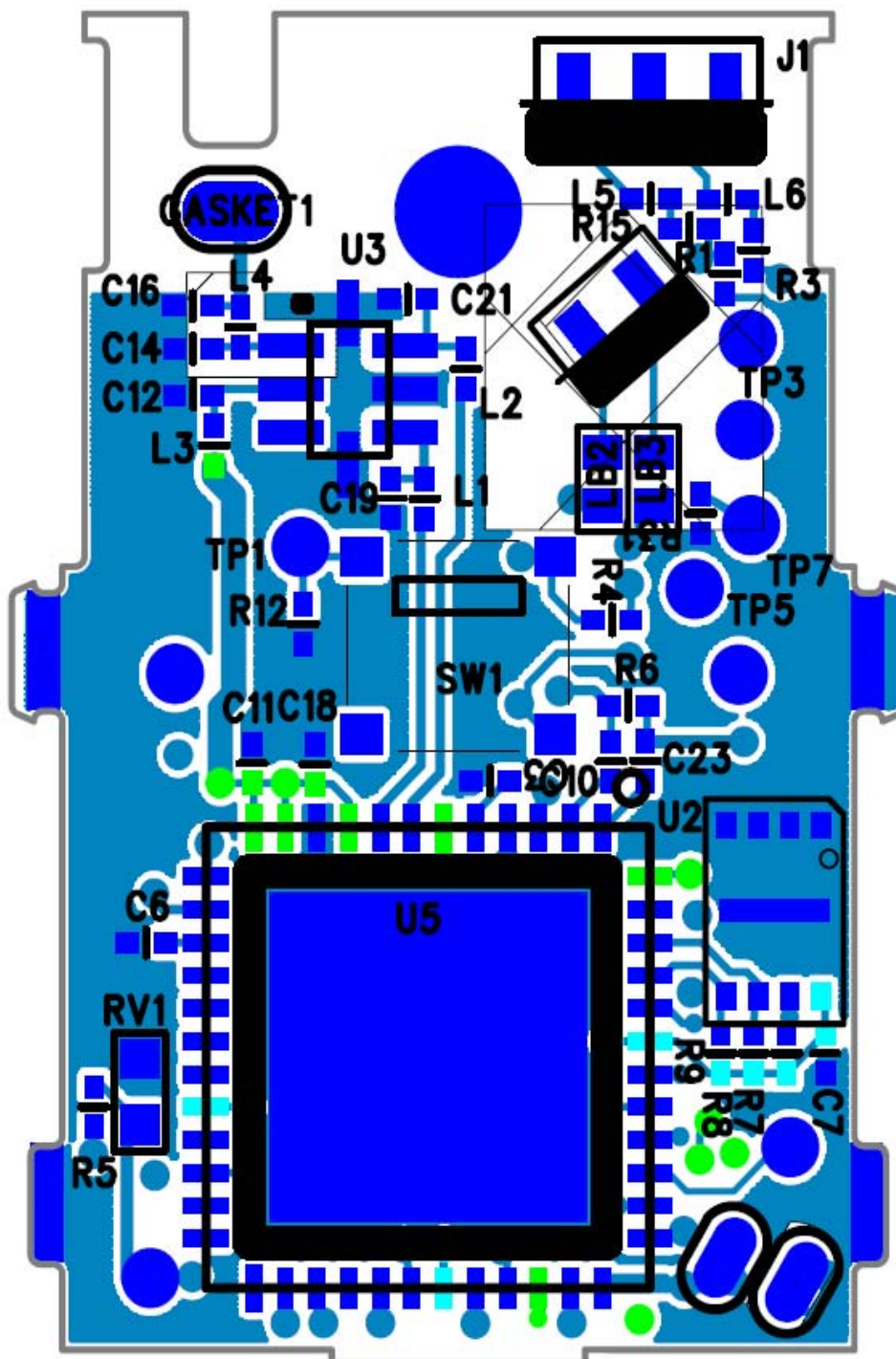


9. PCB Diagram

9.1. TOP



9.6. Bottom Layer

