



TIMCO ENGINEERING INC.

849 NW State Road 45
Newberry, Florida 32669

Date : December 28, 2005

Subject : Correspondence of your question. Job # 2591KC5
FCC ID: BEJL1970HN

Dear TEI,

Thanks for your question.

I would like to answer to your question for the Job #2591KC5 (BEJL1970HN) as below;

- The purpose of change is upgrading the picture performance. The response time of LCD picture is from 8ms to 4ms. The below parts are added or changed to make upgrading the visual performance.
 - Changed Scaler IC : TSU56AWL
 - Added SDRAM 16M
 - Added ODC(overdrive circuit) IC: NVI S5001
- ➔ The main function (i.e. Resolution timing table, Data clock frequency, VCC Power) is exactly same with originals. Therefore, I believe it can comply with FCC regulation Part 2.1043 and to be treated Class II permissive change.
- The applicant fully understands the Grant note 24 and 28. It's not problem to put the Grant note on the Grant of Class II permissive Change

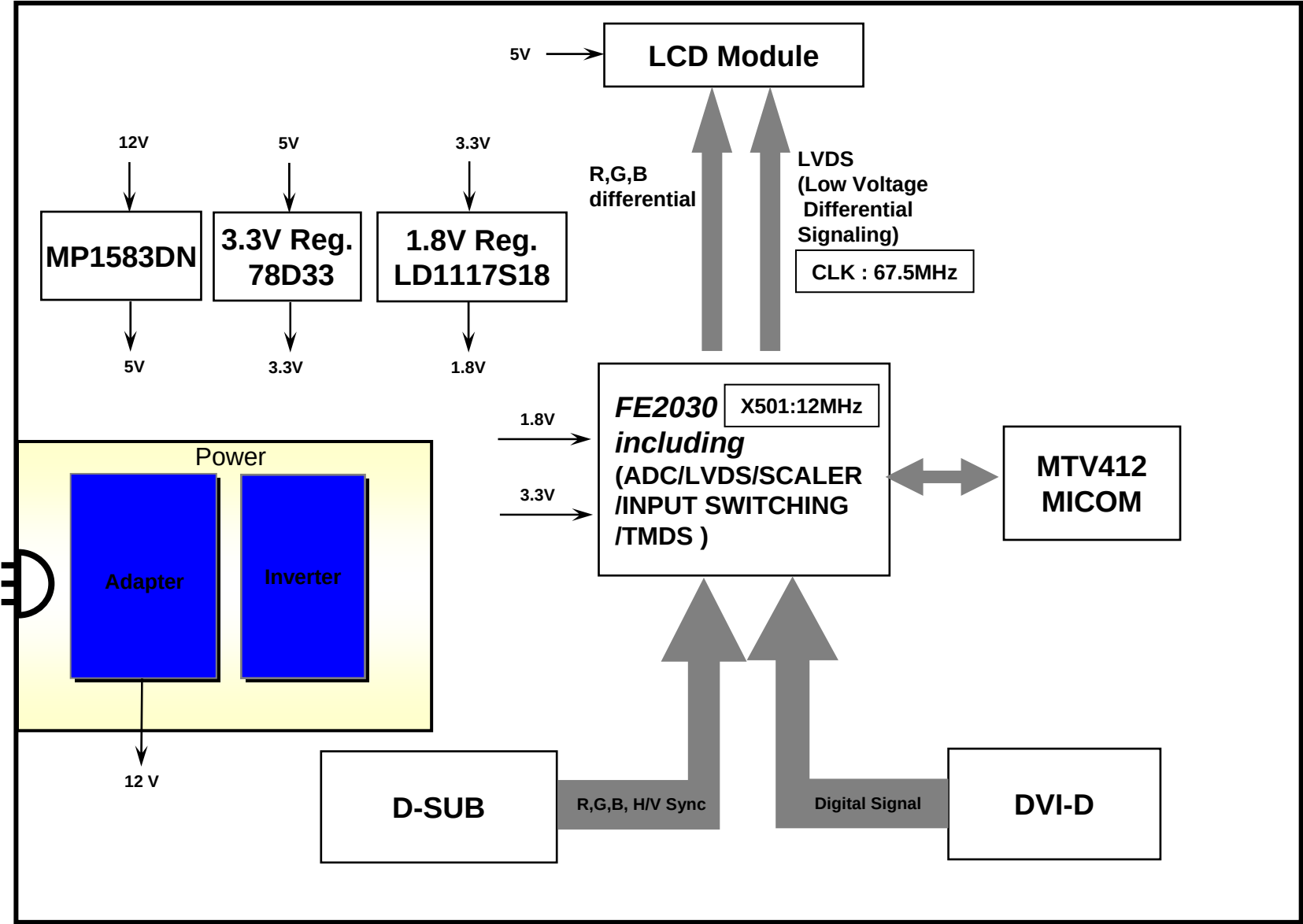
We believe that you would satisfy the attached correspondence. however, if there are any questions or problems, please do not hesitate to contact me.

I look forward to hearing your favorable reply soon.

Best Regards,

Tae-Sig(Ted) Park, Technical Manager
Gumi College EMC Center

Main Board Block Diagram (Before)



Main Board Block Diagram (AFTER)

