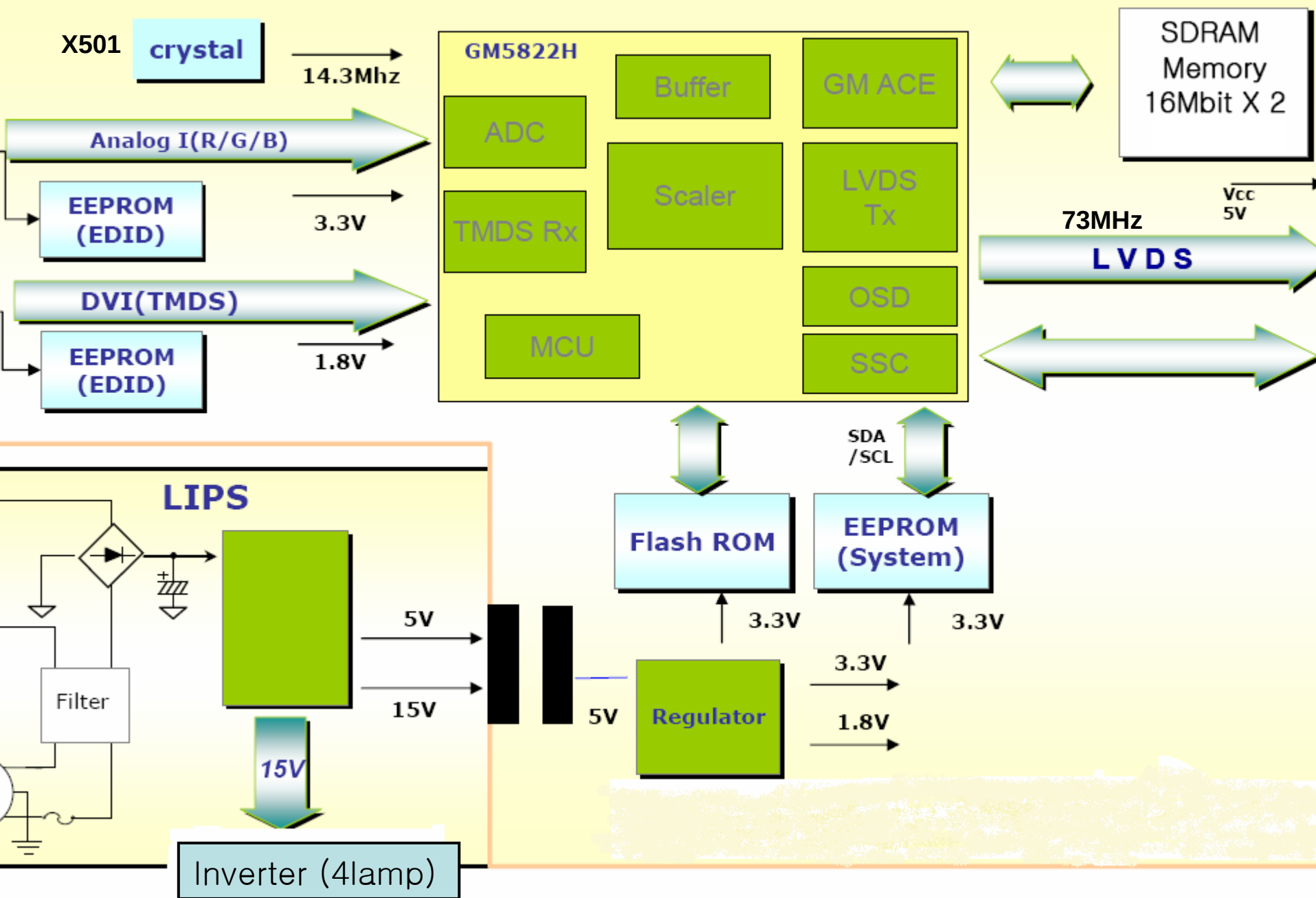


APPENDIX D
: BLOCK DIAGRAM



DESCRIPTION OF BLOCK DIAGRAM

1. Video Controller Part.

This part amplifies the level of video signal for the digital conversion and converts from the analog video signal to the digital video signal using a pixel clock.

The pixel clock for each mode is generated by the PLL.

The range of the pixel clock is **146MHz** In L206WTQ/WTG.

This part consists of the Scaler, ADC convertor, TMDS receiver and LVDS transmitter.

The Scaler gets the video signal converted analog to digital, interpolates input to **1680X1050(L206WTQ/WTG)** resolution signal and outputs 8-bit R, G, B signal to transmitter.

2. Power Part.

This part consists of the one 3.3V, and one 1.8V regulators to convert power which is provided 5V in Power board.

15V is provided for inverter in **L206WTQ/WTG**.

Also, 5V is converted 3.3V and 1.8V by regulator. Converted power is provided for IC in the main board.

The inverter converts from DC **15V** to AC 820Vrms and operates back-light lamps of module in **L206WTQ/WTG**.

3. MICOM Part.

This part is include video controller part. And this part consists of EEPROM IC which stores FRC(OD) memory, control data, Reset IC and the Micom.

The Micom distinguishes polarity and frequency of the H/V sync are supplied from signal cable.

The controlled data of each modes is stored in EEPROM.
