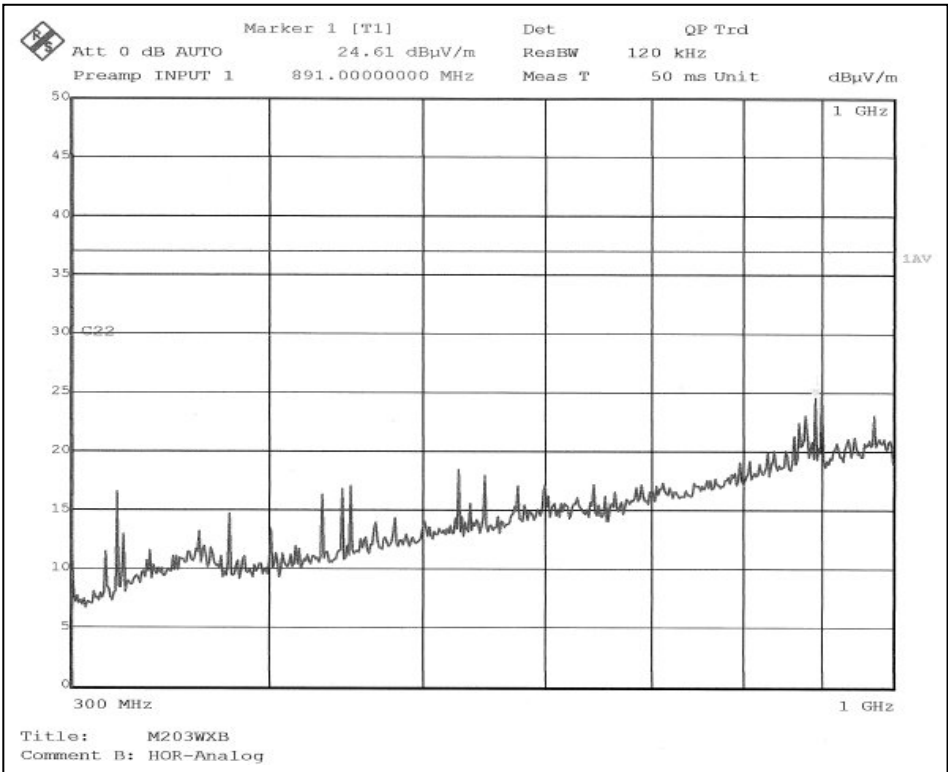
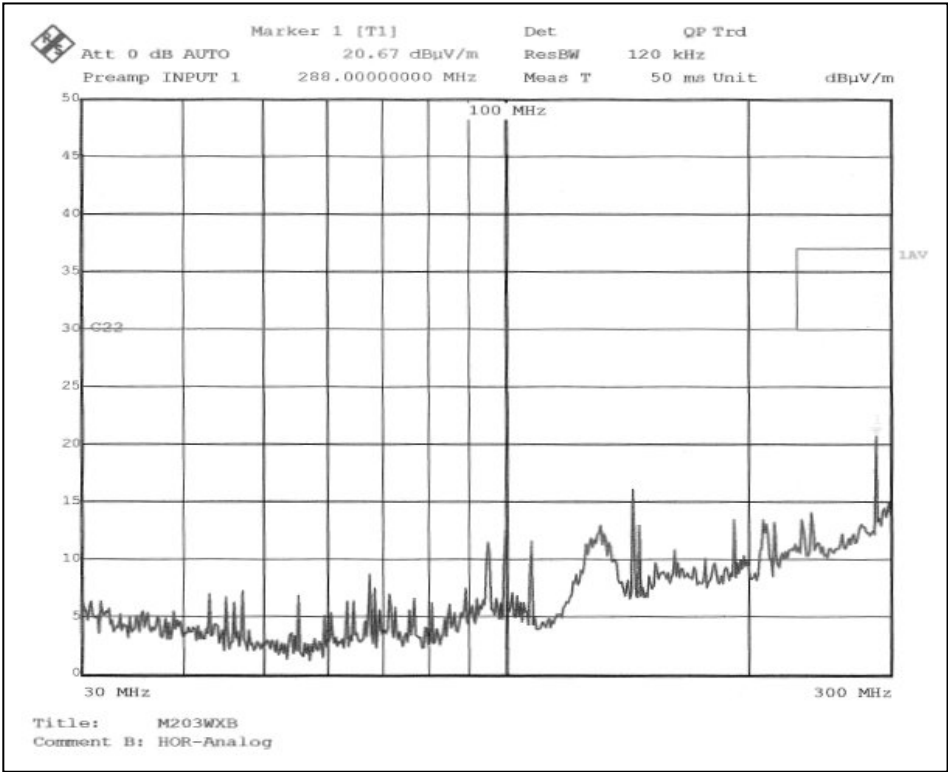
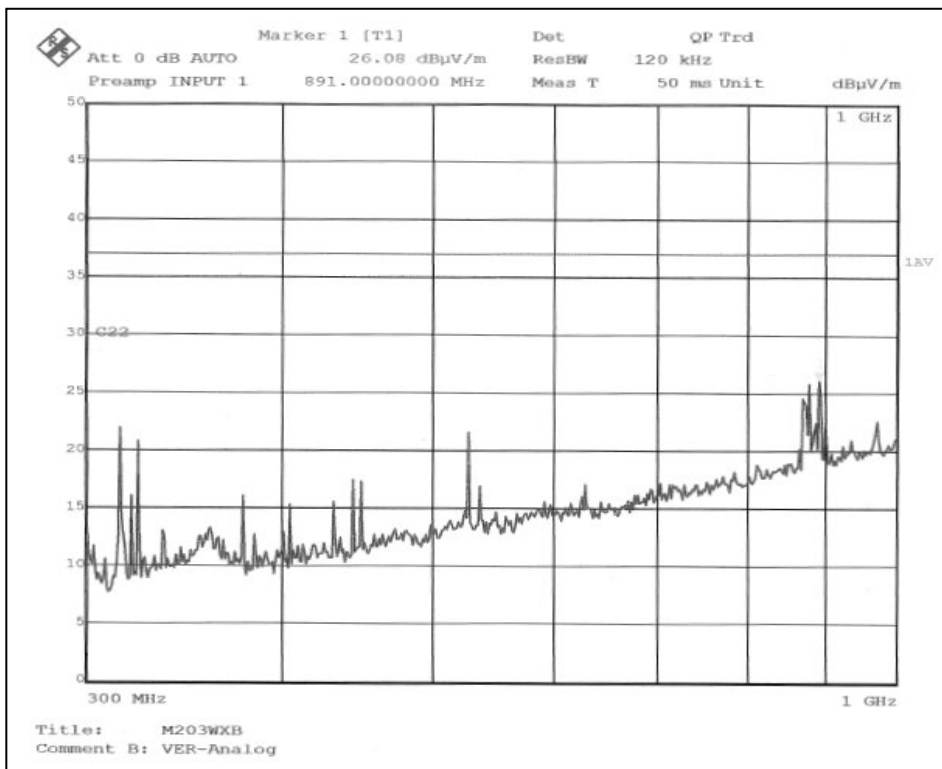
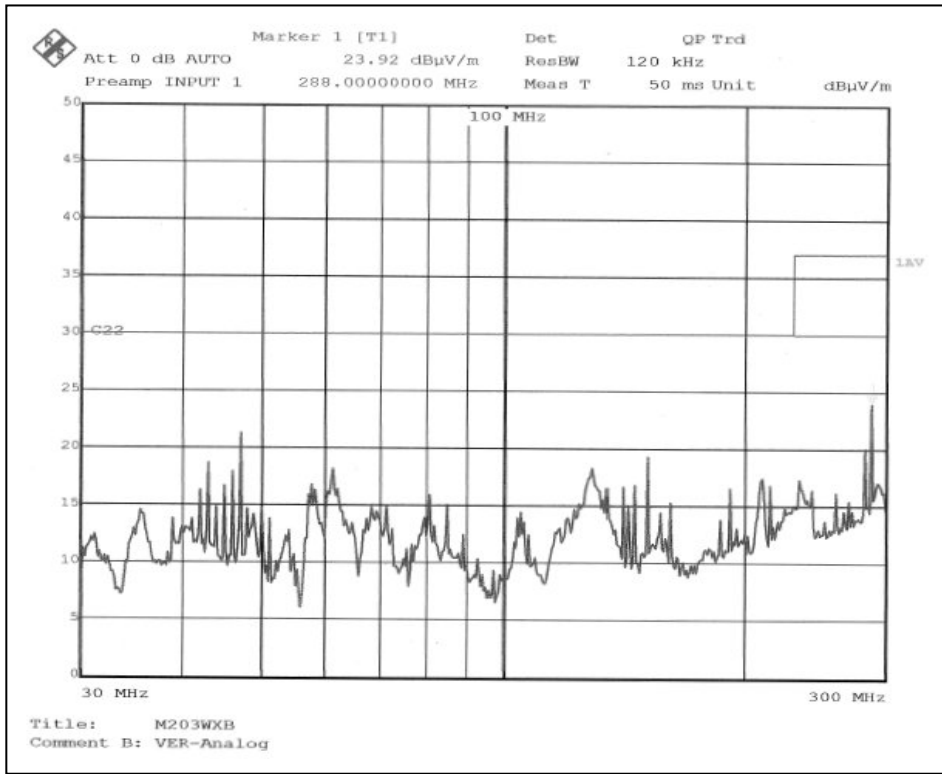


APPENDIX A: TEST GRAPH

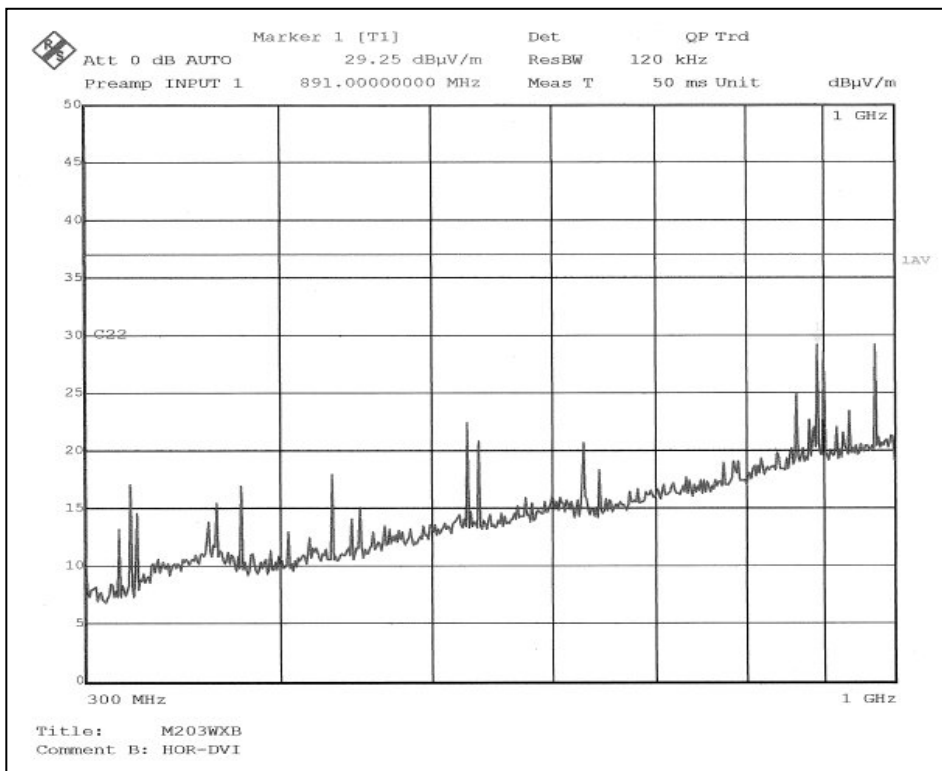
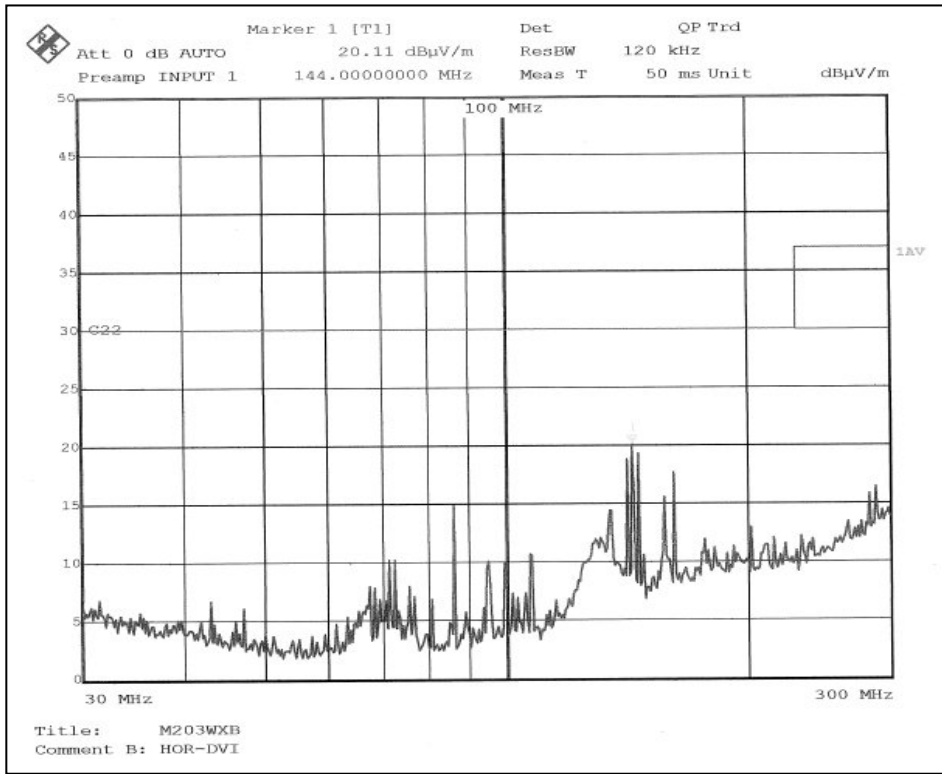
A1.1 Horizontal (D-SUB Input)



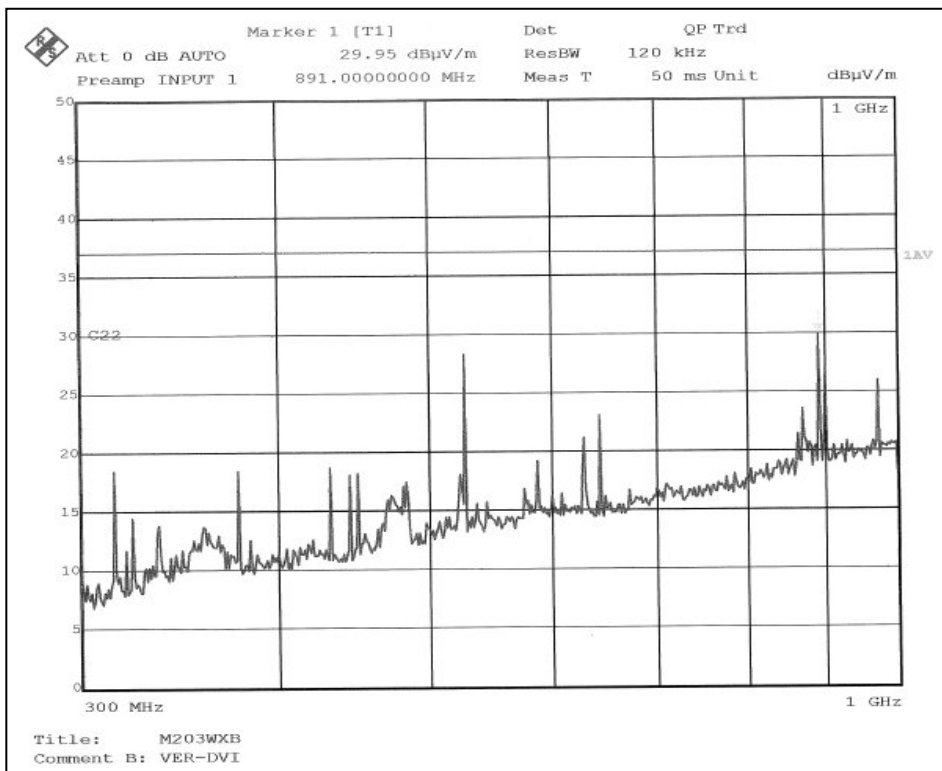
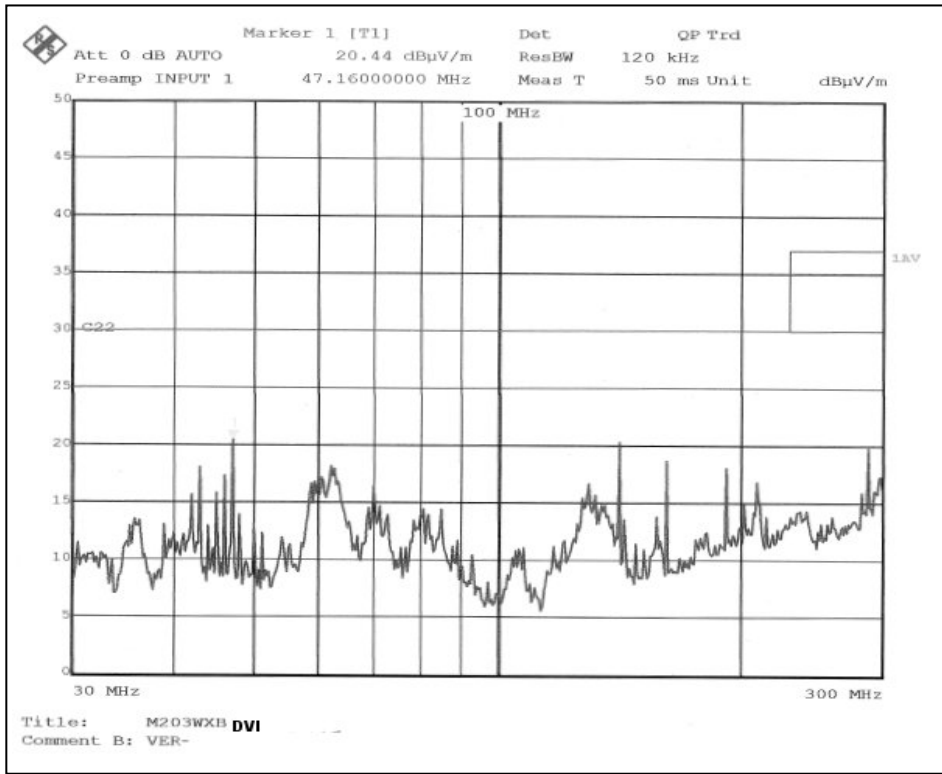
A1.2 Vertical (D-SUB Input)



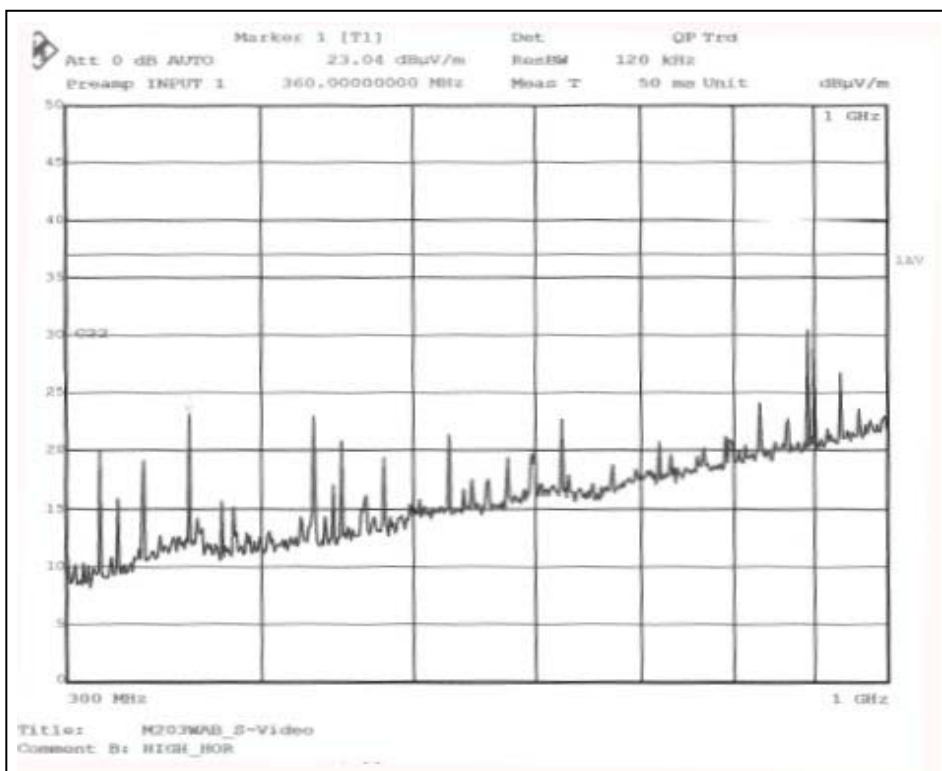
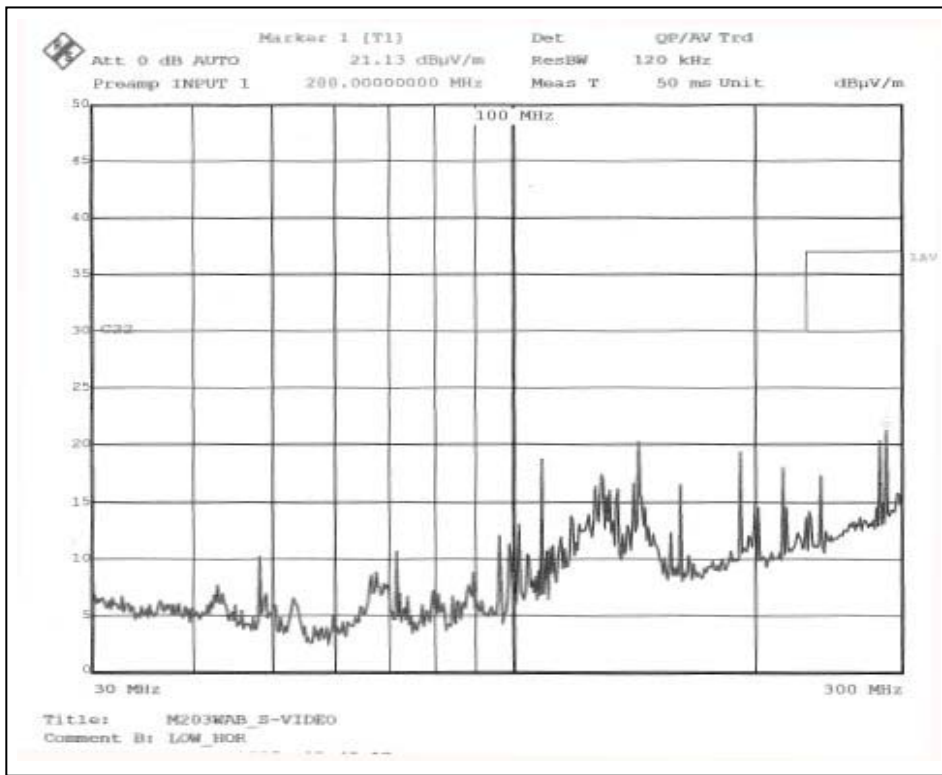
A1.3 Horizontal (DVI Input)



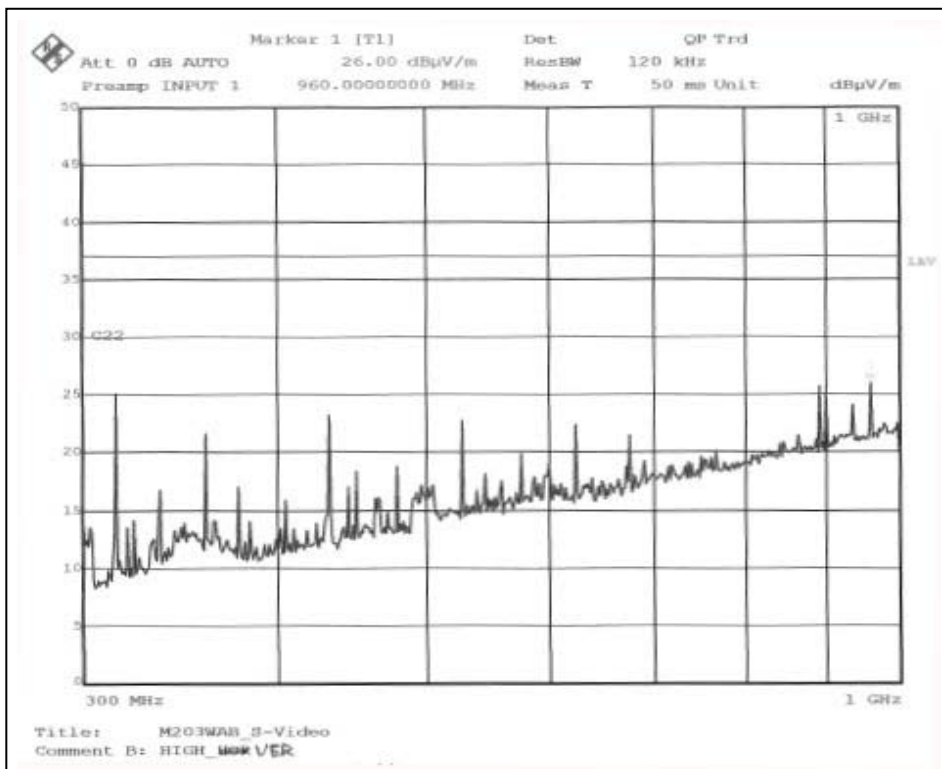
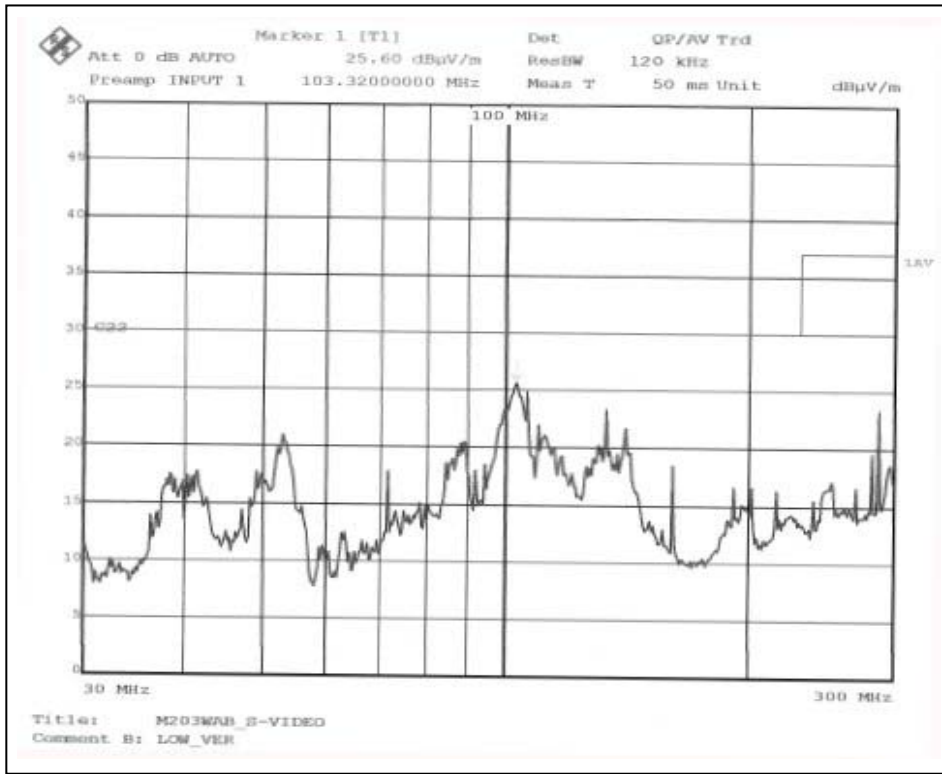
A1.4 Vertical (DVI Input)



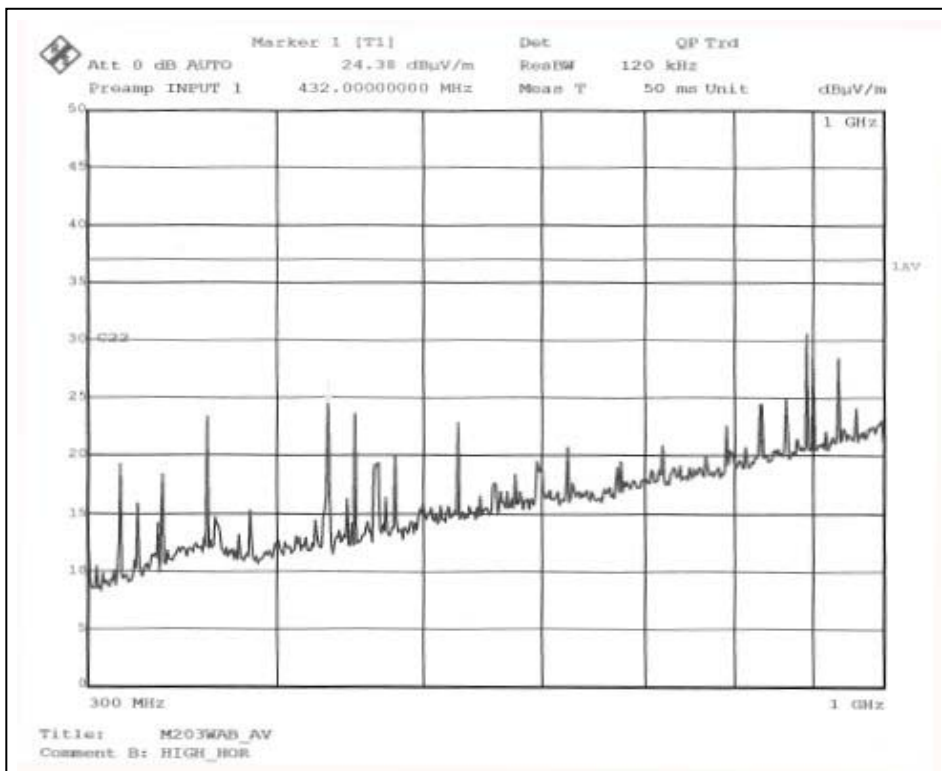
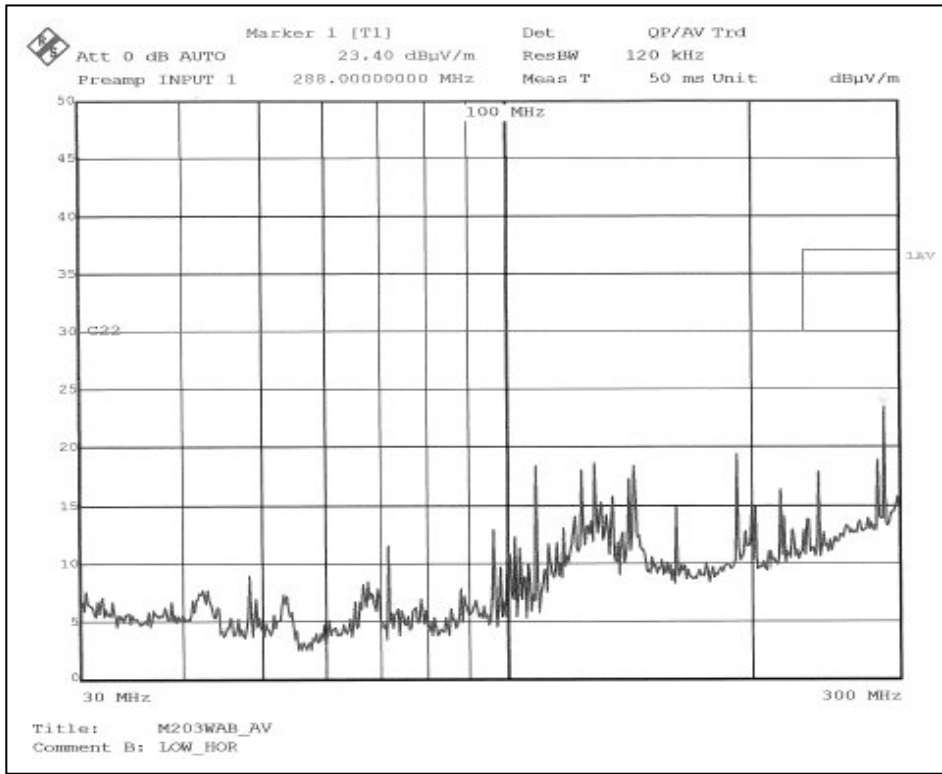
A1.5 Horizontal (S-Video Input)



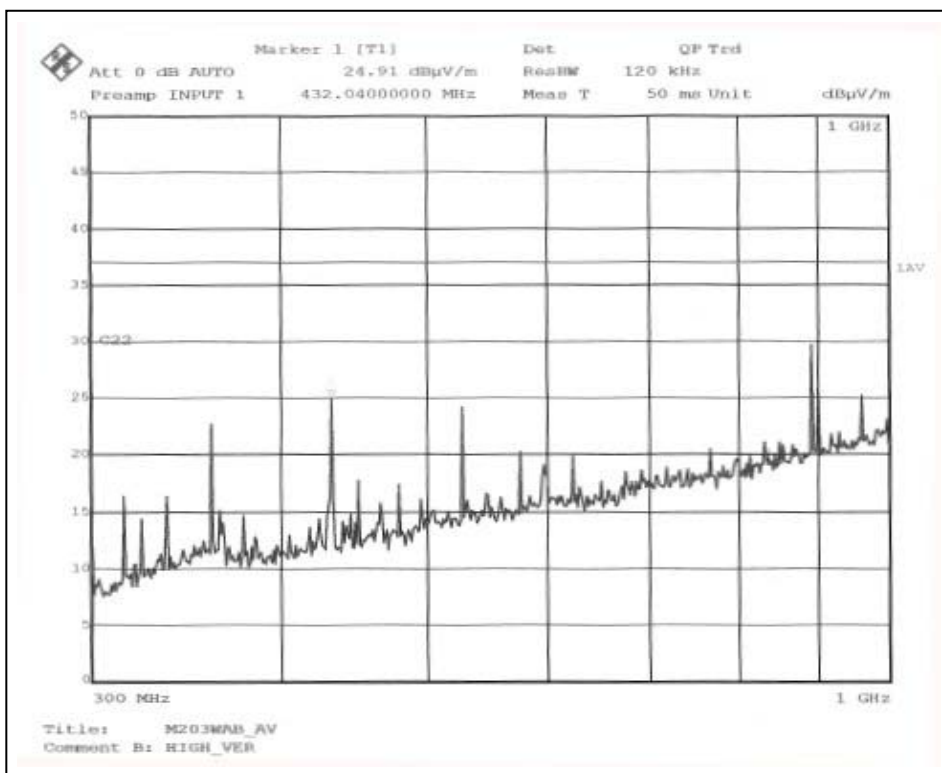
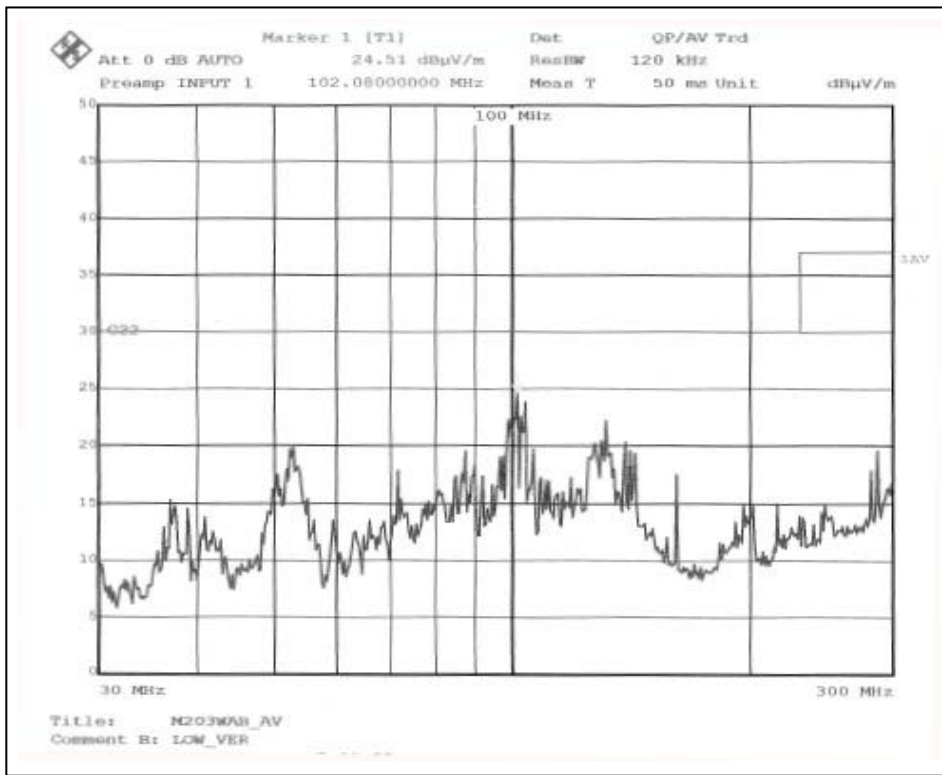
A1.6 Vertical (S-Video Input)



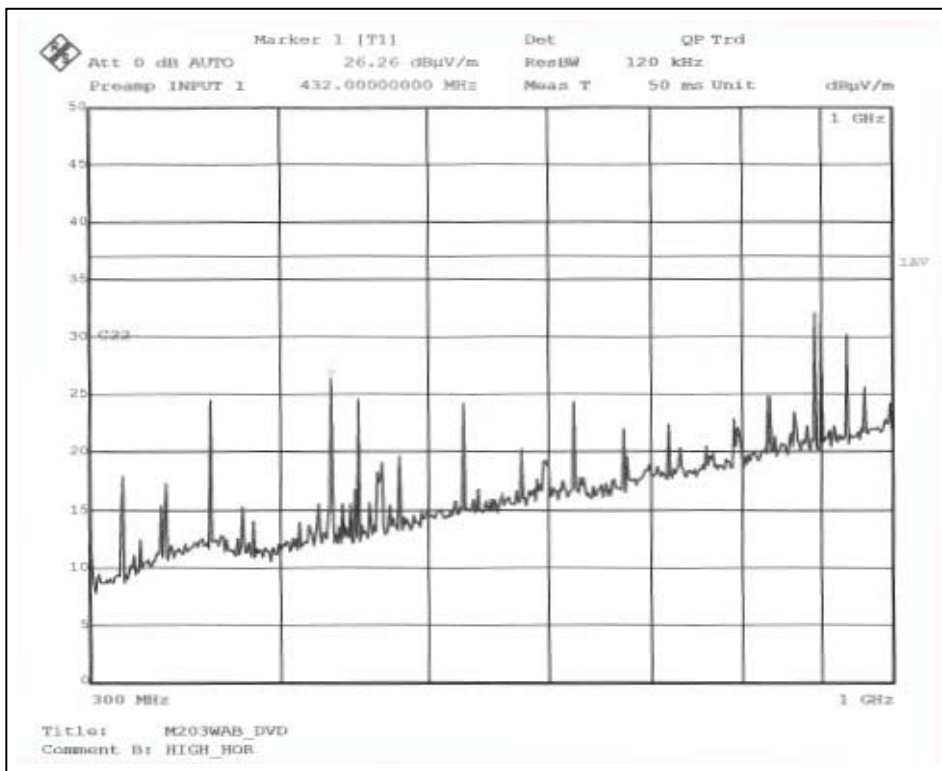
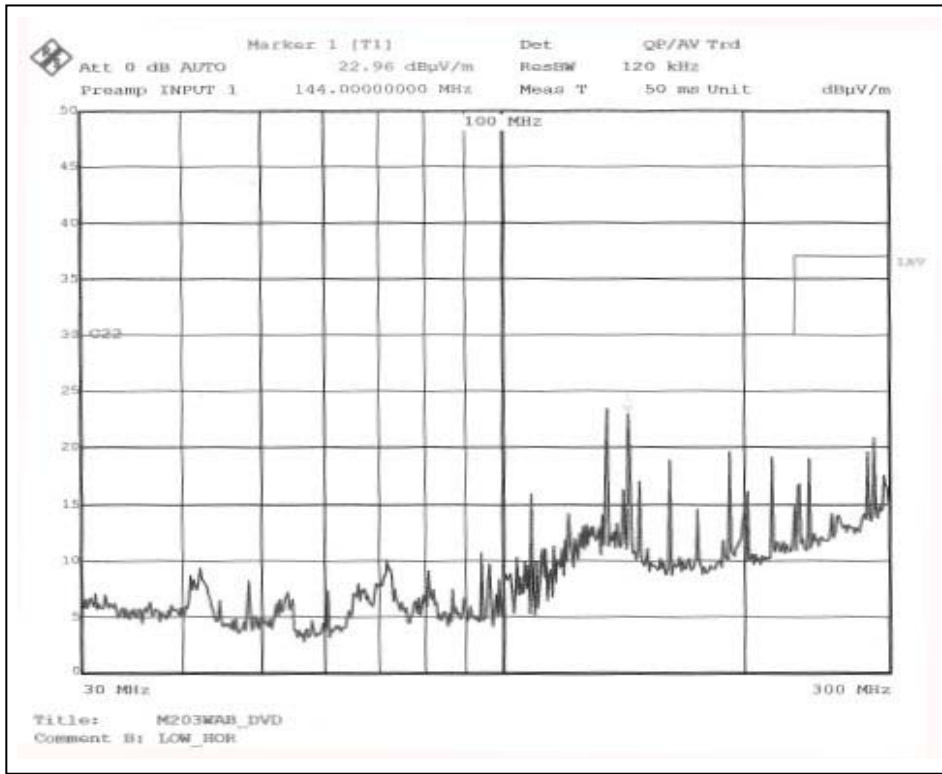
A1.7 Horizontal (A/V Input)



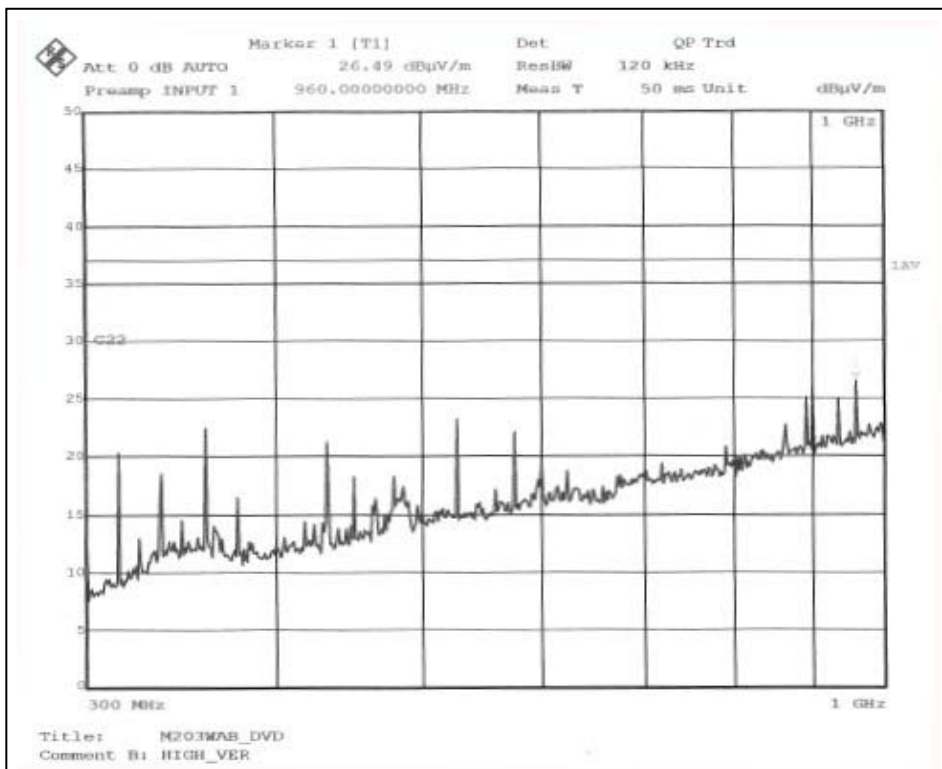
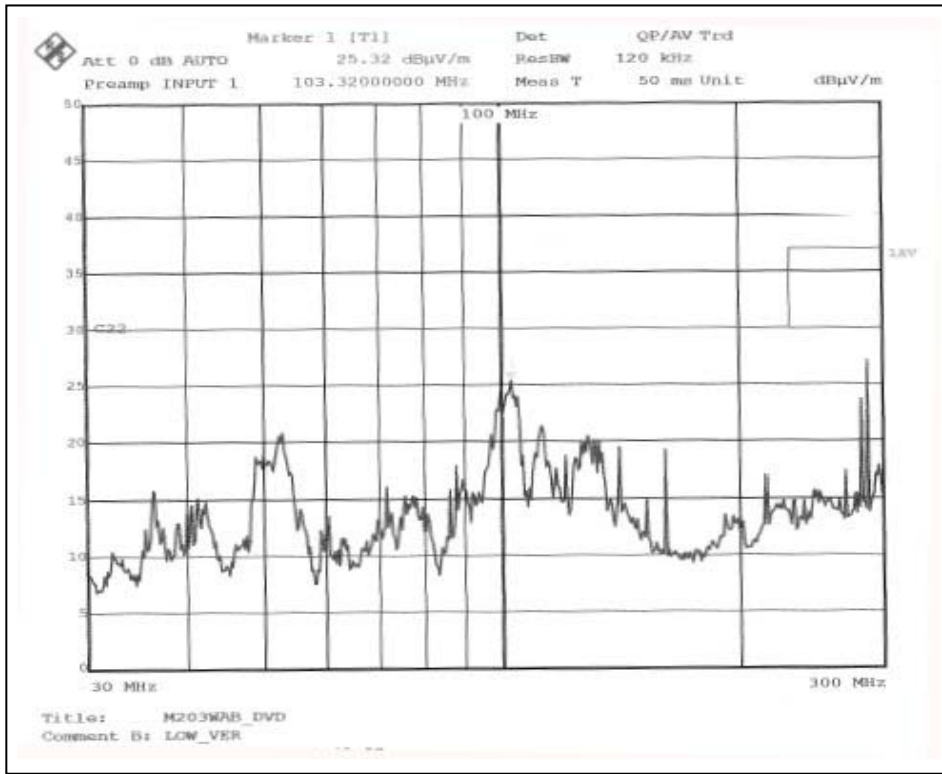
A1.8 Vertical (A/V Input)



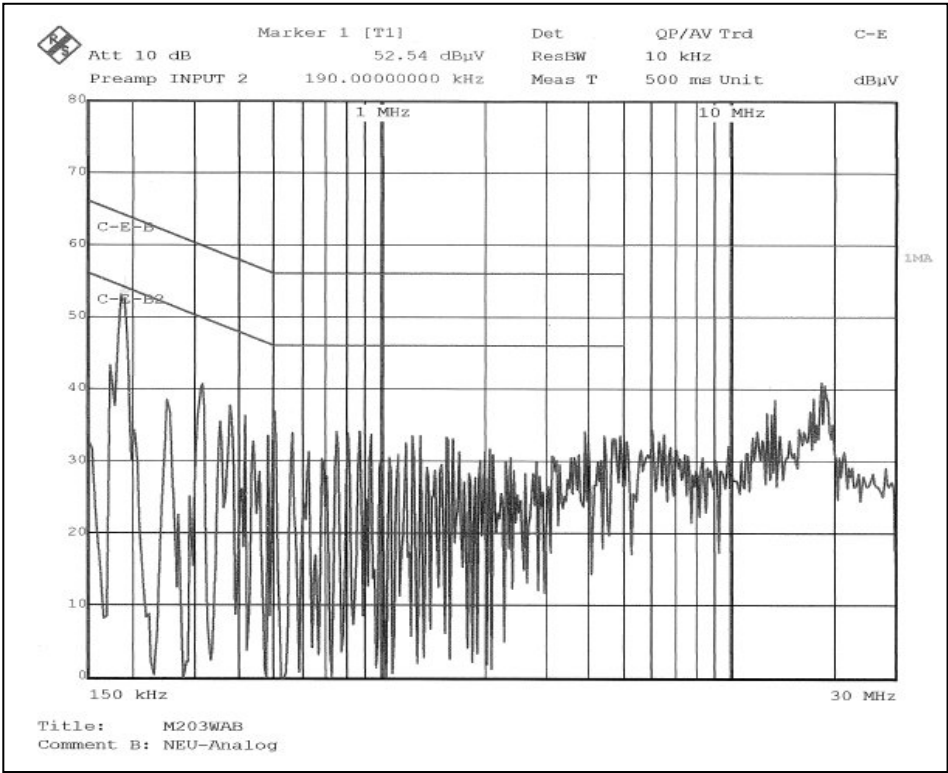
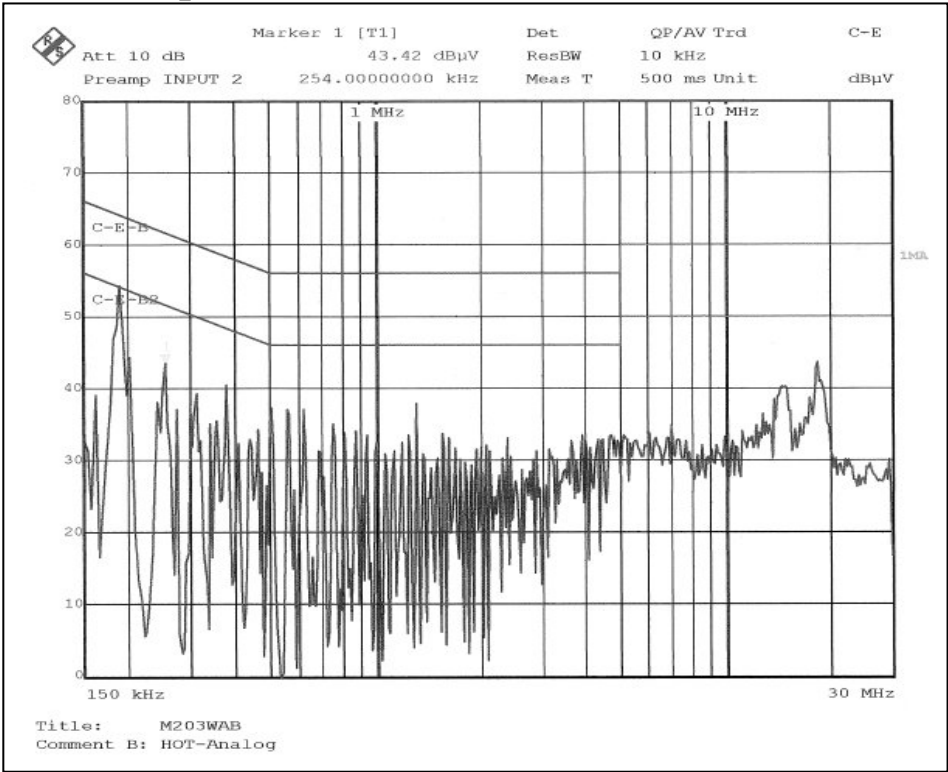
A1.9 Horizontal (Component)



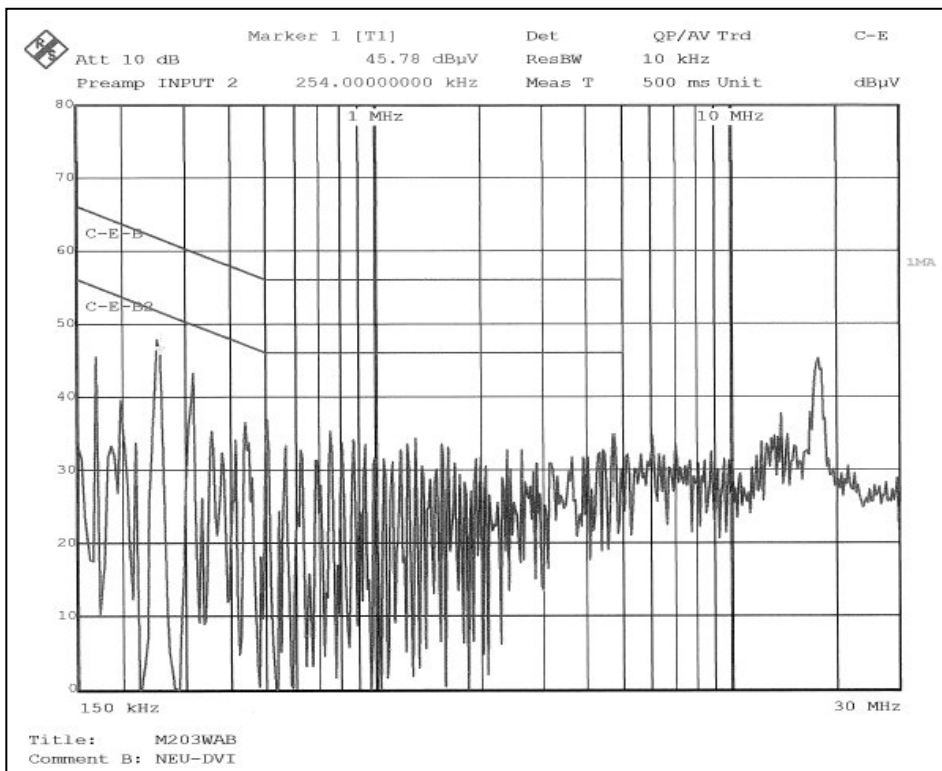
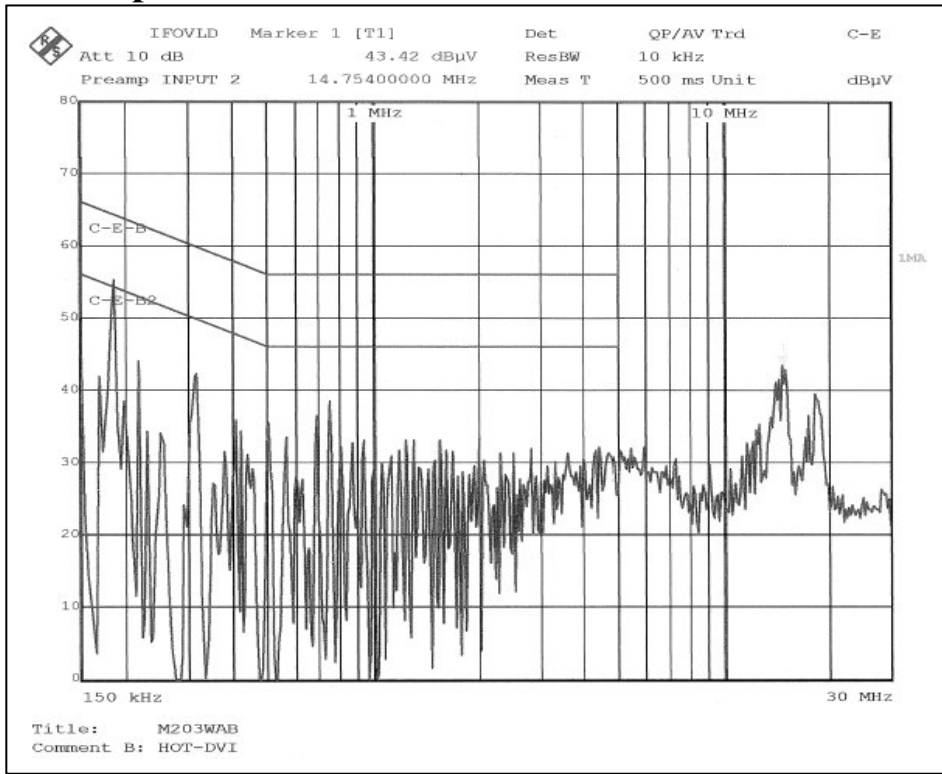
A1.10 Vertical (Component)



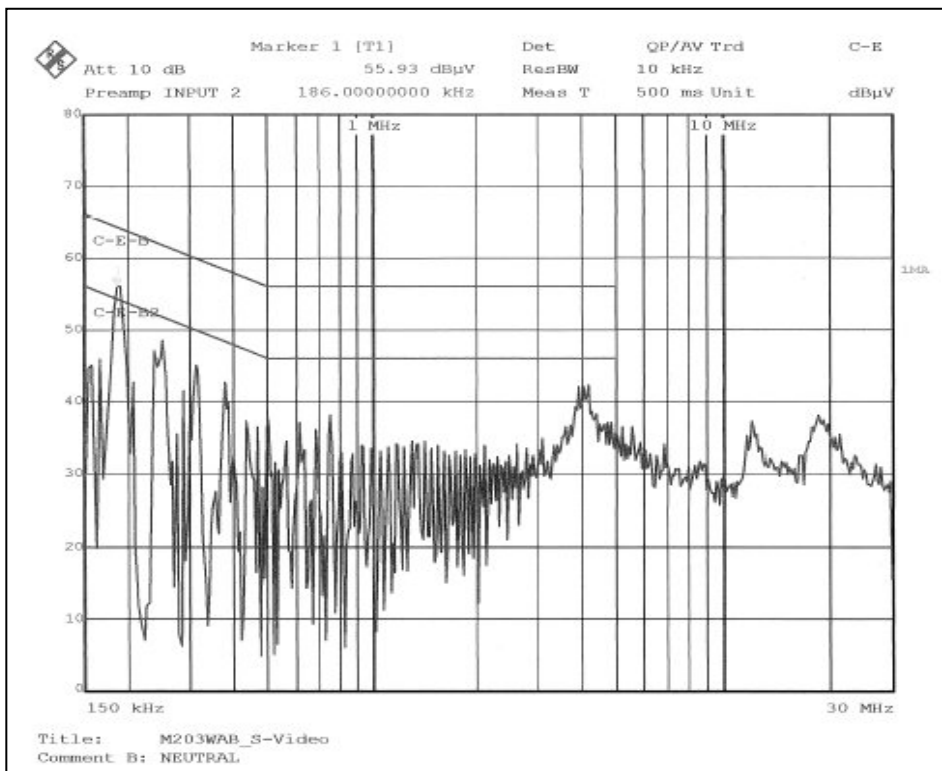
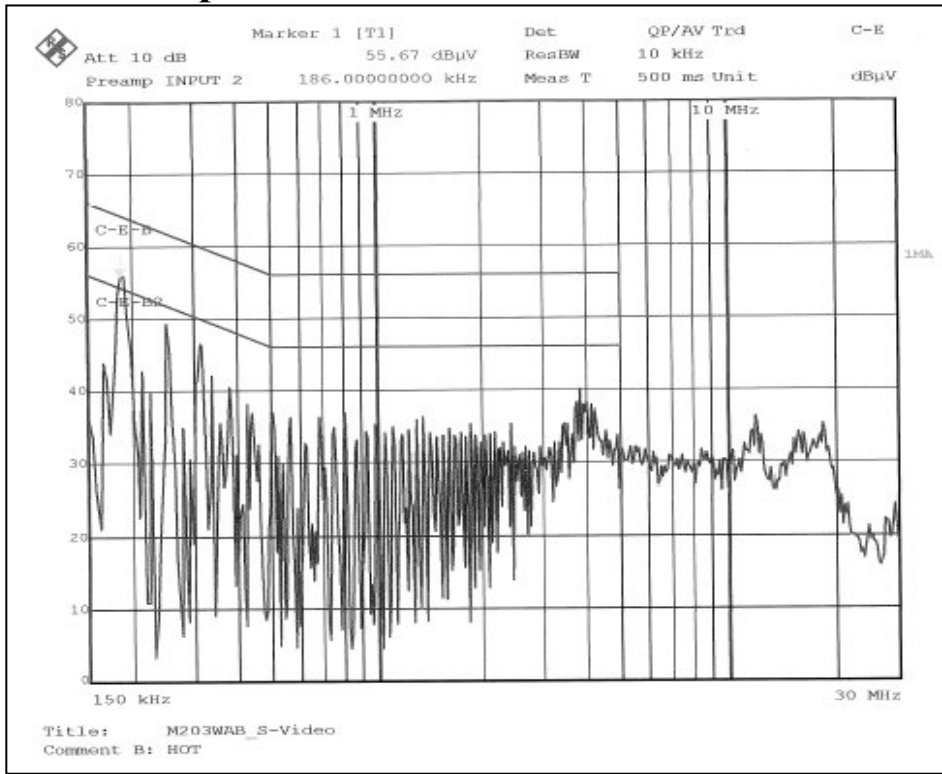
A2.1 D-SUB Input



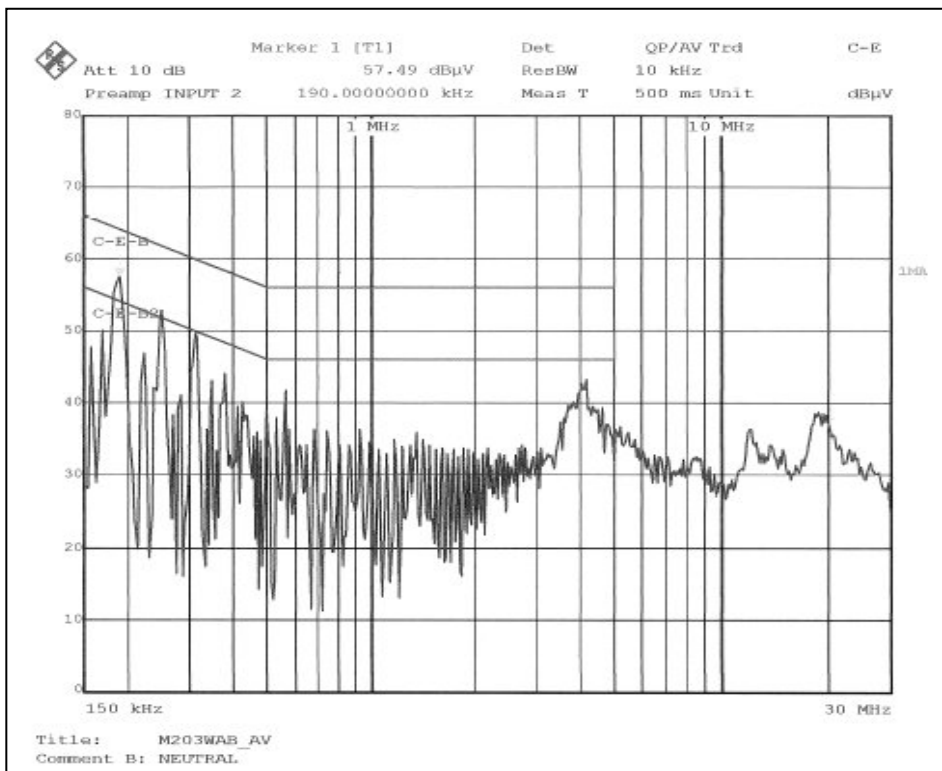
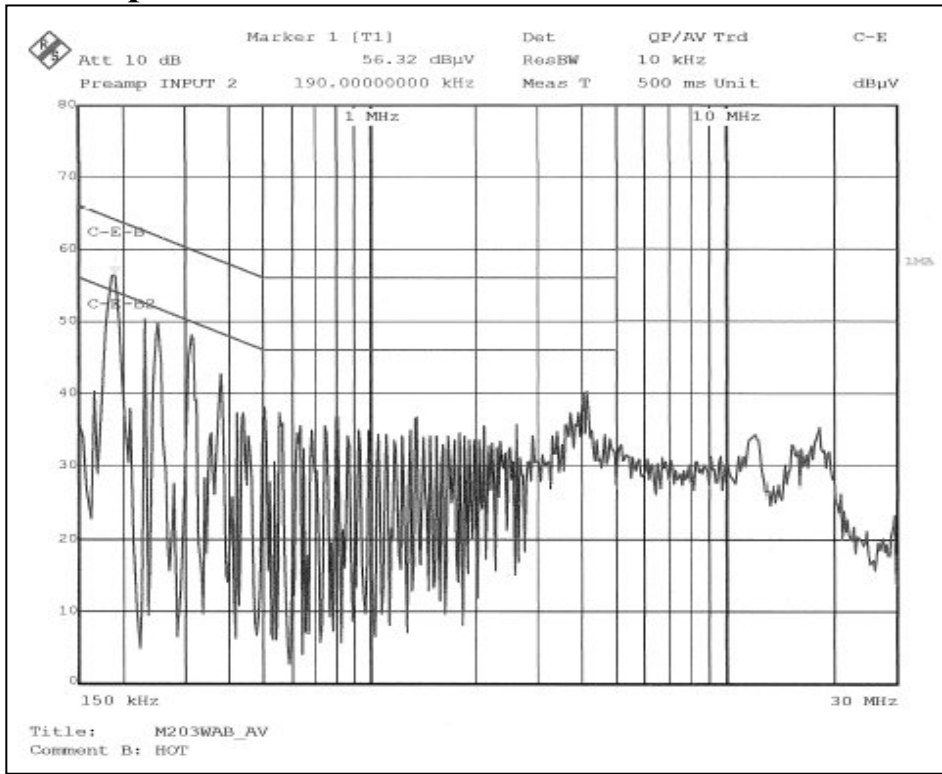
A2.2 DVI Input



A2.3 S-Video Input



A2.4 A/V Input



A2.5 Component Input

