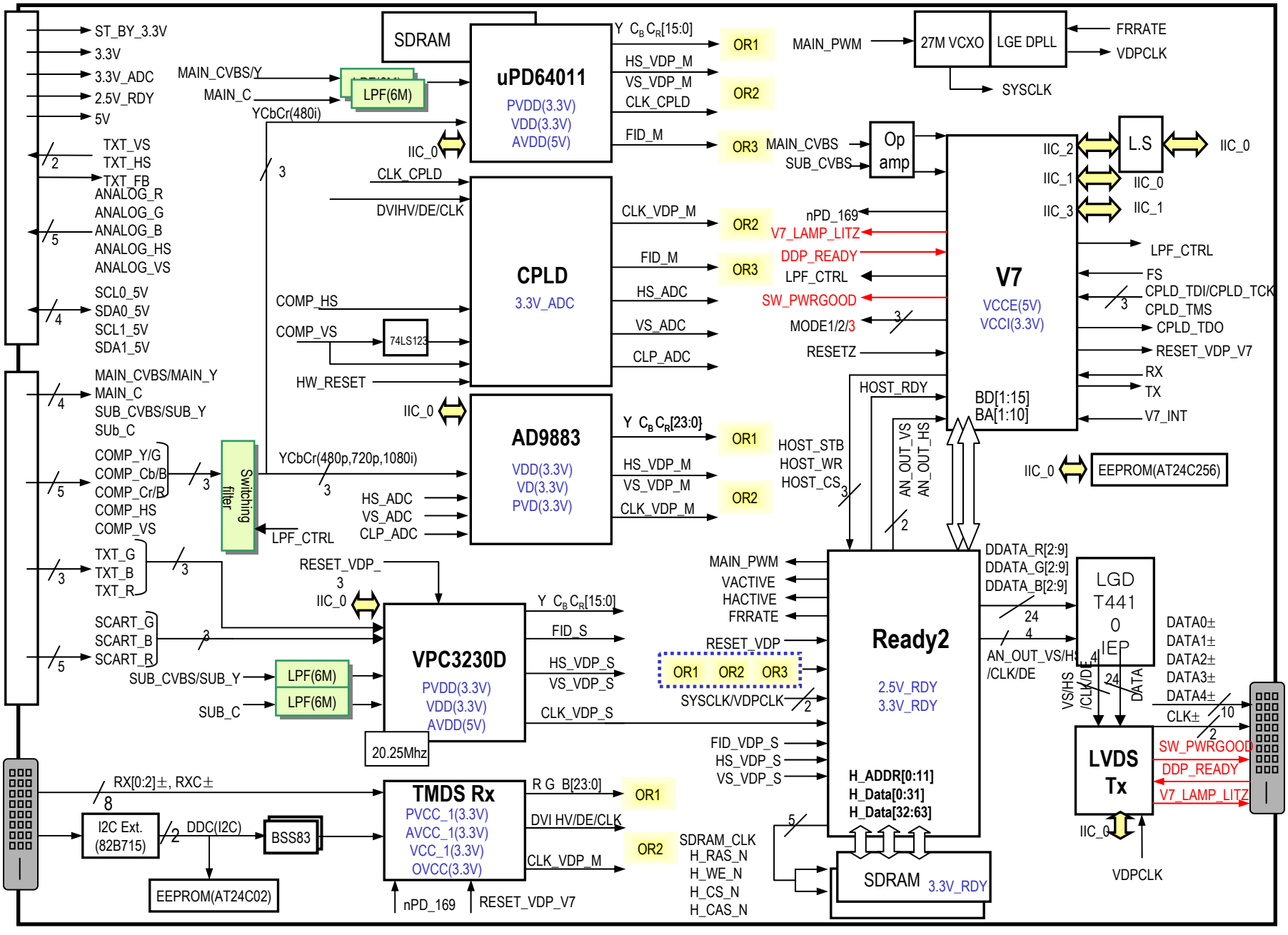
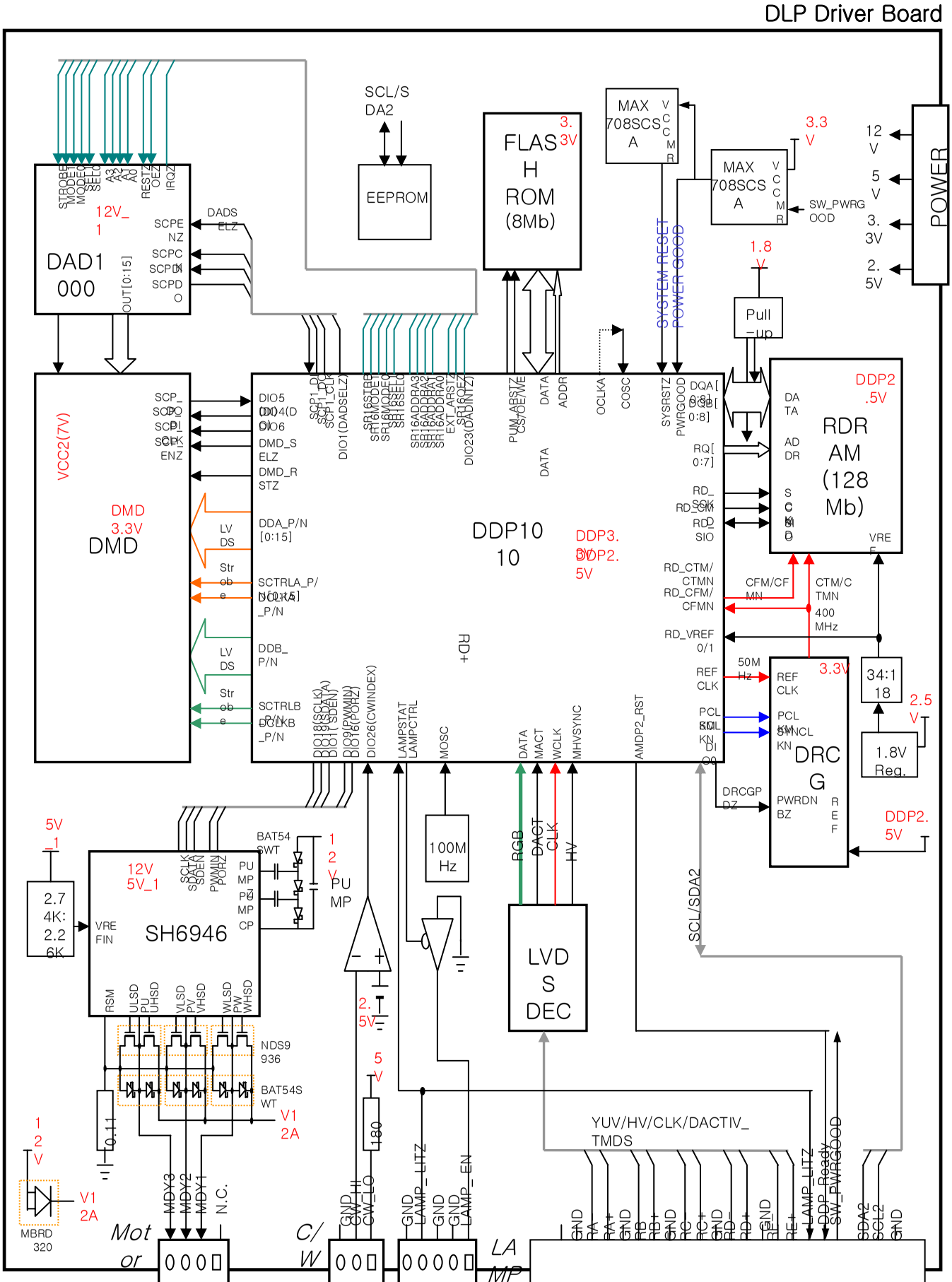

ST_BY_3.3V
3.3V
3.3V_ADC
2.5V_RDY
5V
TXT_VS
TXT_HS
TXT_FB
ANALOG_R
ANALOG_G
ANALOG_B
ANALOG_HS
ANALOG_VS
SCL0_5V
SDA0_5V
SCL1_5V
SDA1_5V
MAIN_CVBS/MAIN_Y
MAIN_C
SUB_CVBS/SUB_Y
SUb_C
COMP_Y/G
COMP_Cb/B
COMP_Cr/R
COMP_HS
COMP_VS
TXT_G
TXT_B
TXT_R
SCART_G
SCART_B
SCART_R
SUB_CVBS/SUB_Y
SUB_C
RX[0:2]±, RXC±
I2C Ext. (82B715)
DDC(I2C)
BSS83
EEPROM(AT24C02)
SDRAM
MAIN_CVBS/Y
MAIN_C
LPF(6M)
YCbCr(480i)
IIC_0
uPD64011
PVDD(3.3V)
VDD(3.3V)
AVDD(5V)
Y C_B C_R[15:0]
HS_VDP_M
VS_VDP_M
CLK_CPLD
FID_M
OR1
OR2
OR3
CLK_CPLD
DVIHV/DE/CLK
CPLD
3.3V_ADC
CLK_VDP_M
FID_M
HS_ADC
VS_ADC
CLP_ADC
COMP_HS
COMP_VS
74LS123
HW_RESET
IIC_0
AD9883
VDD(3.3V)
VD(3.3V)
PVD(3.3V)
Y C_B C_R[23:0]
HS_VDP_M
VS_VDP_M
CLK_VDP_M
YCbCr(480p,720p,1080i)
Switching filter
LPF_CTRL
HS_ADC
VS_ADC
CLP_ADC
RESET_VDP_
IIC_0
VPC3230D
PVDD(3.3V)
VDD(3.3V)
AVDD(5V)
20.25Mhz
Y C_B C_R[15:0]
FID_S
HS_VDP_S
VS_VDP_S
CLK_VDP_S
LPF(6M)
LPF(6M)
TMDS Rx
PVCC_1(3.3V)
AVCC_1(3.3V)
VCC_1(3.3V)
OVCC(3.3V)
R G B[23:0]
DVI HV/DE/CLK
CLK_VDP_M
OR1
OR2
nPD_169
RESET_VDP_V7
MAIN_PWM
27M VCXO
LGE DPLL
FRRATE
VDPCLK
SYSCLK
MAIN_CVBS
SUB_CVBS
Op amp
nPD_169
V7_LAMP_LITZ
DDP_READY
LPF_CTRL
SW_PWRGOOD
MODE1/2/3
RESETZ
V7
VCCE(5V)
VCCI(3.3V)
IIC_2
IIC_1
IIC_3
L.S
IIC_0
IIC_1
LPF_CTRL
FS
CPLD_TDI/CPLD_TCK
CPLD_TMS
CPLD_TDO
RESET_VDP_V7
RX
TX
V7_INT
BD[1:15]
BA[1:10]
HOST_RDY
HOST_STB
HOST_WR
HOST_CS
AN_OUT_VS
AN_OUT_HS
IIC_0
EEPROM(AT24C256)
MAIN_PWM
VACTIVE
HACTIVE
FRRATE
RESET_VDP
OR1 OR2 OR3
SYSCLK/VDPCLK
FID_VDP_S
HS_VDP_S
VS_VDP_S
H_ADDR[0:11]
H_Data[0:31]
H_Data[32:63]
Ready2
2.5V_RDY
3.3V_RDY
SDRAM 3.3V_RDY
SDRAM_CLK
H_RAS_N
H_WE_N
H_CS_N
H_CAS_N
DDATA_R[2:9]
DDATA_G[2:9]
DDATA_B[2:9]
AN_OUT_VS/HS
/CLK/DE
LGD T441 0 IEP
VS/HS
/CLK/DE
DATA
DATA0±
DATA1±
DATA2±
DATA3±
DATA4±
CLK±
LVDS Tx
SW_PWRGOOD
DDP_READY
V7_LAMP_LITZ
IIC_0
VDPCLK


DLP Driver Board
SCL/S
DA2
EEPROM
FLAS
H
ROM
(8Mb)
3.
3V
MAX
708SCS
A
V
C
C
M
R
3.3
V
MAX
708SCS
A
V
C
C
M
R
SW_PWRG
OOD
12
V
5
V
3.
3V
2.
5V
POWER
1.8
V
Pull
−up
SYSTEM RESET
POWER GOOD
DAD1
000
12V_
1
DADS
ELZ
SCPE
NZ
SCPC
SCPD
O
OUT[0:15]
STROBE
MODE
SEL0
A3
A2
A1
A0
RESTZ
OEZ
IRQZ
VCC2(7V)
DMD
3.3V
DMD
DMD_S
ELZ
DMD_R
STZ
DDA_P/N
[0:15]
DDB_
P/N
SCTRLA_P/
_P/N
SCTRLB
_P/N
DDP10
10
DDP3.
3V
DDP2.
5V
DIO5
DIO6
DIO1(DADSELZ)
DIO23(DADINTZ)
PUM_ABSTZ
CS/OE/WE
DATA
ADDR
OCLKA
COSC
SYSRSTZ
PWRGOOD
DQA[
0:8]
DQB[
0:8]
RQ[
0:7]
RD_
SCK
RD_SM
RD_
SIO
DDP2
.5V
RDR
AM
(128
Mb)
VRE
F
RD_CTM/
CTMN
RD_CFM/
CFMN
CFM/CF
MN
CTM/C
TMN
400
MHz
RD_VREF
0/1
50M
Hz
REF
CLK
PCL
SYNCL
KN
DRC
G
PWRDN
BZ
DRCGP
DZ
3.3V
34:1
18
1.8V
Reg.
2.5
V
DDP2.
5V
SCL/SDA2
RD+
DIO18(SCLK)
DIO19(SDATA)
DIO17(SDEN)
DIO16(PORZ)
DIO9(PWMIN)
DIO26(CWINDEX)
LAMPSTAT
LAMPCTRL
MOSC
DATA
MACT
WCLK
MHVSYNC
AMDP2_RST
5V
_1
2.7
4K:
2.2
6K
12V
5V_1
SH6946
VRE
FIN
SCLK
SDA
SDEN
PWMIN
PORZ
PU
MP
CP
BAT54
SWT
1
2
V
PU
MP
RSM
ULSD
PU
UHSD
VLSD
PV
VHSD
WLSD
PW
WHSD
NDS9
936
BAT54S
WT
V1
2A
0.11
100M
Hz
RGB
DACT
CLK
HV
LVD
S
DEC
2.
5V
5
V
180
1
2
V
V1
2A
MBRD
320
MDY3
MDY2
MDY1
N.C.
Mot
or
GND
CW_HI
CW_LO
C/
W
GND
LAMP_LITZ
GND
GND
LAMP_EN
LA
MP
YUV/HV/CLK/DACTIV_
TMDS
GND
RA-
RA+
GND
RB-
RB+
GND
RC-
RC+
GND
RD-
RD+
GND
RE-
RE+
LAMP_LITZ
DDP_Ready
SW_PWRGOOD
SDA2
SCL2
GND