

WIT-300H using Agere's VoWLAN chipset consist of T8307 handheld VoIP processor, CSP8307C1 analog conversion and power management IC, WL600114 media access controller(MAC), WL1411 radio module and intel MCP(32M byte Flash X 8M byte PSRAM), FPGA, MIDI IC(YMU765), LCD module.

In details, T8307 IC is dual-core device including ARM946E-S processor and DSP16000, CSP8307C1 is IC of audio CODEC and Power Management function. CSP8307C1 transmit and receive the audio data using databus and communicate to ARM946E-S with various control signal, serial interface and support 5X6 Key Pad matrix and uses external Boot ROM.

Lattice's FPGA interpret T8307 IC with MAC IC, LCD module, MIDI IC. In other words, FPGA shift voltage level up and down between 1.8V and 3.0V.

