

FIGURE 2a

**Photograph(s) for Fundamental, Spurious and Digital Devices Emissions
Dipole Configuration**

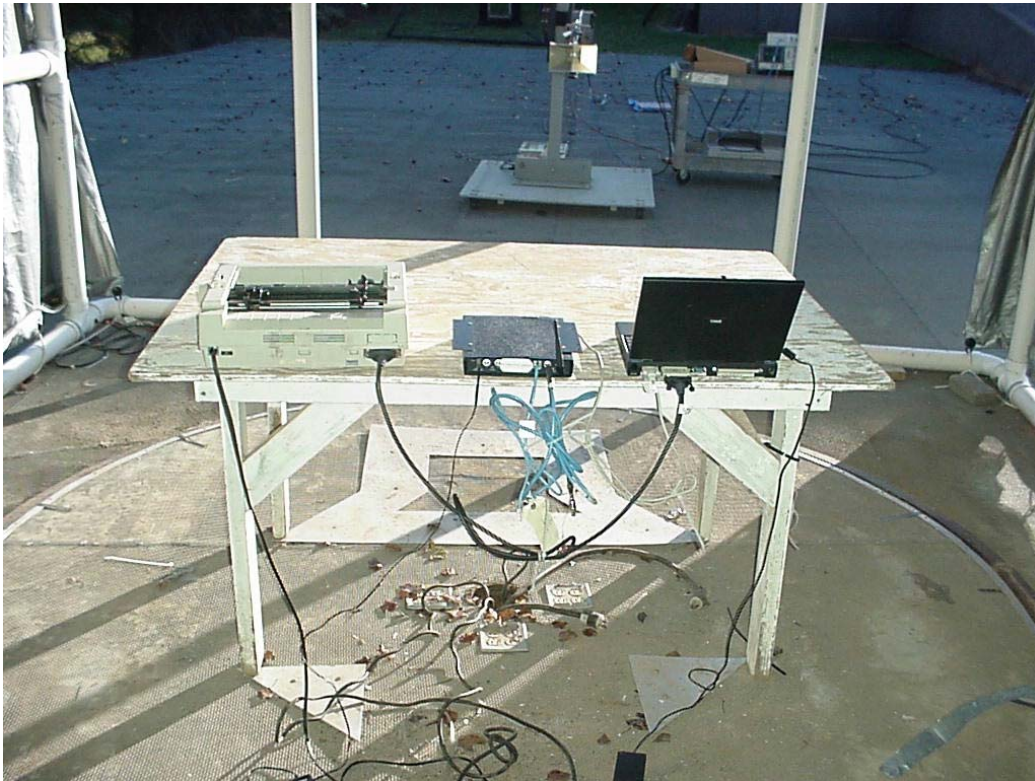


FIGURE 2b

**Photograph(s) for Fundamental, Spurious and Digital Devices Emissions
Patch Configuration**

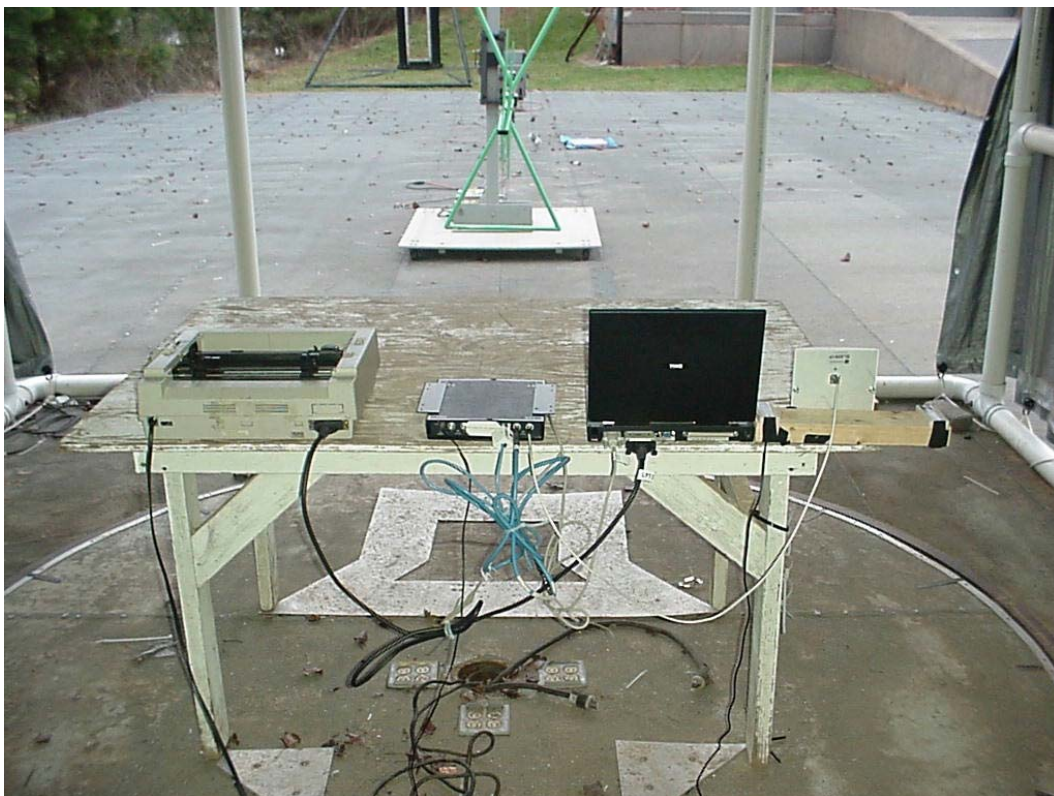


FIGURE 2c

Photograph(s) for Conducted Emissions

