

APPENDIX E: TEST SEQUENCES

1. Test sequence is generated based on below parameters of the DUT:

- Measured maximum power (P_{max})
- Measured Tx_power_at_SAR_design_target (P_{limit})
- SAR_time_window (30s for FCC)

2. Test Sequence 1 Waveform:

Since WLAN radios do not have closed loop power control, average Tx power level of WLAN radios is indirectly varied by transmitting at varying duty cycles (i.e., varying UL data rates). Test sequence #1 described previously can be converted into duty cycle at Pmax, i.e., duty cycle for an arbitrary Tx power level = (Tx power level / Pmax).

Table E-1 Test Sequence 1 for WLAN radio

Time duration (seconds)	Duty Cycle (%)
30	100%
60	50%

NOTE: Test sequence #2 is not achievable due to current test capability. Therefore, in the interim, it is exempt.

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DUT Type: Wireless Module	This report is Blockchain Verified. To verify the report authenticity, please visit HTTPS://WWW.BLOCKCHAIN-VERIFIED.COM or by clicking on the Blockchain logo. 	APPENDIX E: Page 1 of 1