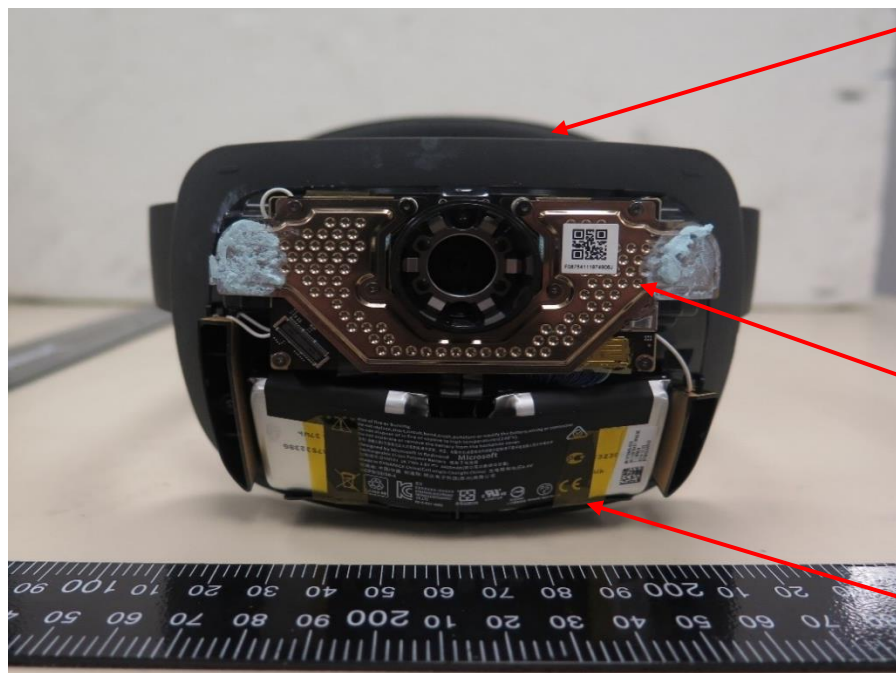




Top edge

Main Logic Board
with Wi-Fi/BT RF
section - shielded

Bottom edge

Figure 1 EUT back cover off

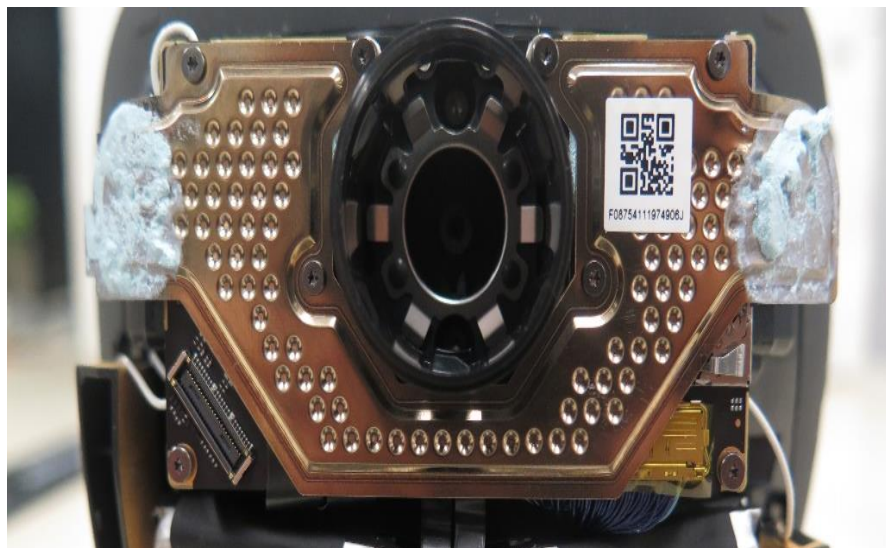


Figure 2 Close up Main Logic Board section

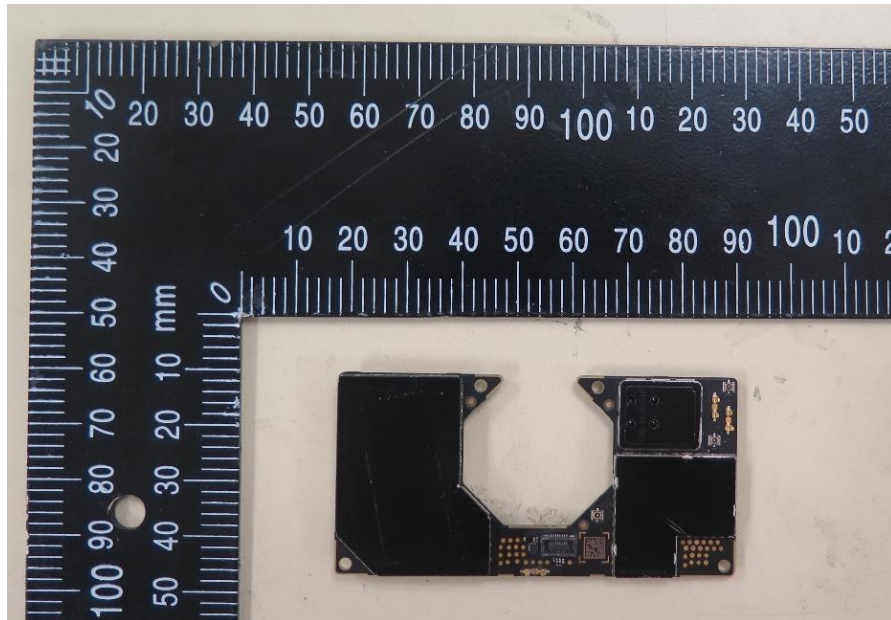
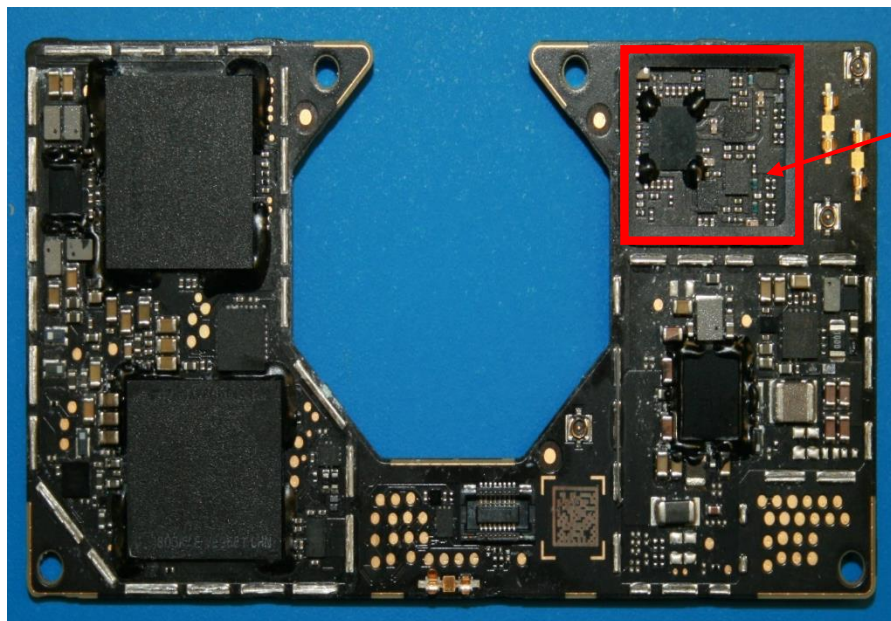


Figure 3 Close up Main Logic Board -Top View



**Wi-Fi/BT
RF section**

Figure 4 Close up Wi-Fi RF section without shield -Top View

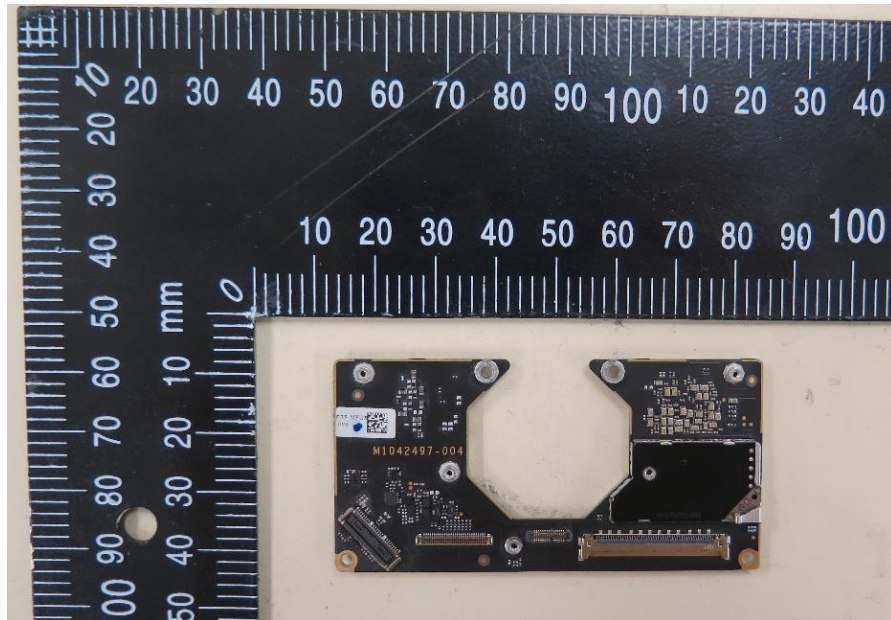


Figure 5 Close up Wi-Fi RF section -Bottom View

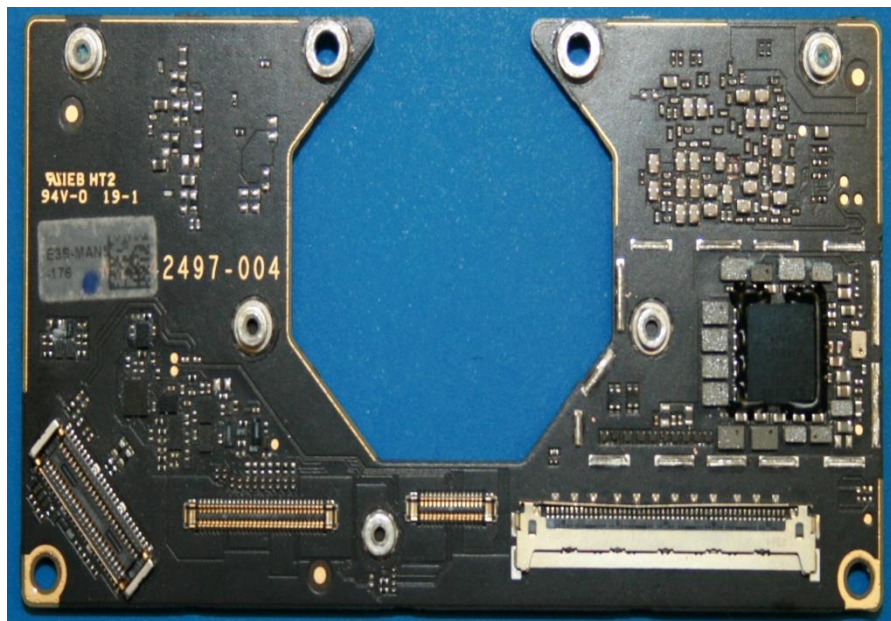


Figure 6 Close up Wi-Fi RF section -Bottom View