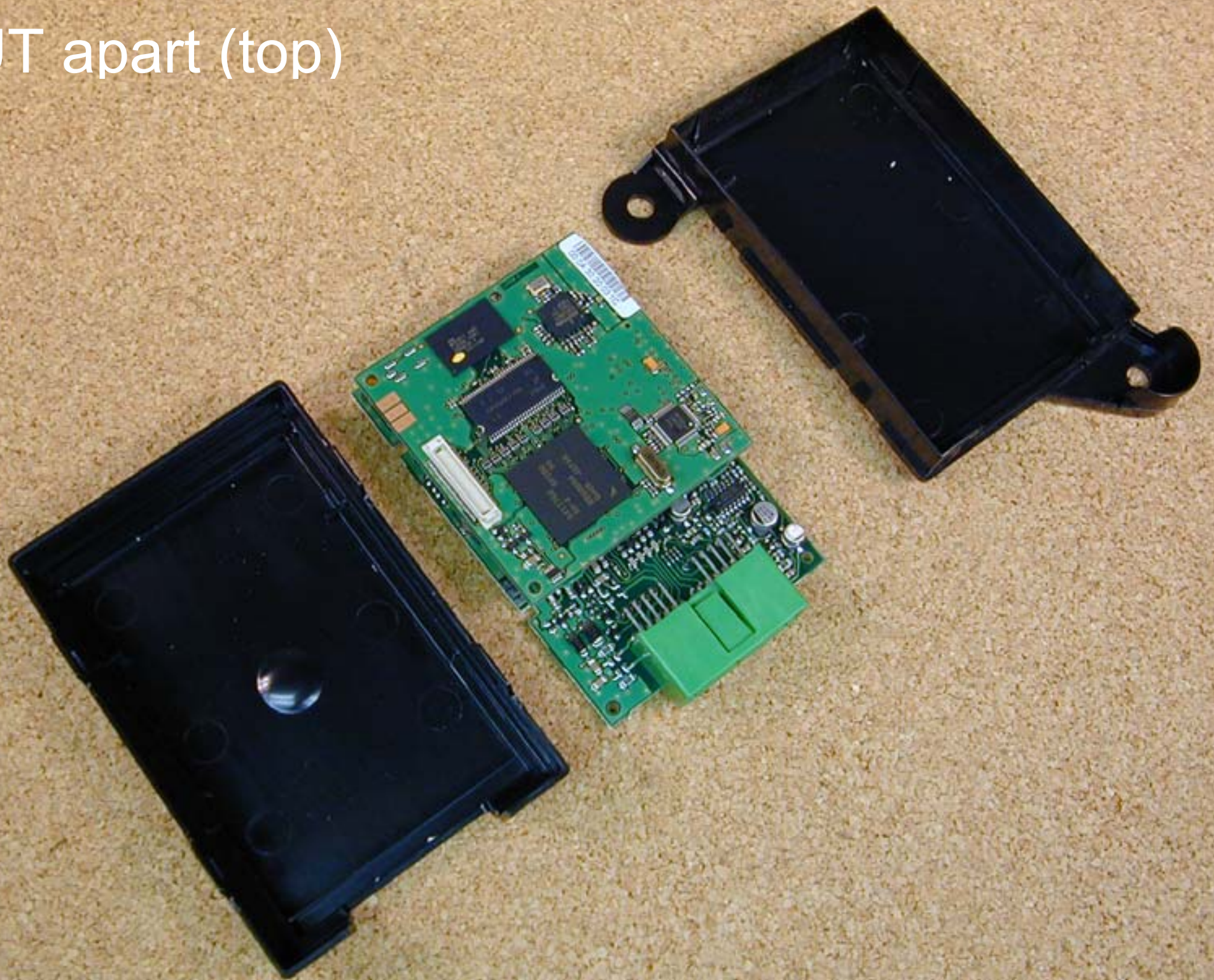
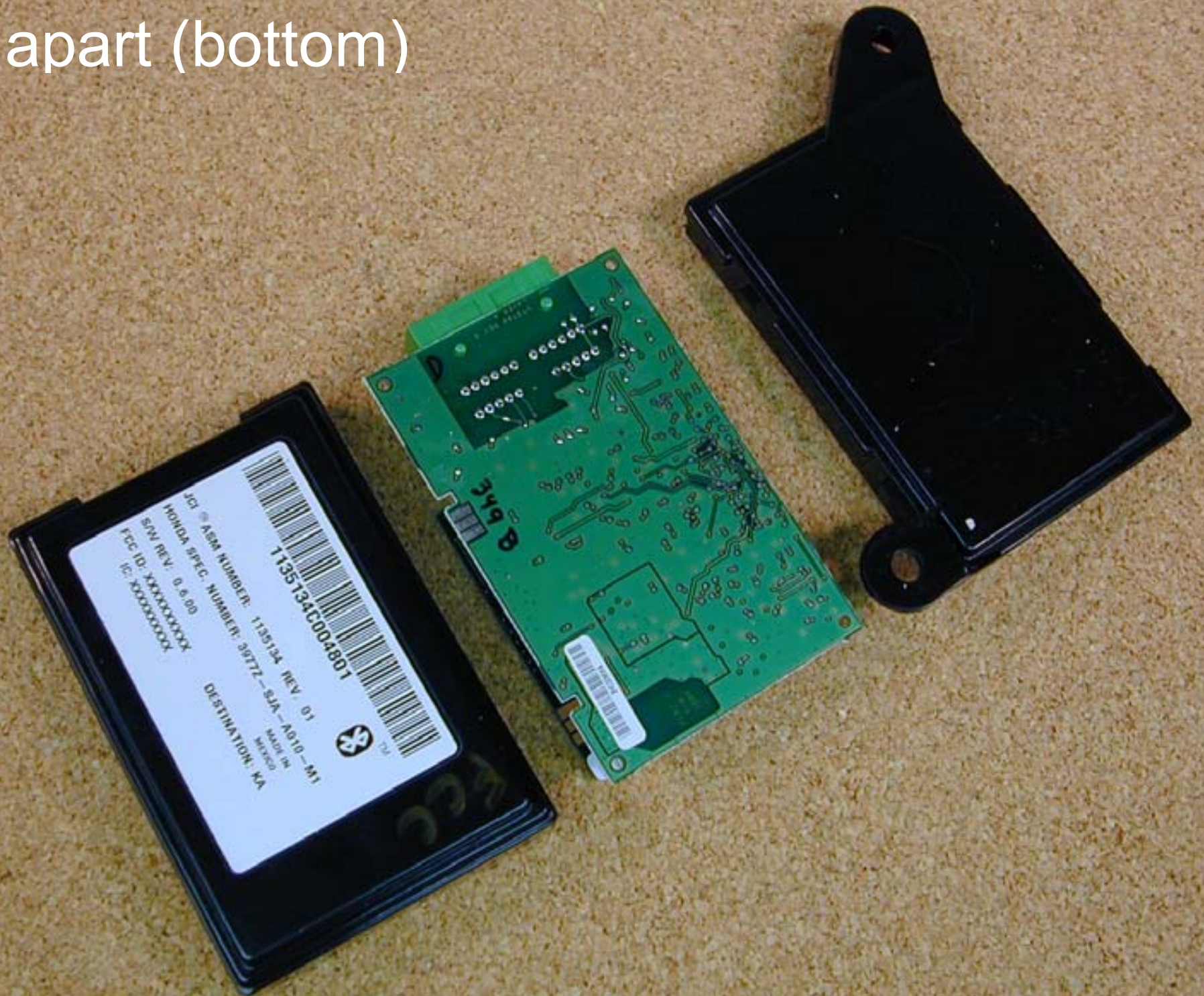


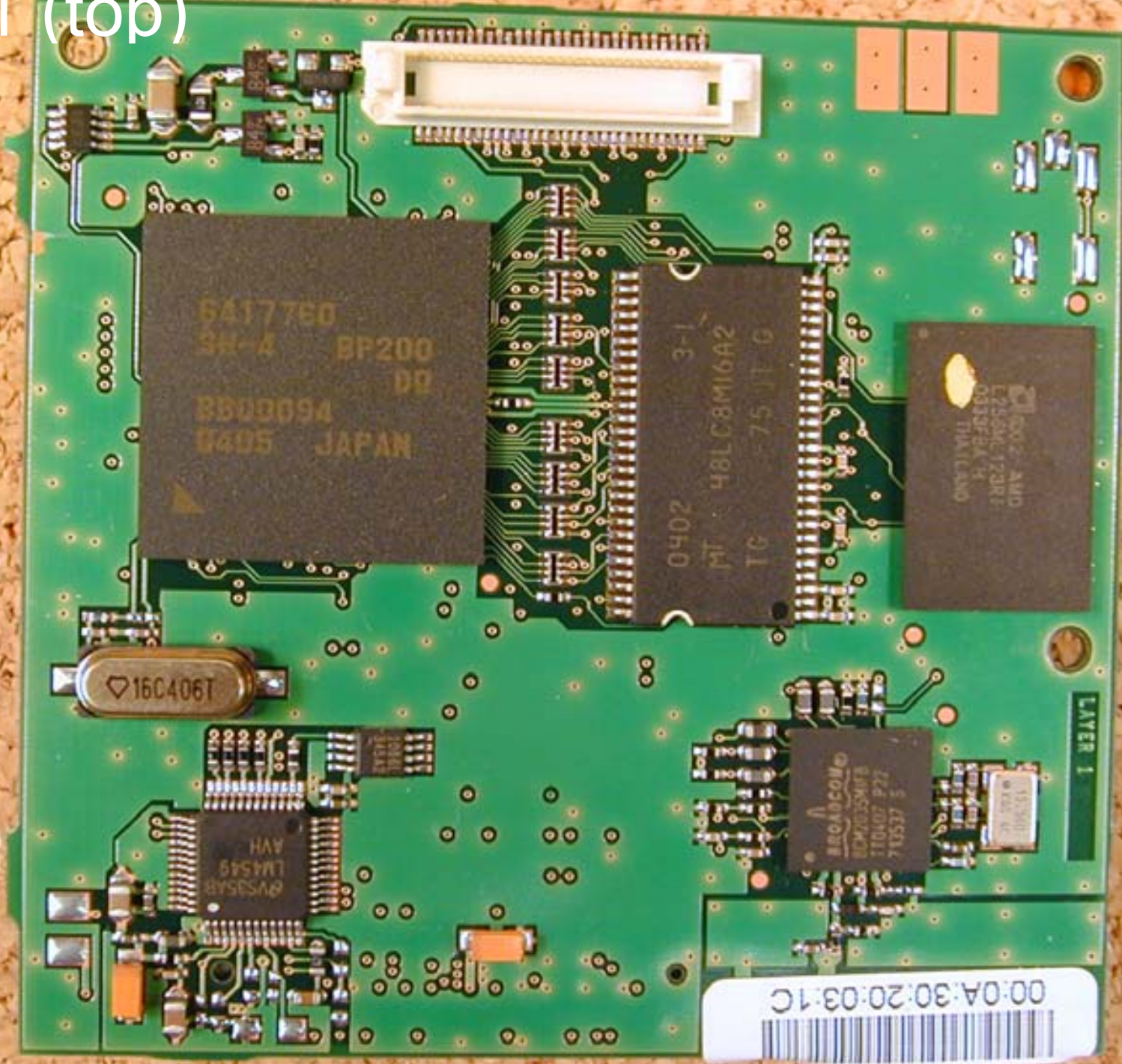
DUT apart (top)



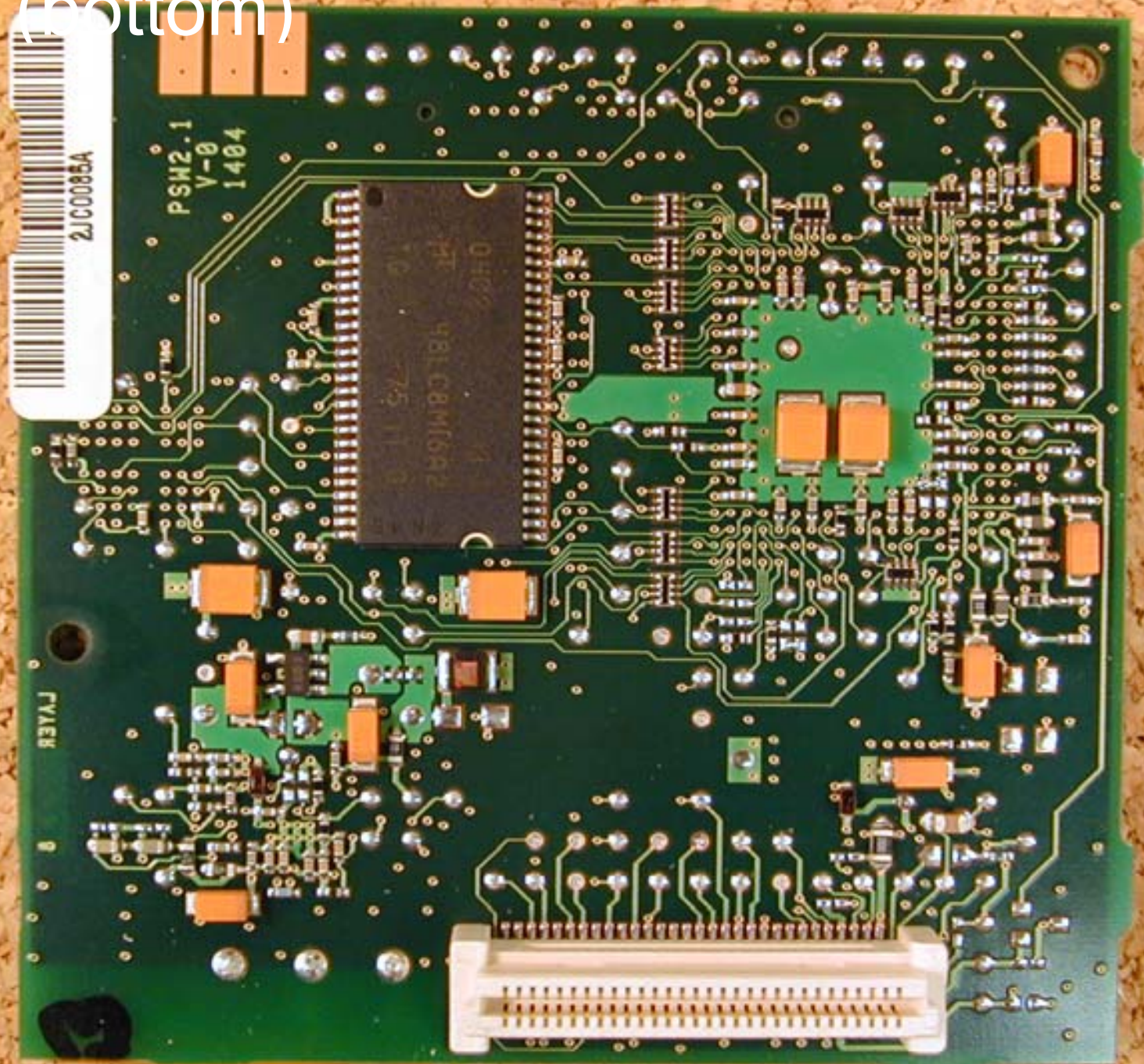
DUT apart (bottom)



PCB 1 (top)



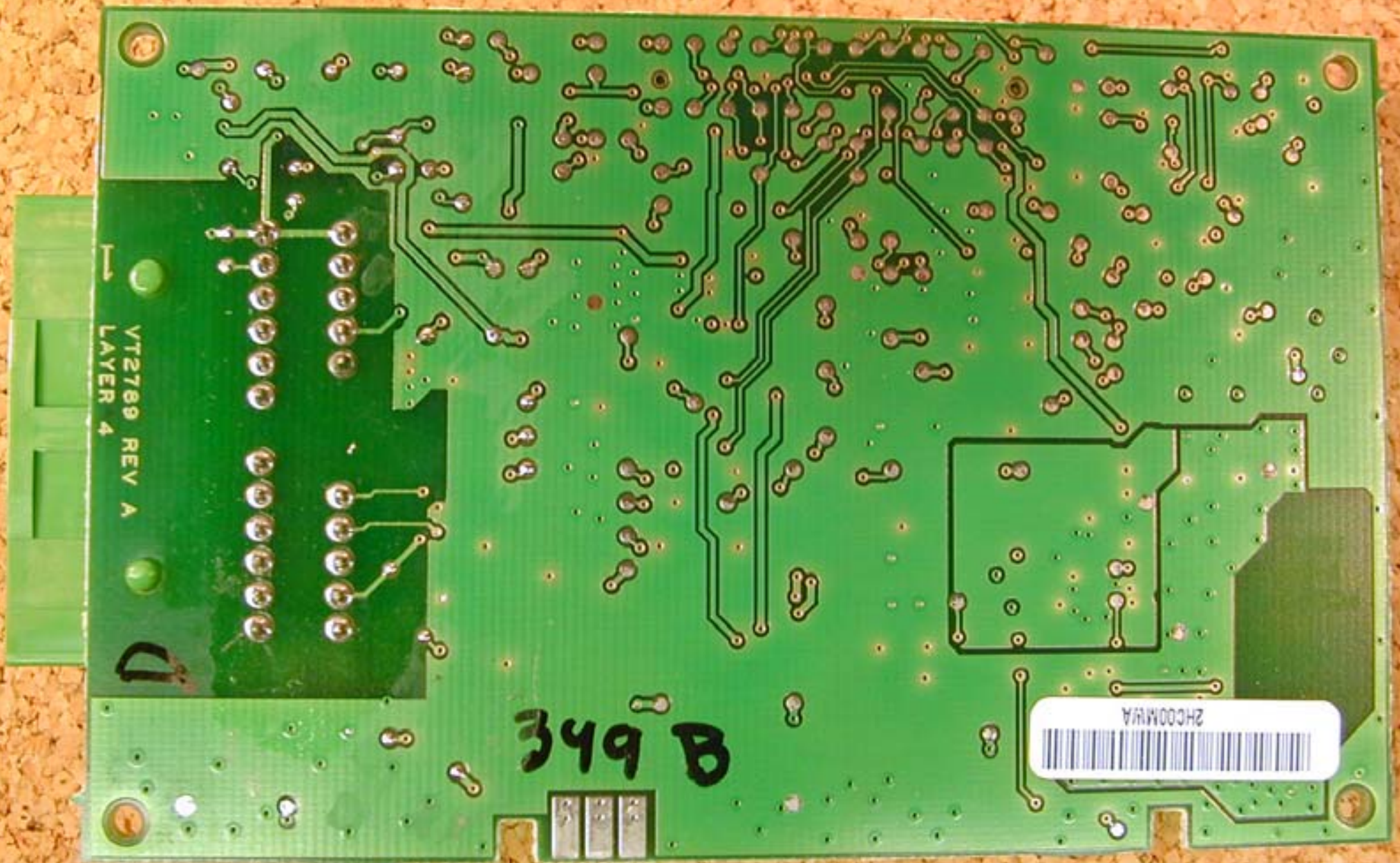
PCB 1 (bottom)

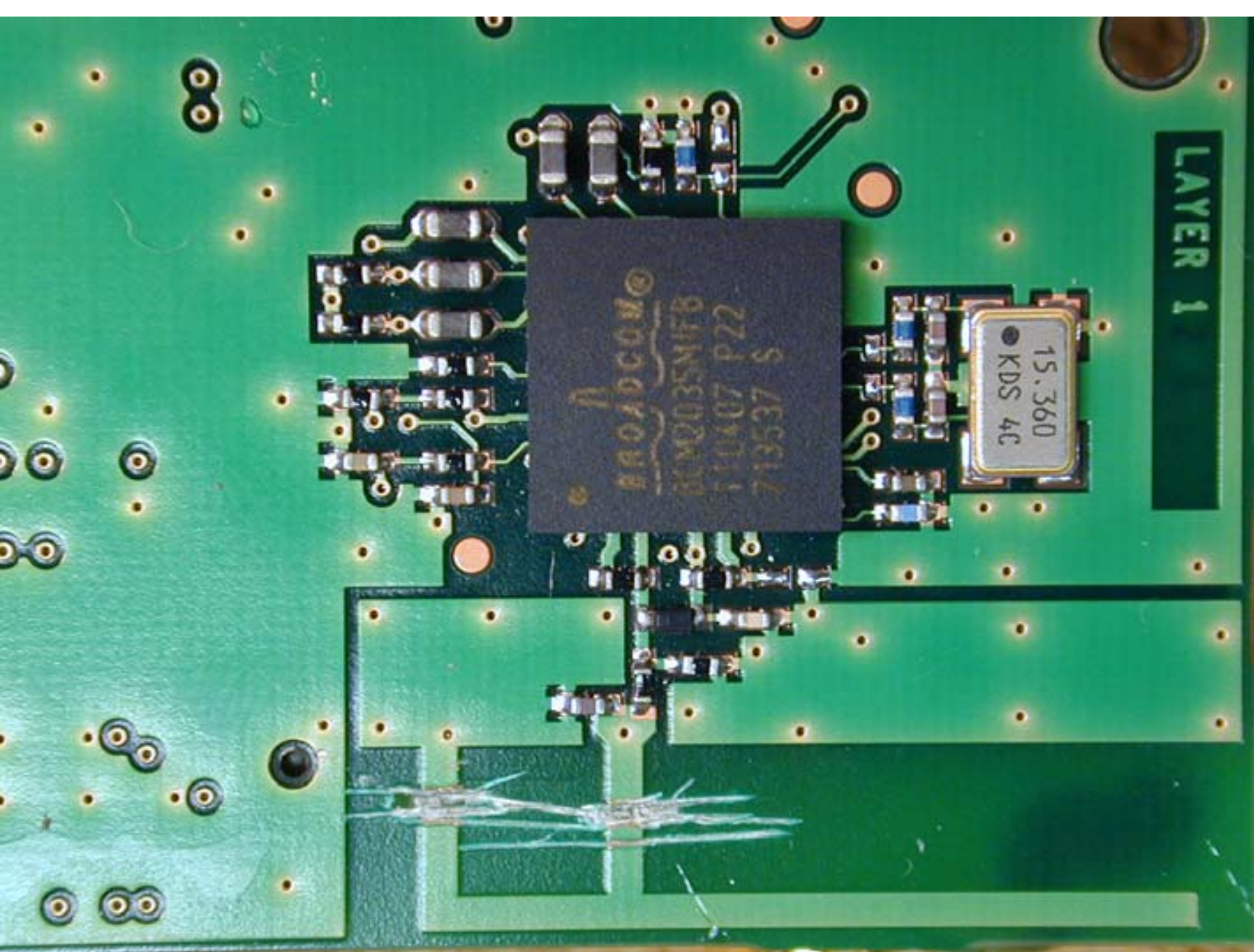


PCB 2 (top)

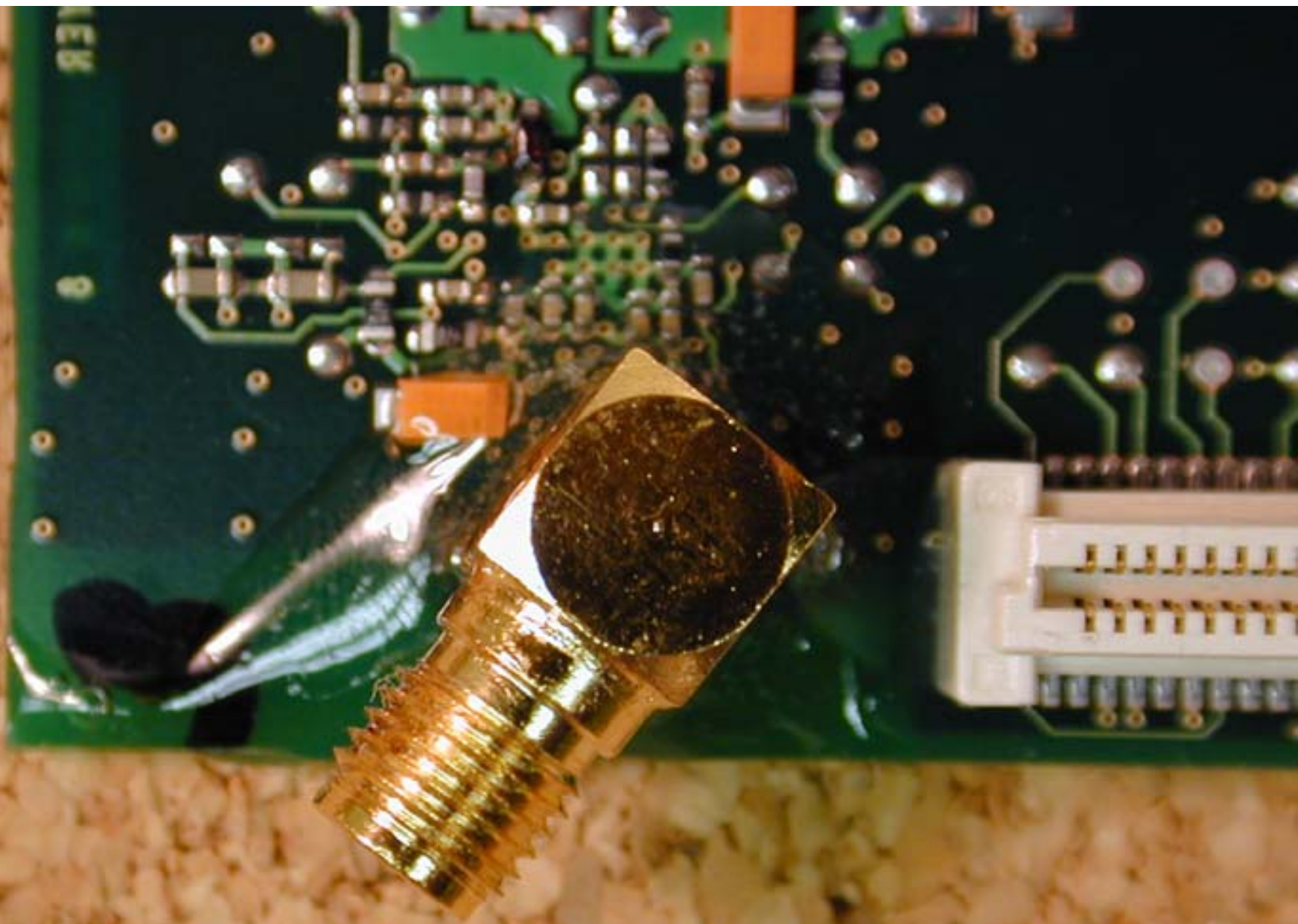


PCB 2 (bottom)





PCB antenna (removed for conducted testing)



PCB w/ SMA (added for conducted testing)