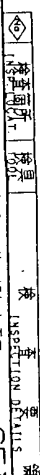
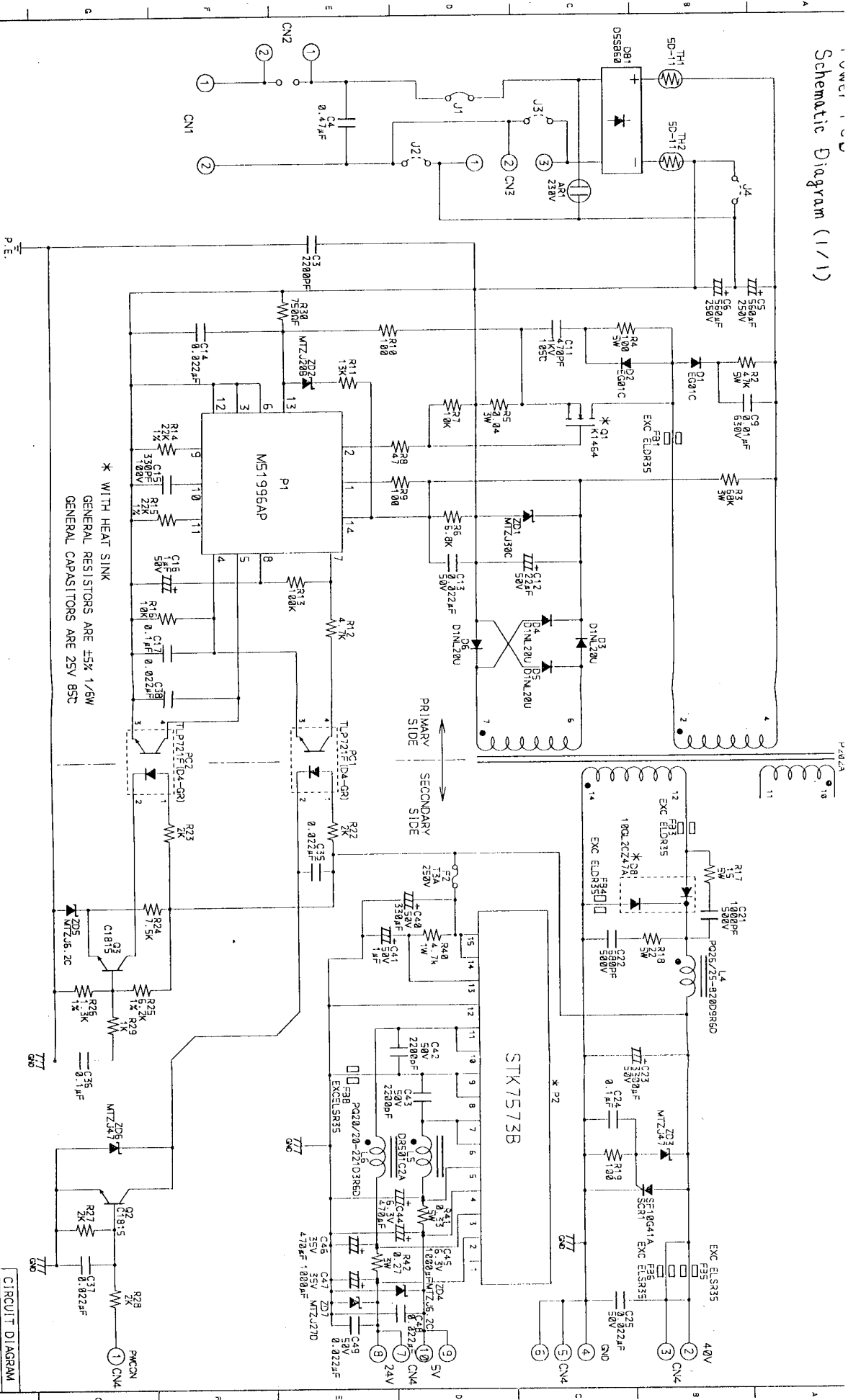


[illegible]

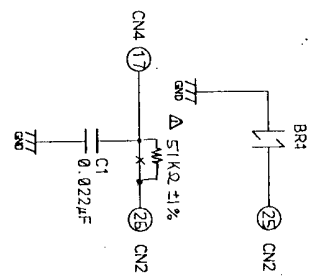
Mechanism



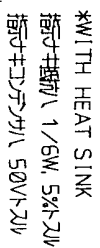
Schematic Diagram (1/1)

[illegible]

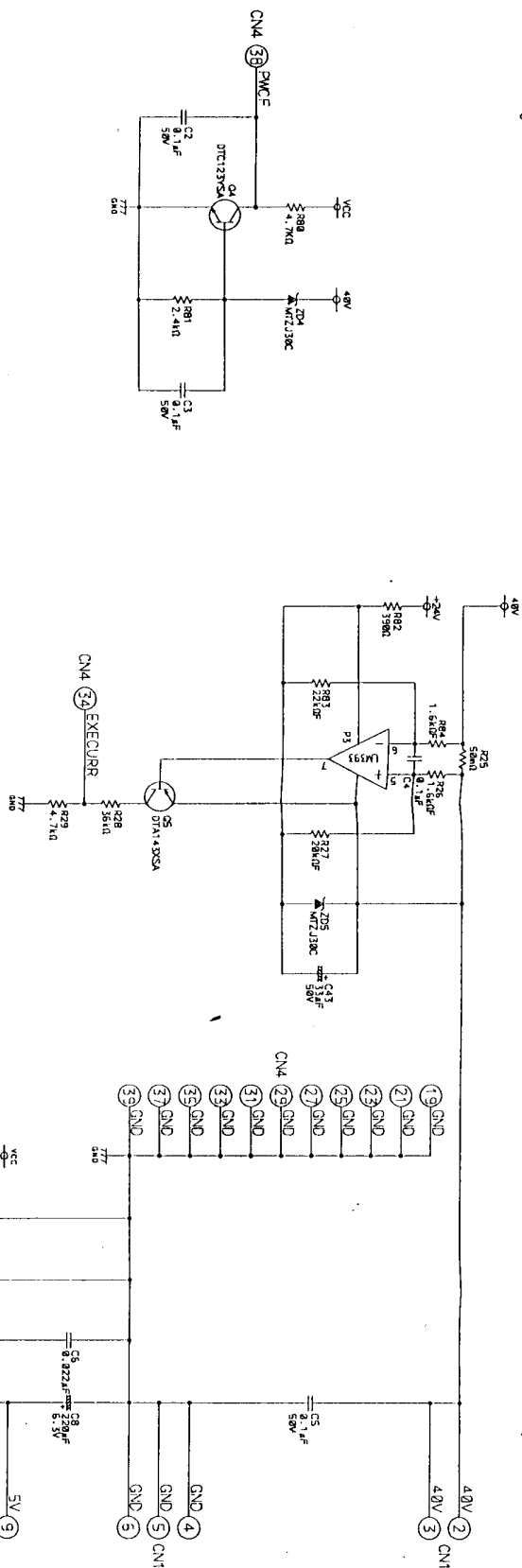
Schematic Diagram (1/3)

[illegible]

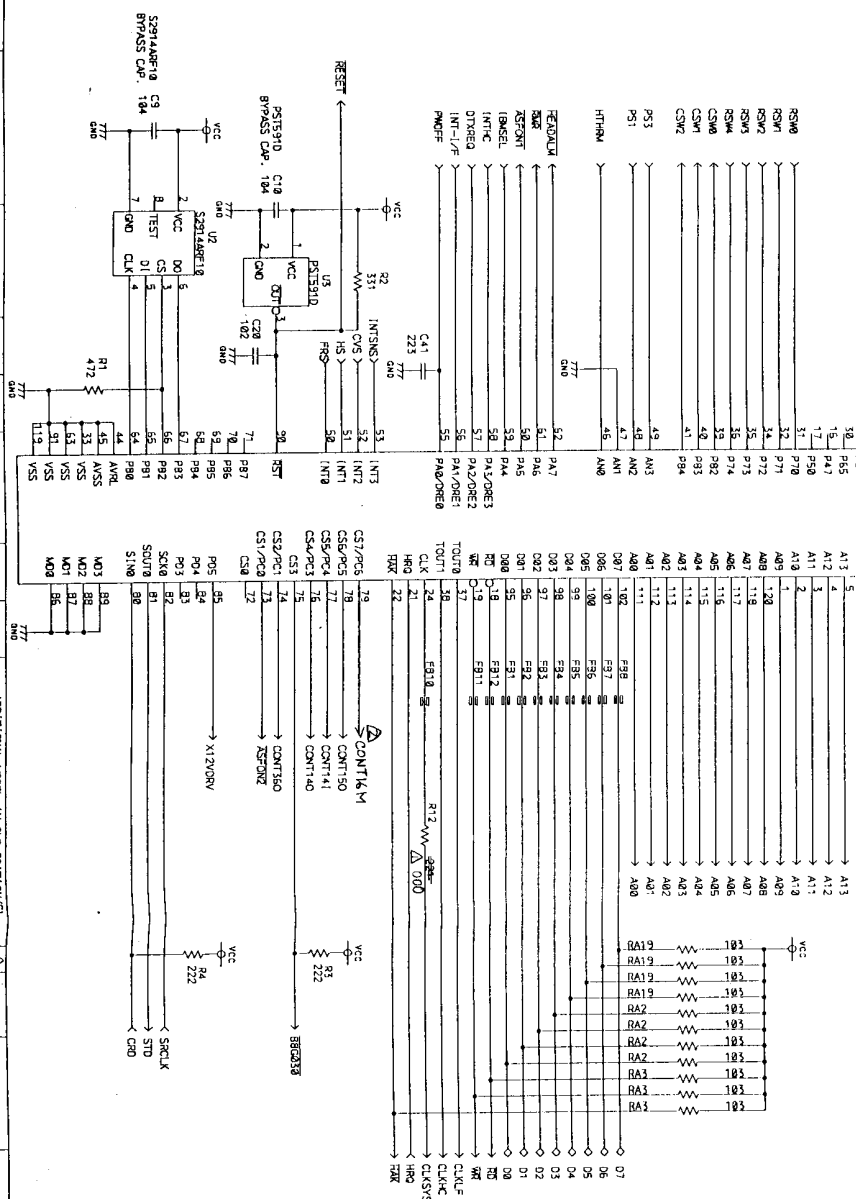
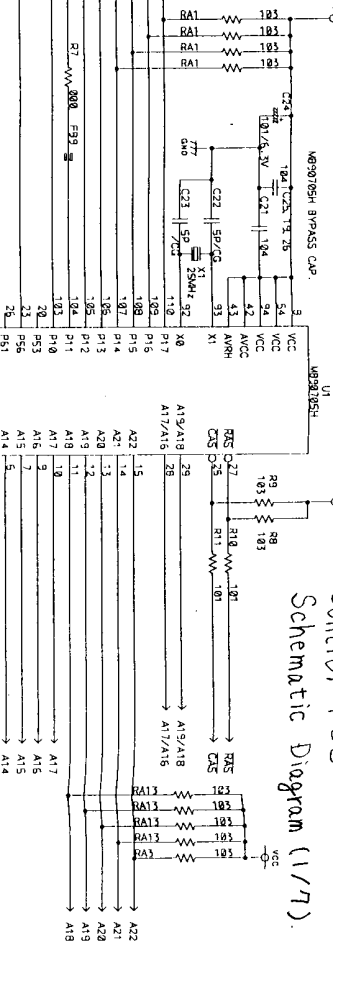
LF-ACCM ⑤
LF-BCCM ⑥

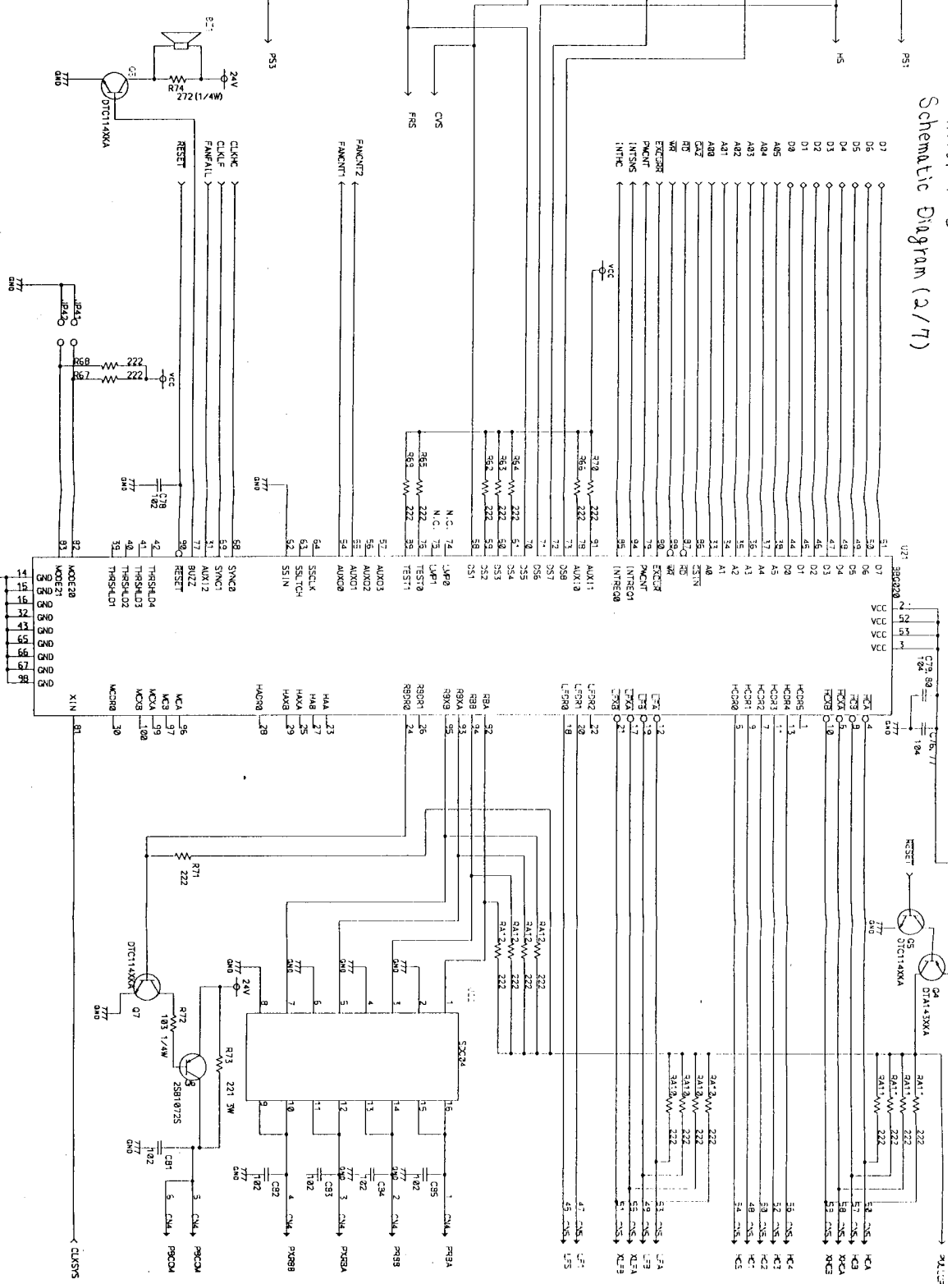
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Driver PCB
Schematic Diagram (3/3)



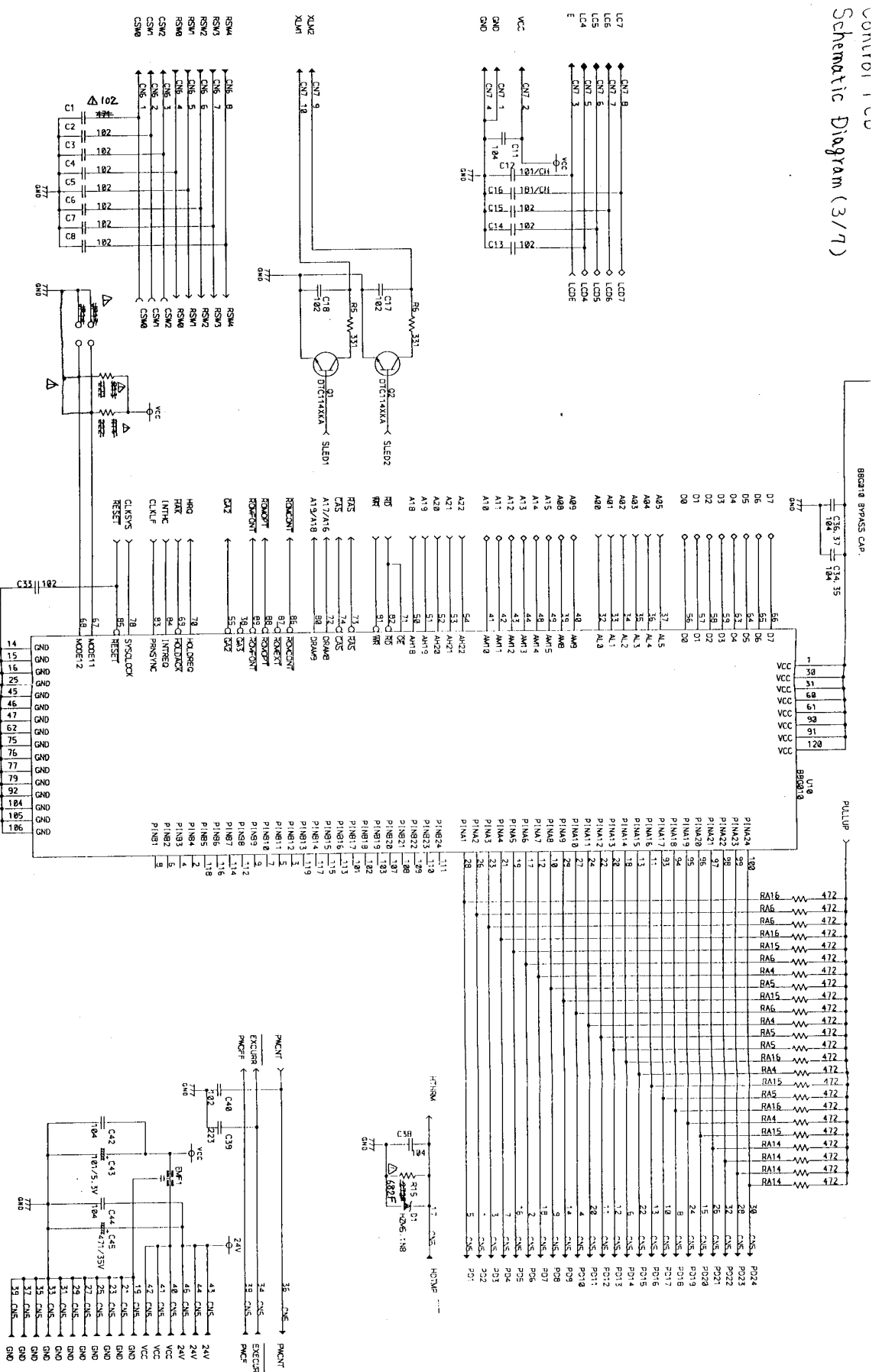
CIRCUIT DIAGRAM									
FUNG.		DRIVER		MODEL BP-9000A1a3					
95250-5037		CODE NO.							
95250-DR		PCB NAME							
KKE-P-030		SCHEMATICS NO.							
1		EDITION NO.		1					
3/73		DATE		7					
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[illegible]

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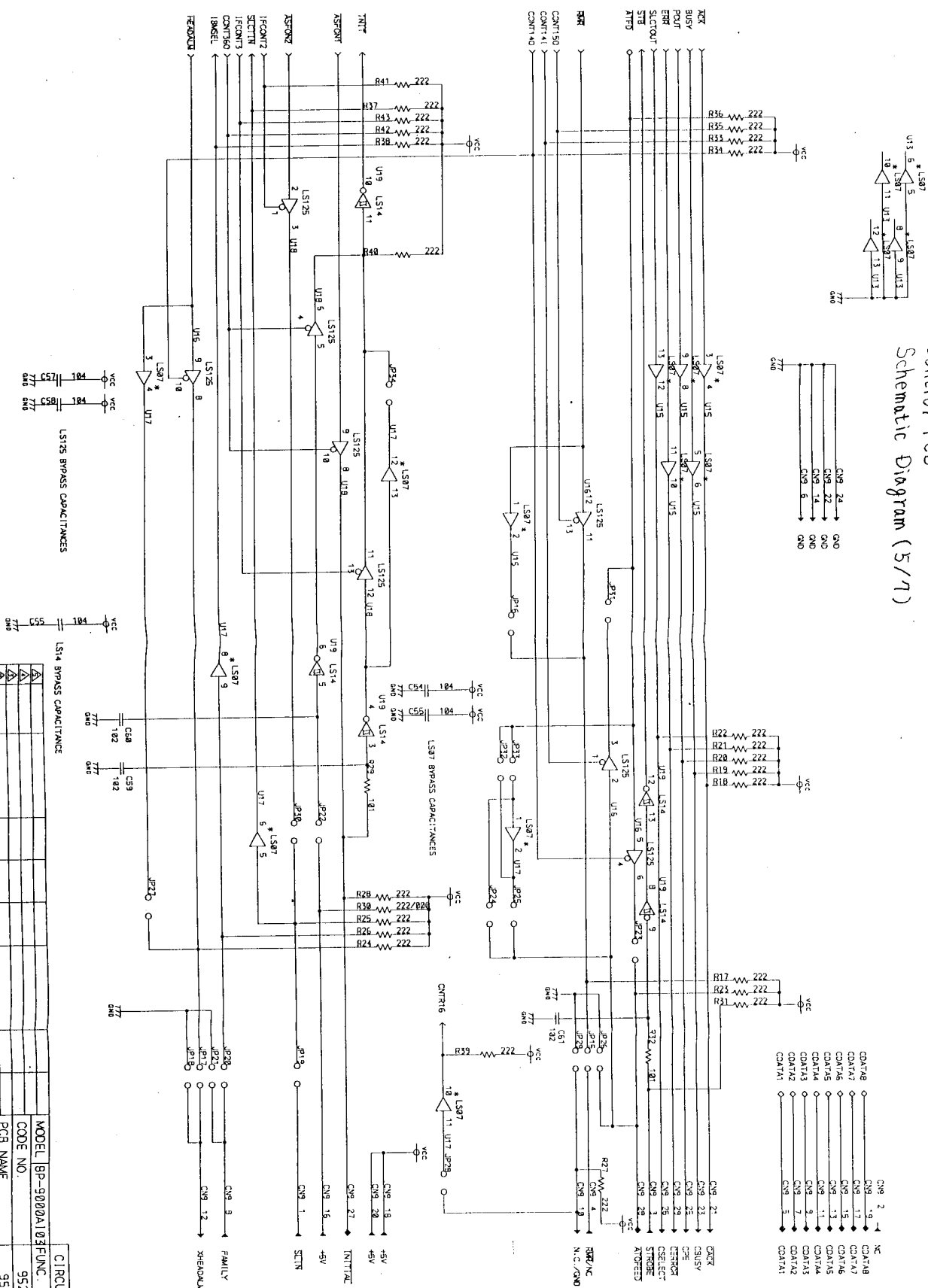
CONTROL IC

Schematic Diagram (3/7)



DATE	14/7/11	NOTE	MODE 3V	CHECKED	APPROVED	SHEET NO.	3/7	EDITION NO.	2
MODEL	BP-9000A103FUNG.	CONTROL							
CODE NO.	95250-5001								
PCB NAME	95250-CNT								
SCHEMATICS NO.	KKE-C-00X								
ASSIGNED	PERSON	SV							

[illegible][illegible]

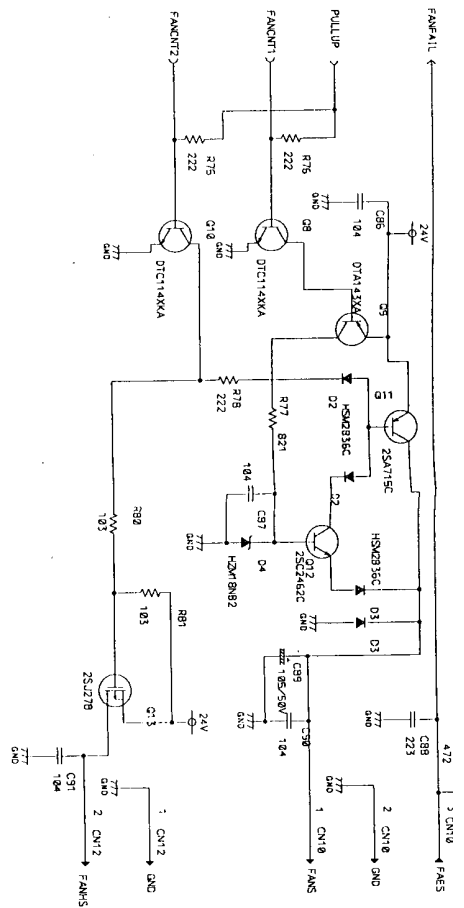


14 BYPASS CERTIFICATE										CIRCUIT DIAGRAM	
△									MODEL BP-5000A103	FUNC.	CONTROL *
△									CODE NO.	95250-5001	
△									PCB NAME	95250-CNT	
△									SCHEMATICS NO.	KKE C-00X	
1	DATE	NOTE	MADE BY	CHECKED BY	APPROVED BY	SE. NO.		ASSIGNED PERSON	SHEET NO.	5/7	EDITION NO. 2
6											

JUMPER INFORMATION A

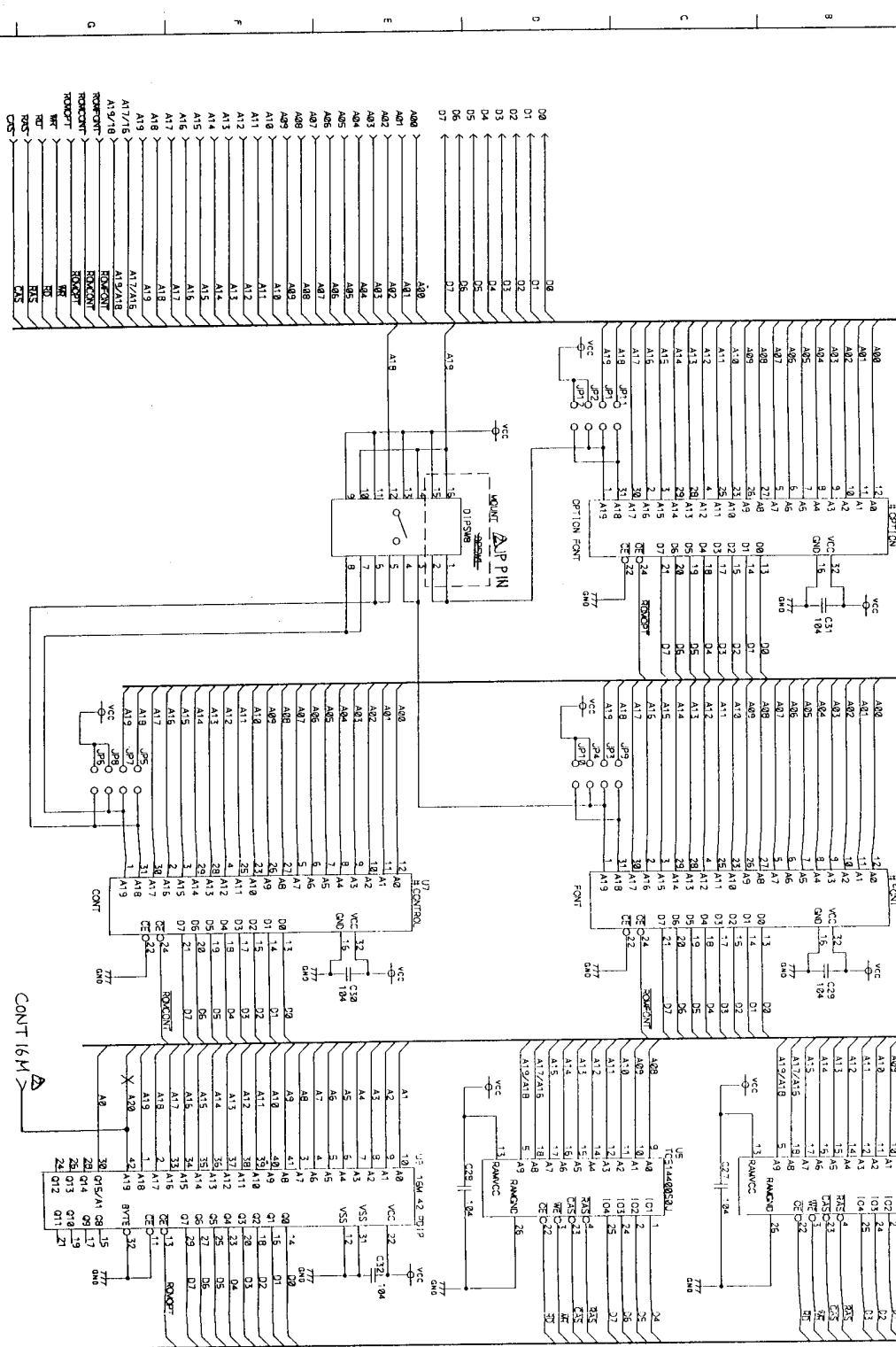
Schematic Diagram (6/7)

Pin	Signal	Condition	Value
4126	4126	PSST	LM92
4127	4127	PSST	LM92
4128	4128	PSST	LM92
4129	4129	PSST	LM92
4130	4130	PSST	LM92
4131	4131	PSST	LM92
4132	4132	PSST	LM92
4133	4133	PSST	LM92
4134	4134	PSST	LM92
4135	4135	PSST	LM92
4136	4136	PSST	LM92
4137	4137	PSST	LM92
4138	4138	PSST	LM92
4139	4139	PSST	LM92
4140	4140	PSST	LM92
4141	4141	PSST	LM92
4142	4142	PSST	LM92
4143	4143	PSST	LM92
4144	4144	PSST	LM92
4145	4145	PSST	LM92
4146	4146	PSST	LM92
4147	4147	PSST	LM92
4148	4148	PSST	LM92
4149	4149	PSST	LM92
4150	4150	PSST	LM92
4151	4151	PSST	LM92
4152	4152	PSST	LM92
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4155	4155	PSST	LM92
4156	4156	PSST	LM92
4157	4157	PSST	LM92
4158	4158	PSST	LM92
4159	4159	PSST	LM92
4160	4160	PSST	LM92
4161	4161	PSST	LM92
4162	4162	PSST	LM92
4163	4163	PSST	LM92
4164	4164	PSST	LM92
4165	4165	PSST	LM92
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4194	4194	PSST	LM92
4195	4195	PSST	LM92
4196	4196	PSST	LM92
4197	4197	PSST	LM92
4198	4198	PSST	LM92
4199	4199	PSST	LM92
4200	4200	PSST	LM92



CIRCUIT DIAGRAM	
MODEL BP-9000A183FUNG.	CONTROL
CODE NO.	95250-5001
PCB NAME	95250-CNT
SCHMATIC NO.	KKE-C-0XX
EDITION NO.	2
DATE	1991.11.20
NOTE	DESIGNED BY: [Signature]
DESIGNED BY	APPROVED BY: [Signature]
SE NO.	ASSIGNED PERSON
10	11

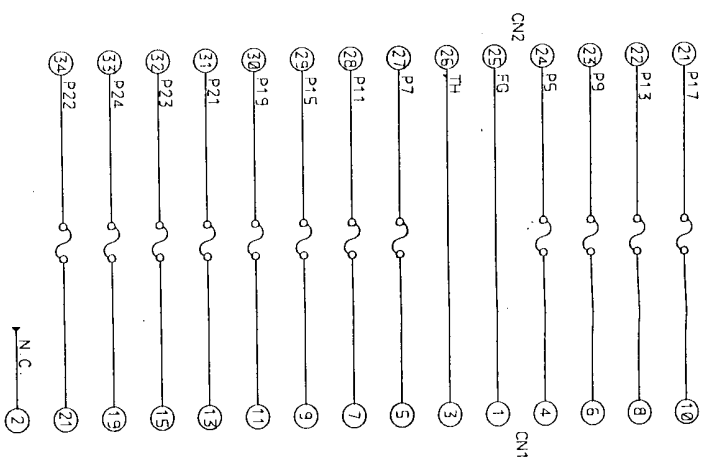
Control PCB
Schematic Diagram (7/7)



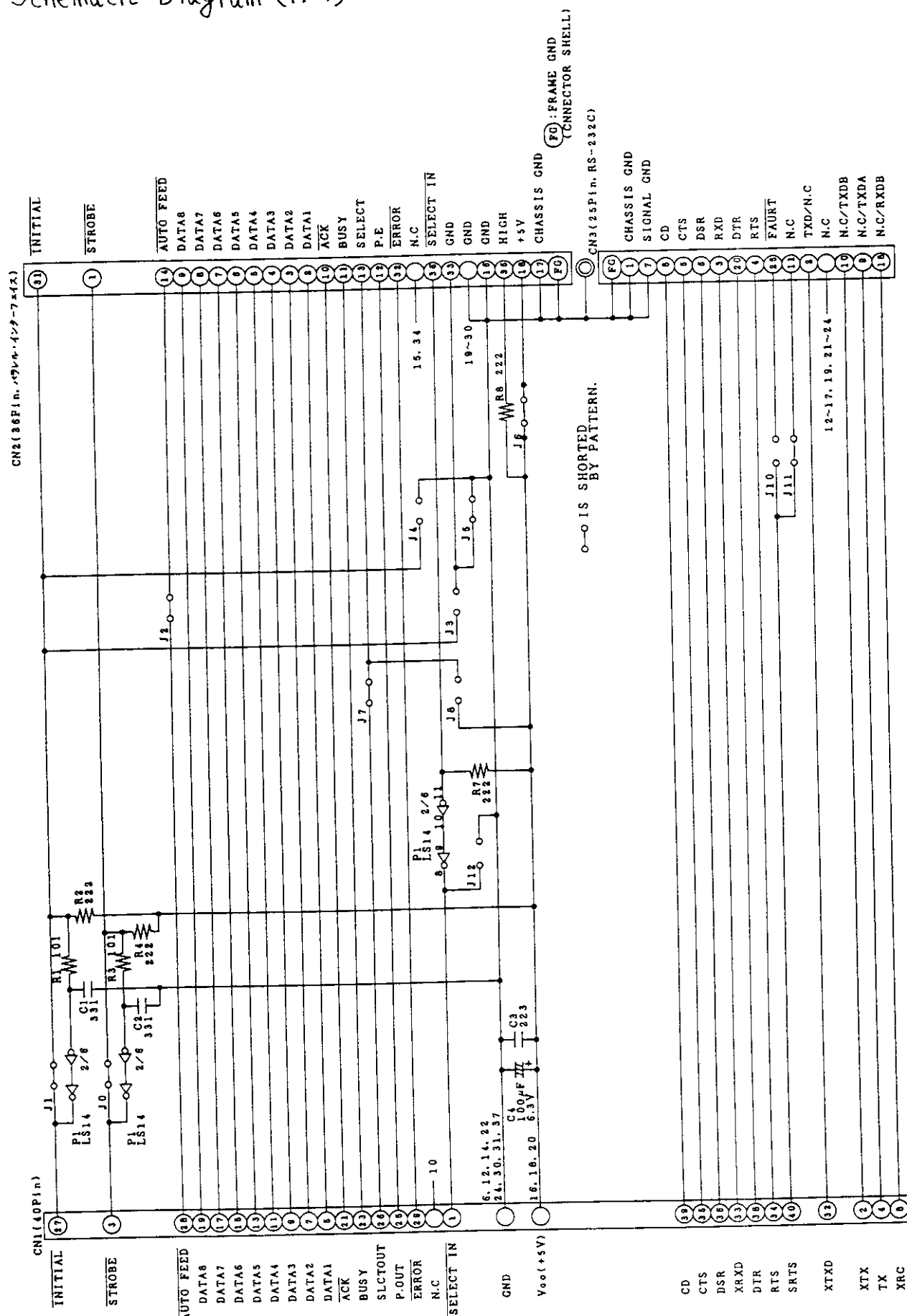
LAPERS		: INFORMATION		B			
J#2	1W/2M	MM		MM		CONTRACT	SOL TYPE
J#1	OPEN	SHORT	OPEN	OPEN	OPTION FORT		
J#12	SHORT	OPEN	SHORT	OPEN	KNUJ FORT		
J#11	OPEN	SHORT	SHORT				
J#7	SHORT	SHORT	OPEN	OPEN			
J#1	OPEN	OPEN	OPEN	OPEN		RESIDENT FORT	
J#1	SHORT	SHORT	OPEN	OPEN			
J#4	SHORT	SHORT	OPEN	OPEN			
J#3	SHORT	OPEN	SHORT	OPEN			
J#3	OPEN	SHORT	SHORT	SHORT			

[illegible][illegible]

The diagram illustrates a 27-bit bus architecture. It features two main horizontal bus lines, CN1 (top) and CN2 (bottom). Between these are 10 vertical bus lines labeled P1 through P10. To the left of the P lines, there are 20 numbered nodes (1-20) connected to the P lines. To the right of the CN lines, there are 7 numbered nodes (21-27) connected to the CN lines. The P lines are labeled P1 to P10 from top to bottom. The CN lines are labeled CN1 and CN2. The nodes on the left are labeled 1 to 20, and the nodes on the right are labeled 21 to 27. The diagram shows a complex interconnection of these nodes and lines.

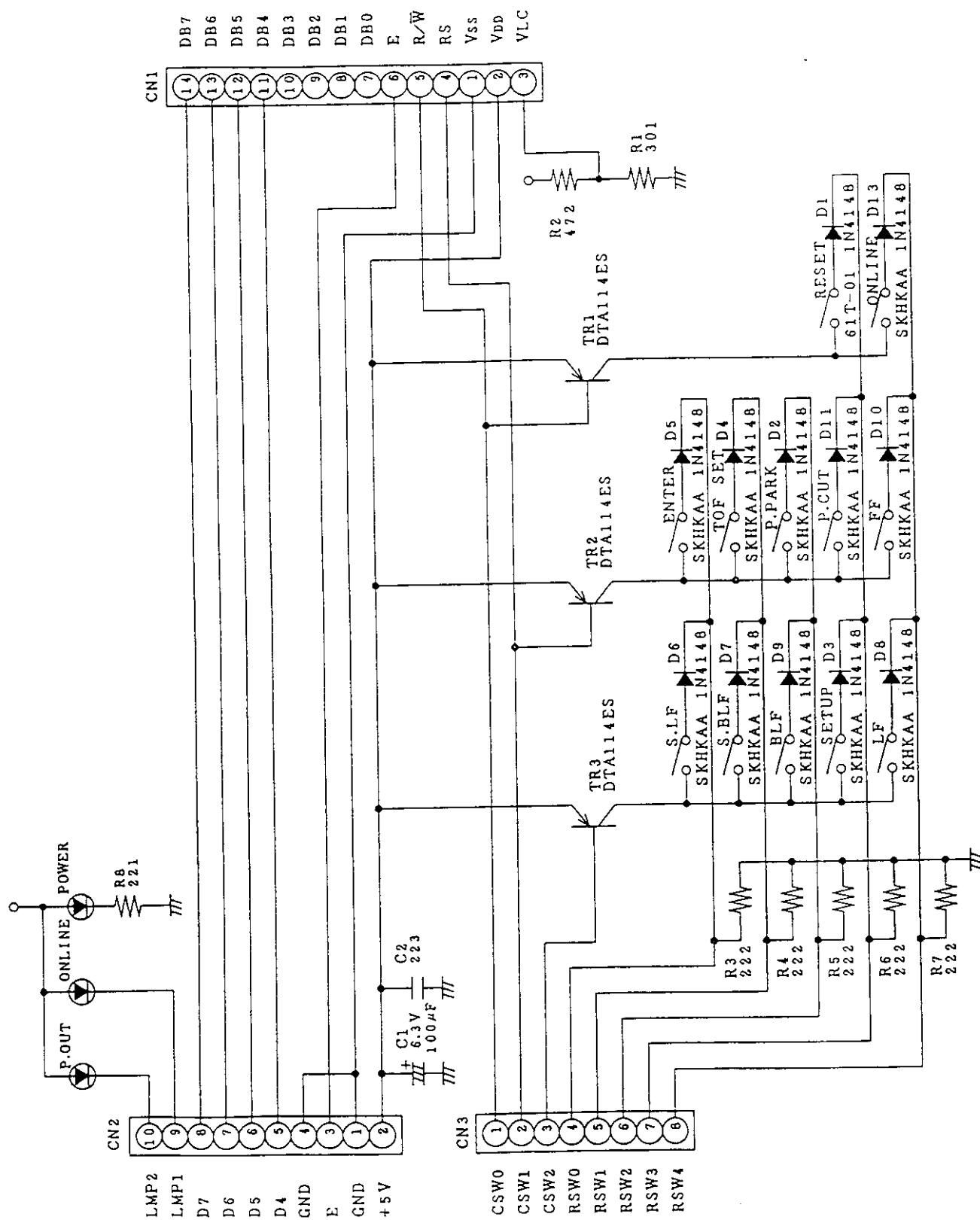
[illegible]

Schematic Diagram (1/1)



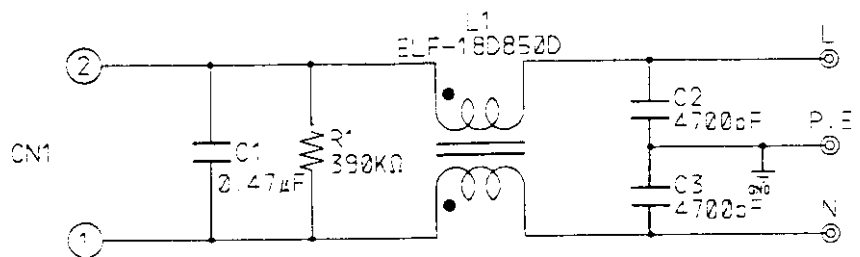
Indicator PCB

Schematic Diagram (1/1)

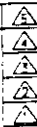


AC PCB

Schematic Diagram (1/1)



ADDITIONAL MODEL IN OLD EDITION(S):



1997.6.9 新 規 最 本

YP47-175

CHECKED, APPROVED

EE NO.

ASSIG: