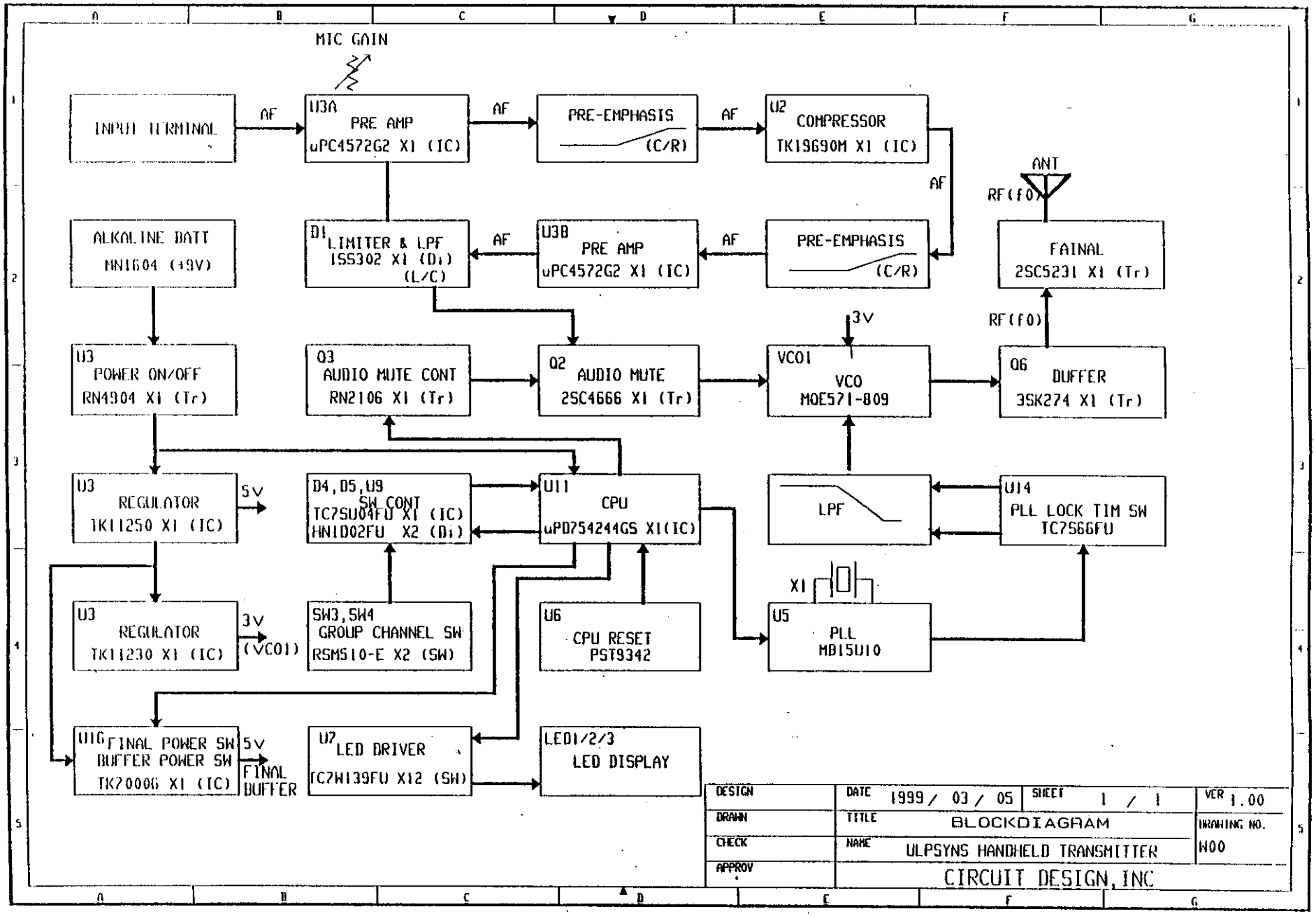
MIC GAIN
INPUT TERMINAL
AF
U3A
PRE AMP
uPC4572G2 X1 (IC)
PRE-EMPHASIS
(C/R)
U2
COMPRESSOR
TK19690M X1 (IC)
ANT
RF(f0)
ALKALINE BATT
MN1604 (+9V)
D1
LIMITER & LPF
ISS302 X1 (D1)
(L/C)
U3B
PRE AMP
uPC4572G2 X1 (IC)
FAINAL
2SC5231 X1 (Tr)
3V
U3
POWER ON/OFF
RN4904 X1 (Tr)
Q3
AUDIO MUTE CONT
RN2106 X1 (Tr)
Q2
AUDIO MUTE
2SC4666 X1 (Tr)
VCO1
VCO
MOE571-809
Q6
BUFFER
3SK274 X1 (Tr)
U3
REGULATOR
TK11250 X1 (IC)
5V
D4,D5,U9
SW CONT
TC7SU04FU X1 (IC)
HN1D02FU X2 (D1)
U11
CPU
uPD754244GS X1(IC)
LPF
U14
PLL LOCK TIM SW
TC7S66FU
X1
U3
REGULATOR
TK11230 X1 (IC)
3V
(VCO1)
SW3,SW4
GROUP CHANNEL SW
RSM510-E X2 (SW)
U6
CPU RESET
PST9342
U5
PLL
MB15U10
U16
FINAL POWER SW
BUFFER POWER SW
TK20006 X1 (IC)
5V
FINAL
BUFFER
U7
LED DRIVER
(C7W139FU X12 (SW)
LED1/2/3
LED DISPLAY
DESIGN
DRAWN
CHECK
APPROV
DATE 1999 / 03 / 05
SHEET 1 / 1
VER 1.00
TITLE BLOCKDIAGRAM
NAME ULPSYNS HANDHELD TRANSMITTER
DRAWING NO. N00
CIRCUIT DESIGN,INC