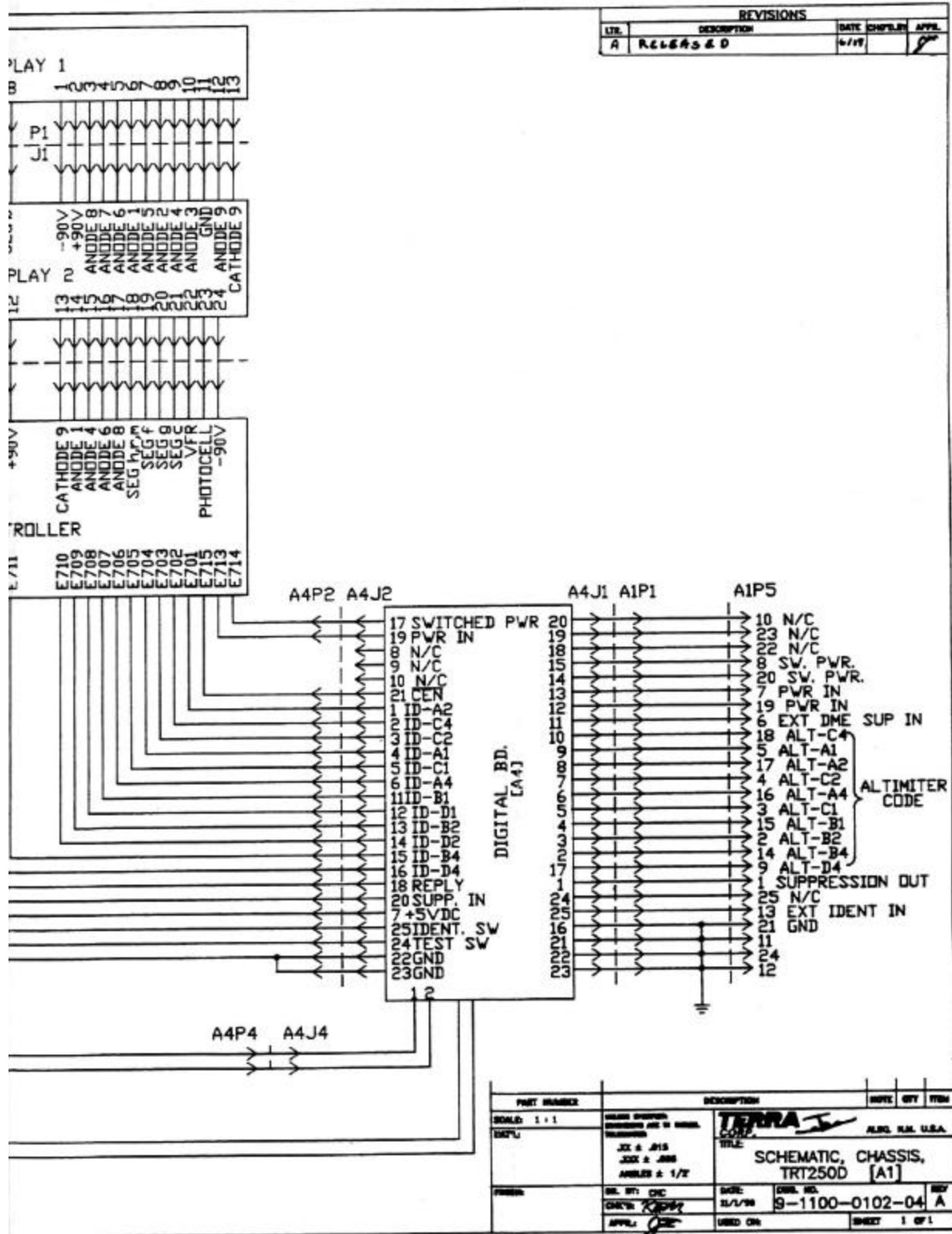
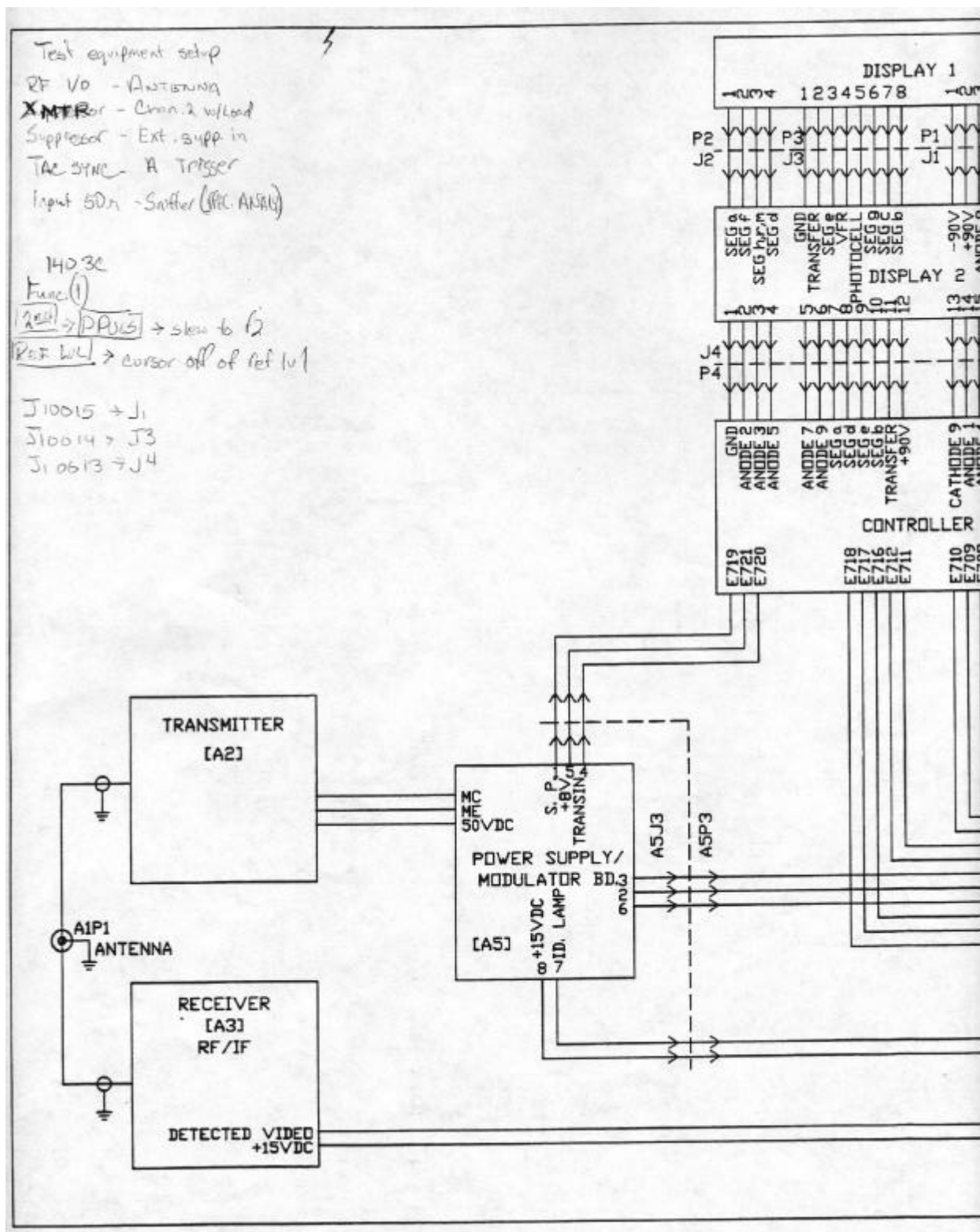


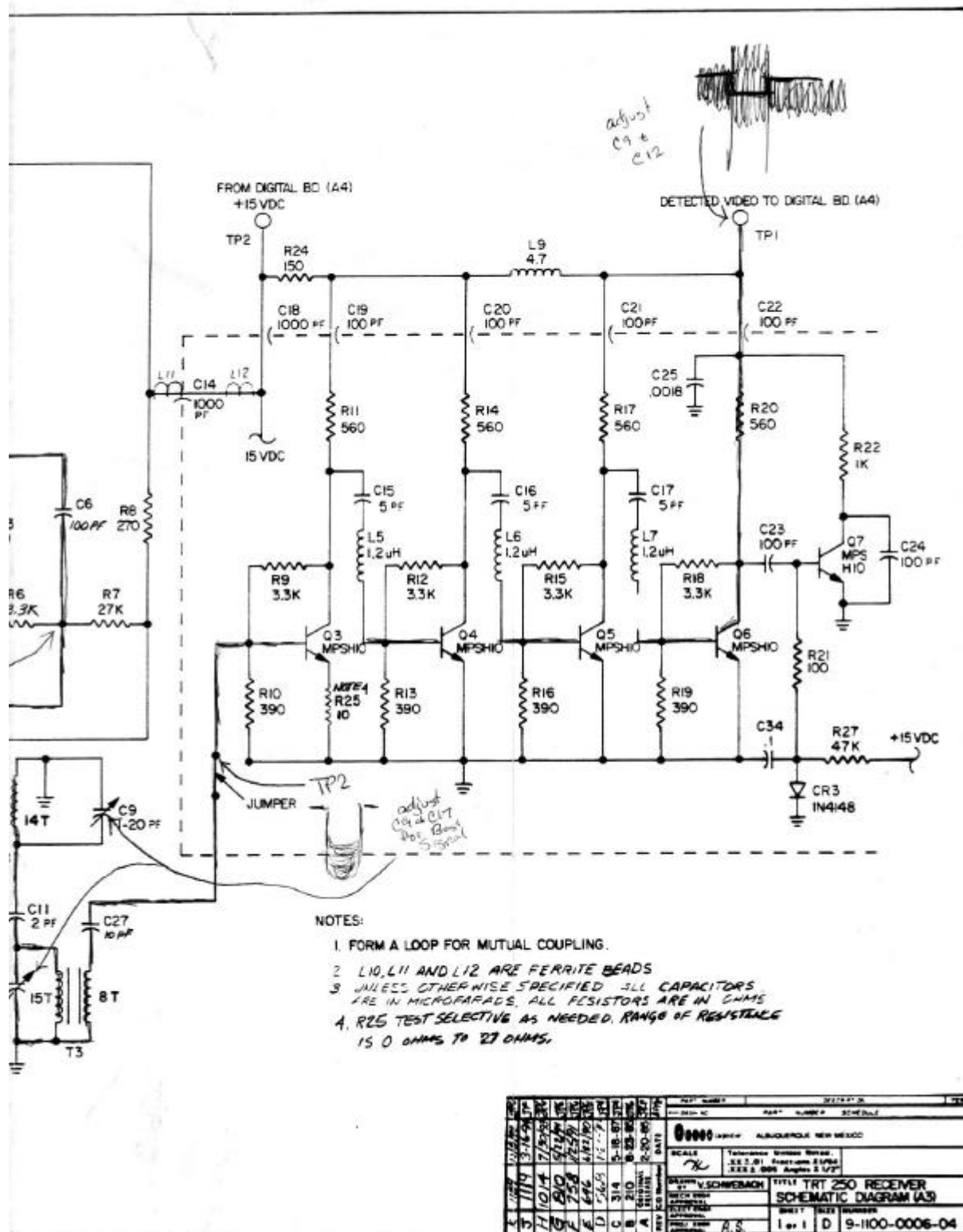
Chassis, Page 1 of 2



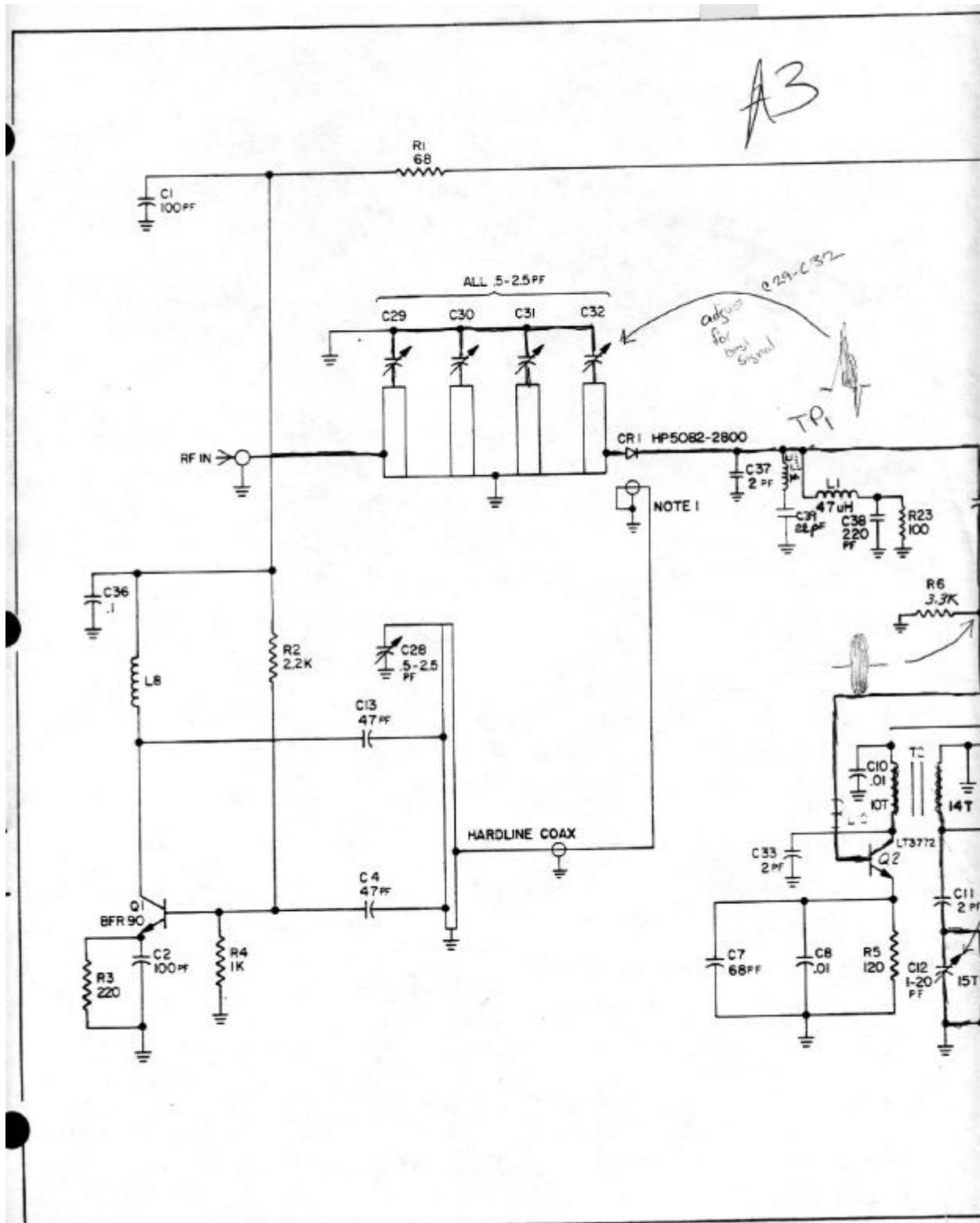
Chassis, Page 2 of 2



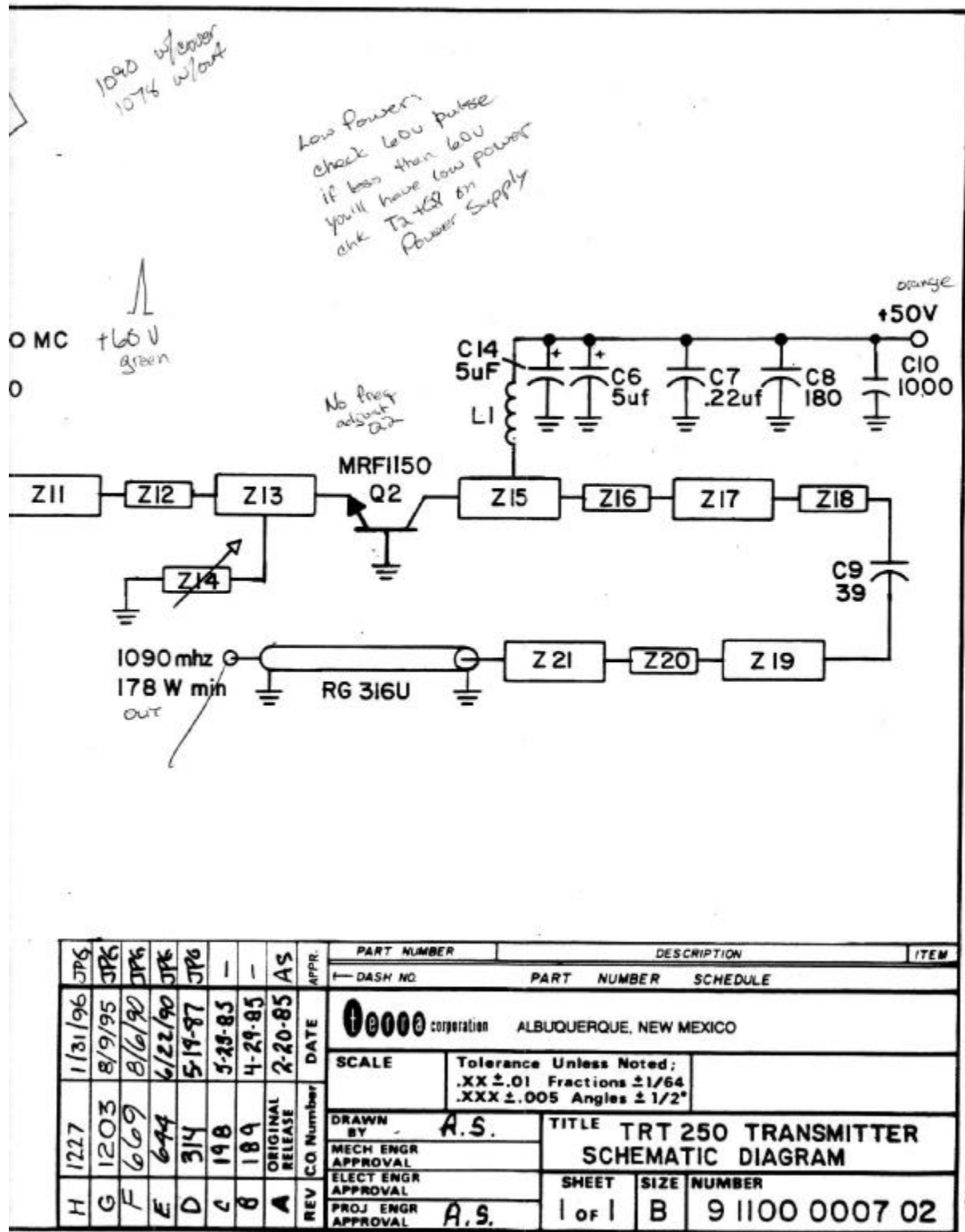
Receiver, Page 1 of 2



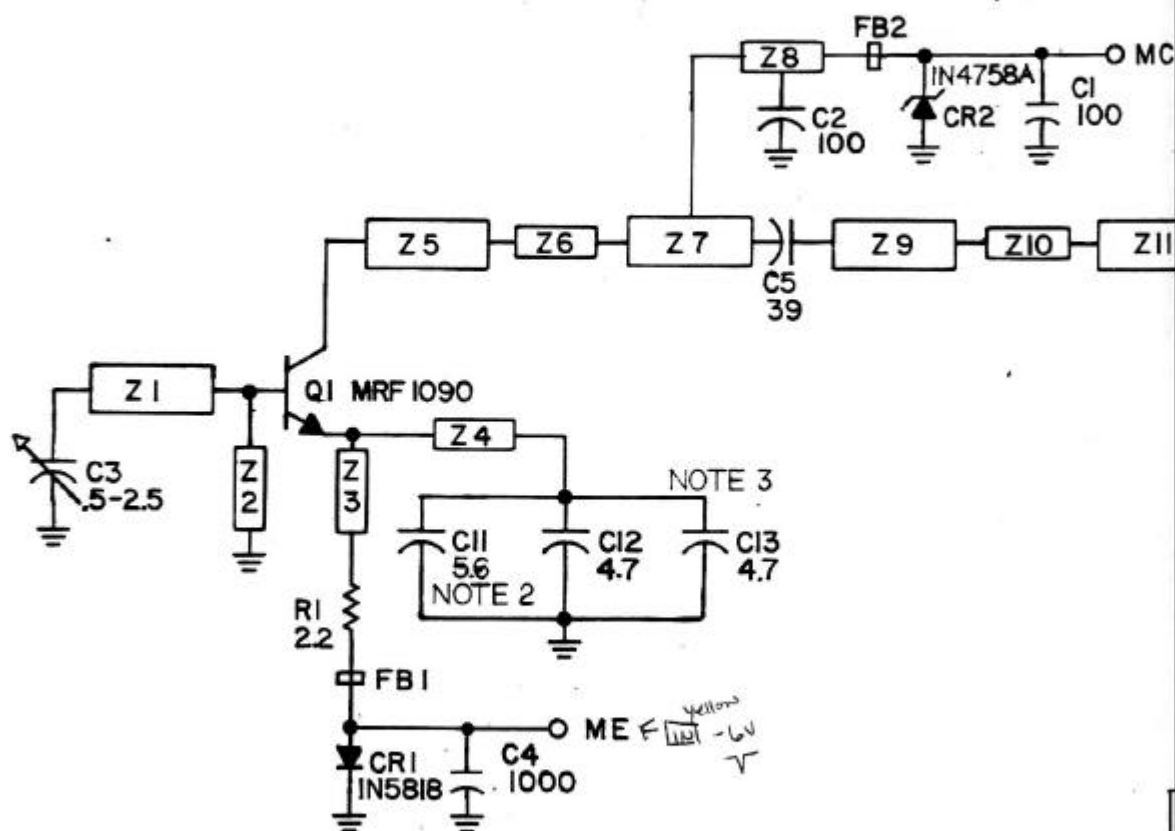
A3



Transmitter, Page 1 of 2



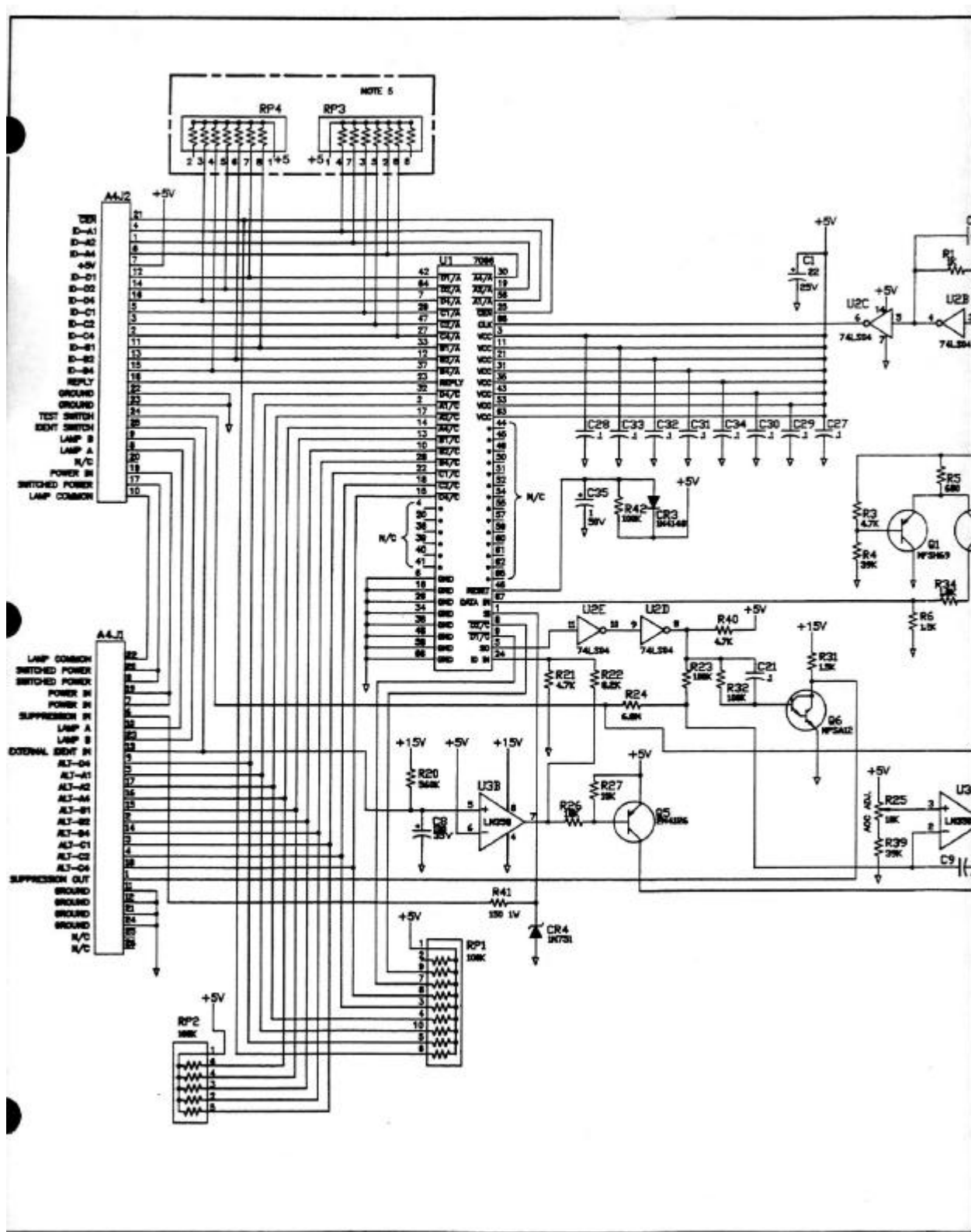
Transmitter, Page 2 of 2



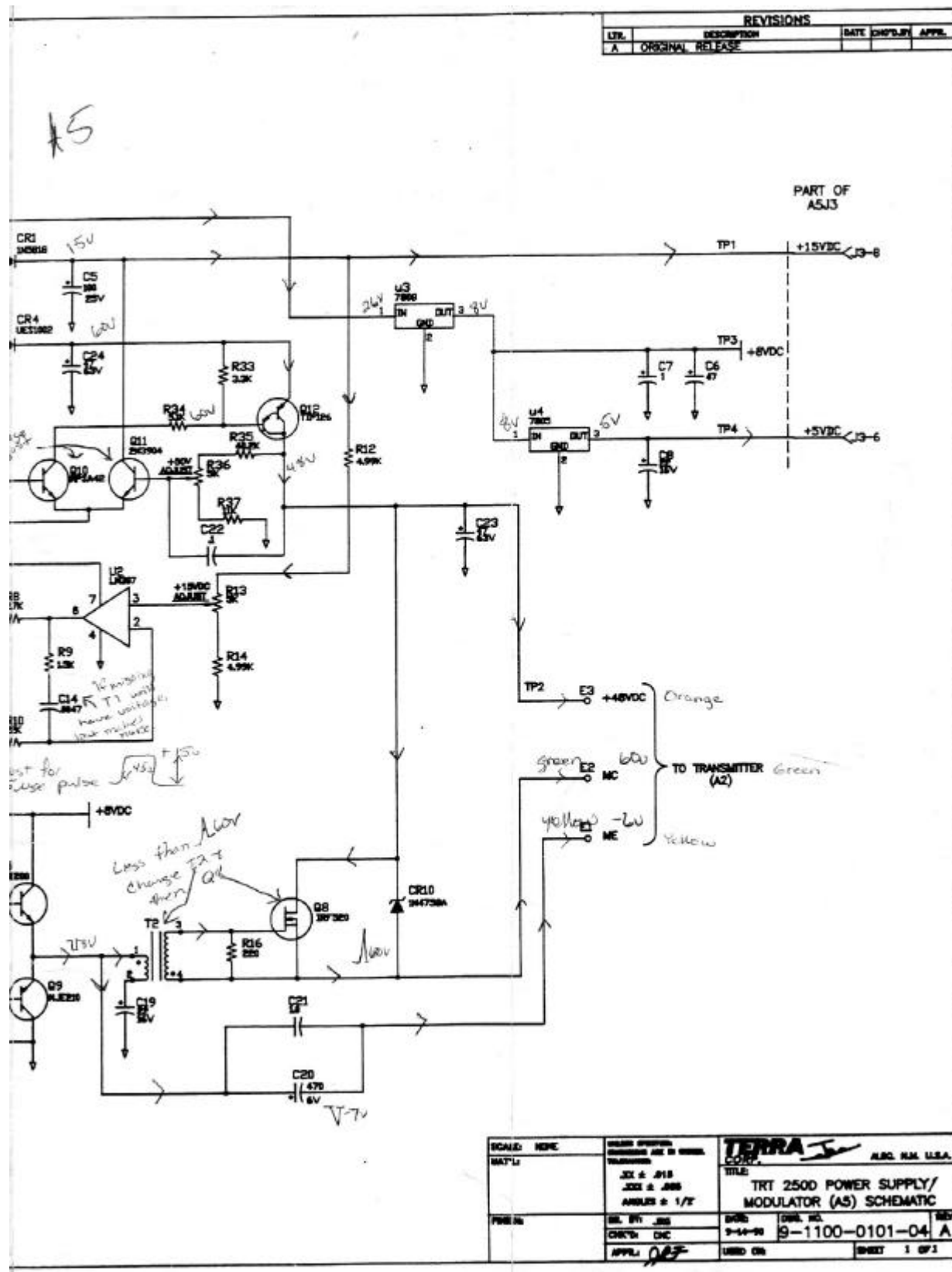
NOTES:

1. ALL CAPACITANCE IN pf's UNLESS NOTED.
2. C11, C12, & C13 ARE ~~TEST~~ SELECT FROM 4.7 TO 5.6pF
3. C12 & C13 ARE INSTALLED IN TEST AS REQUIRED.

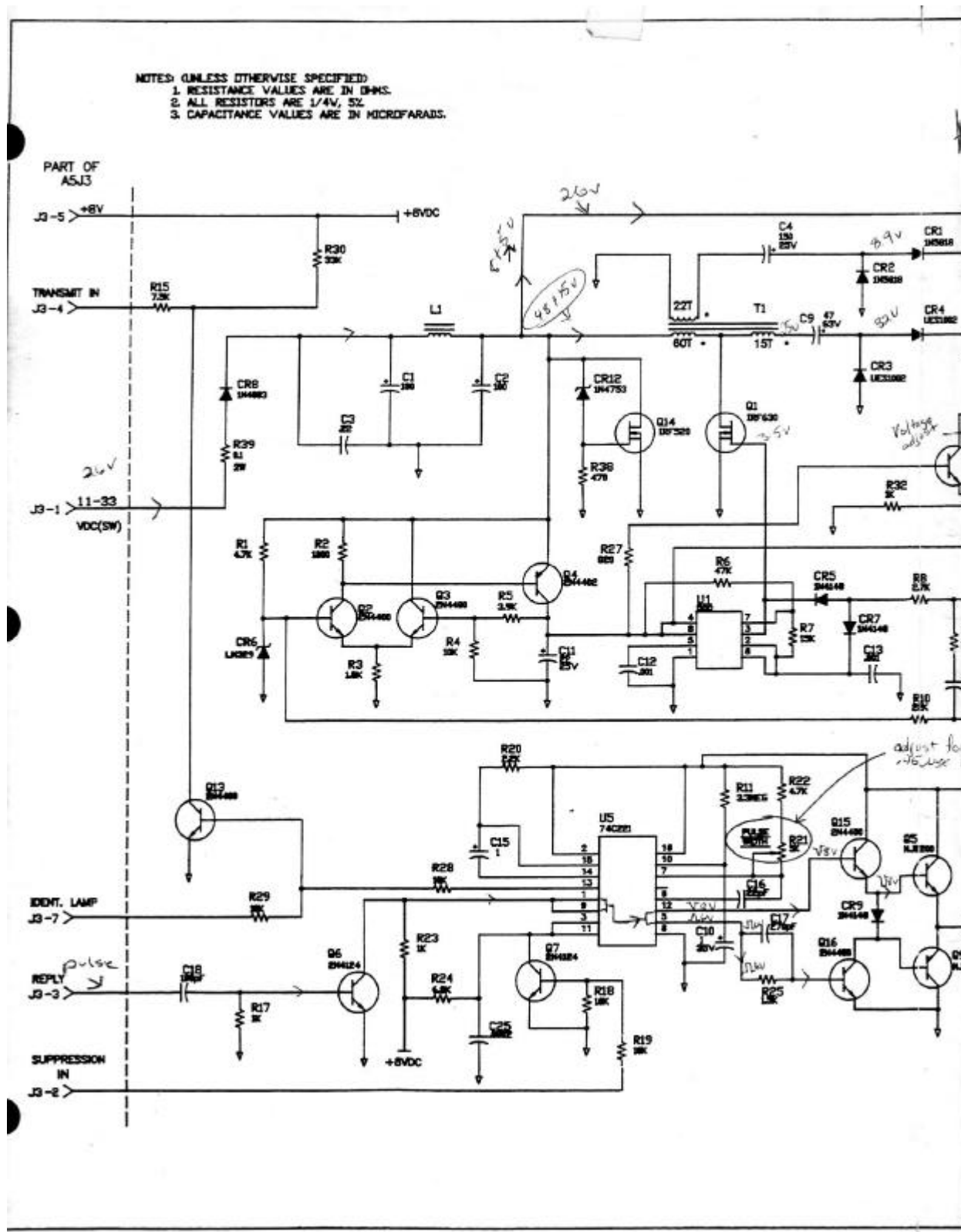
Digital Board, Page 2 of 2



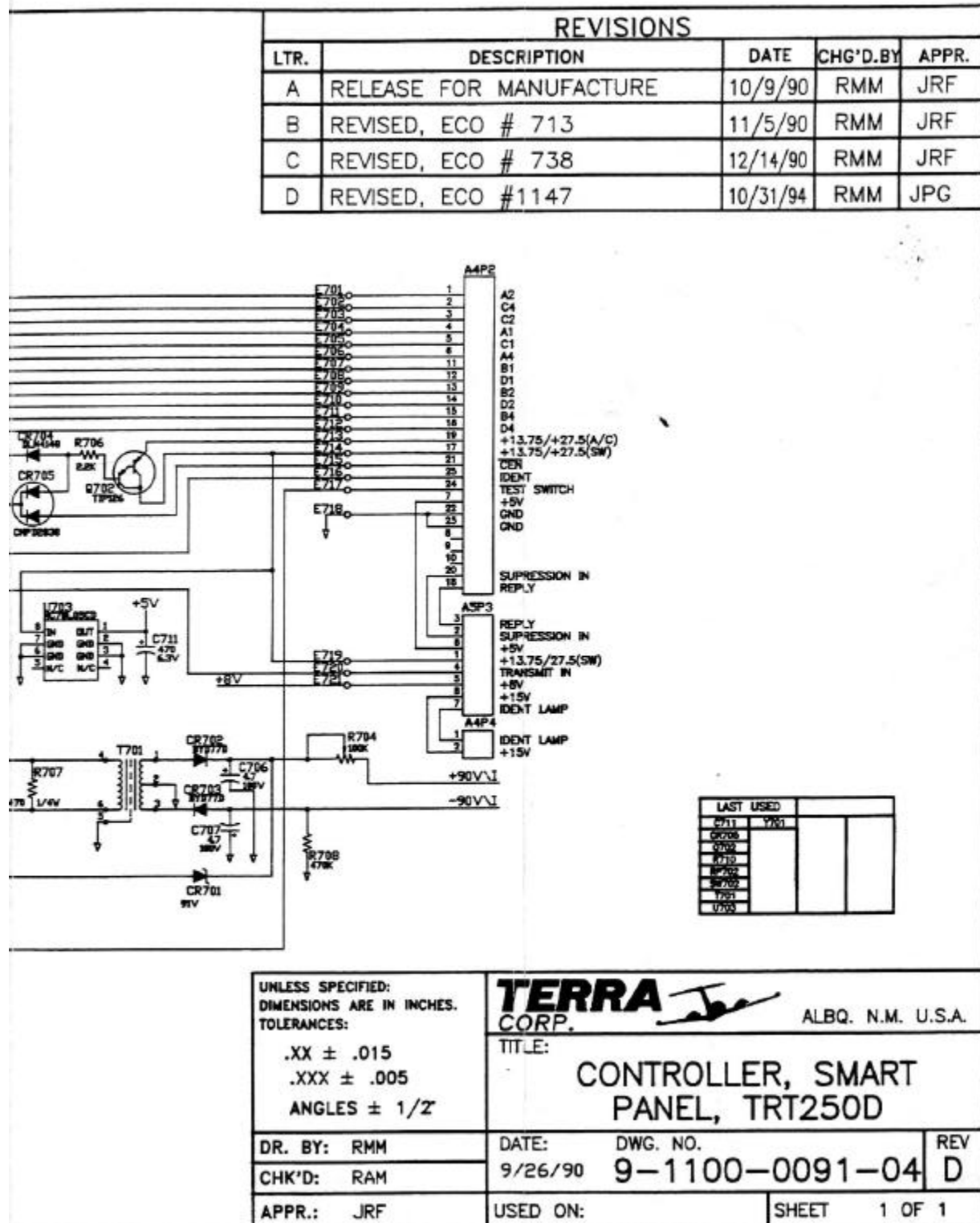
Power Supply/Modulator, Page 1 of 2



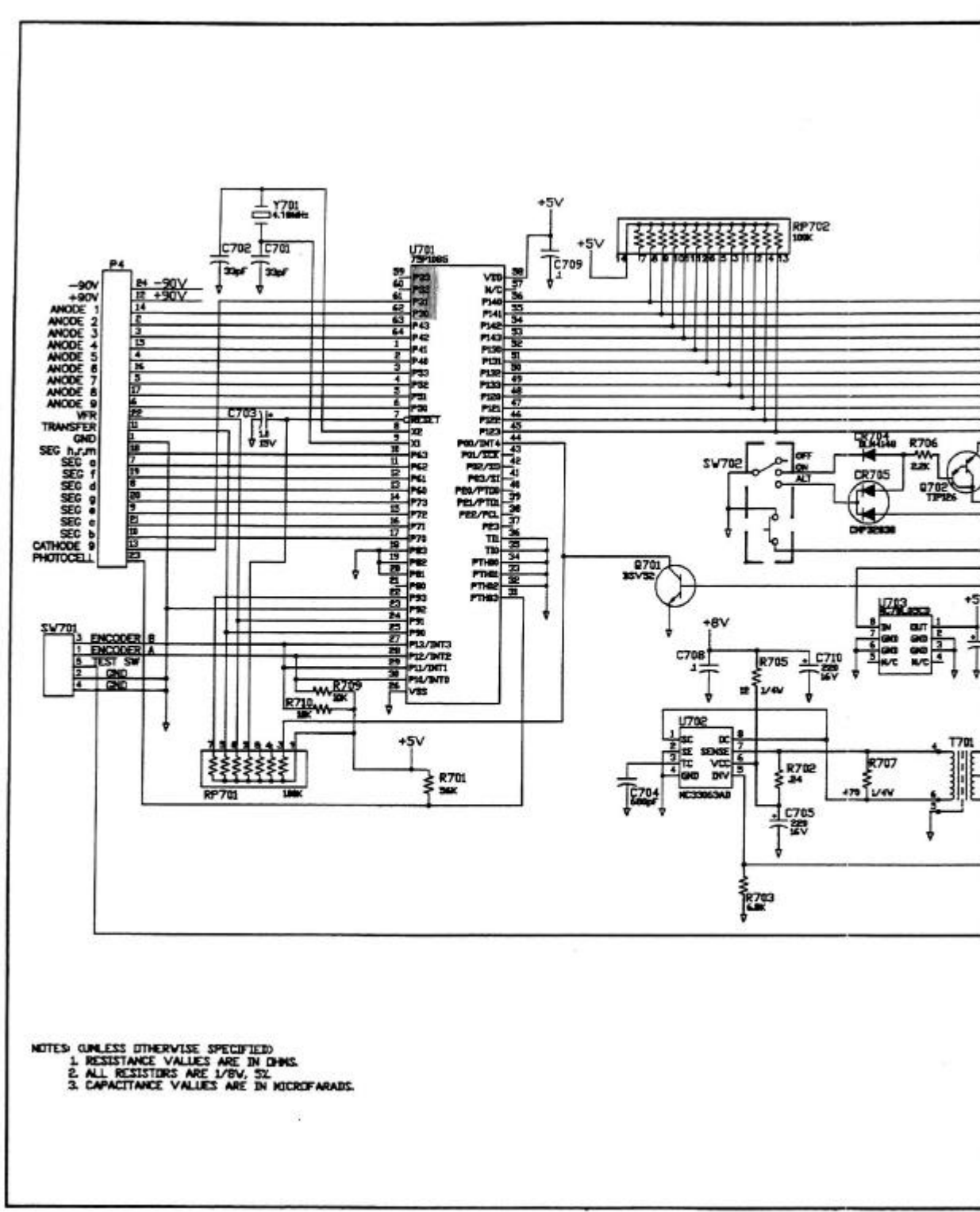
Power Supply/Modulator, Page 1 of 2



Smart Panel Controller, Page 1 of 2



Smart Panel Controller, Page 2 of 2



NOTE 4)
DS901
SAS 115P

DISPLAY BOARD #1

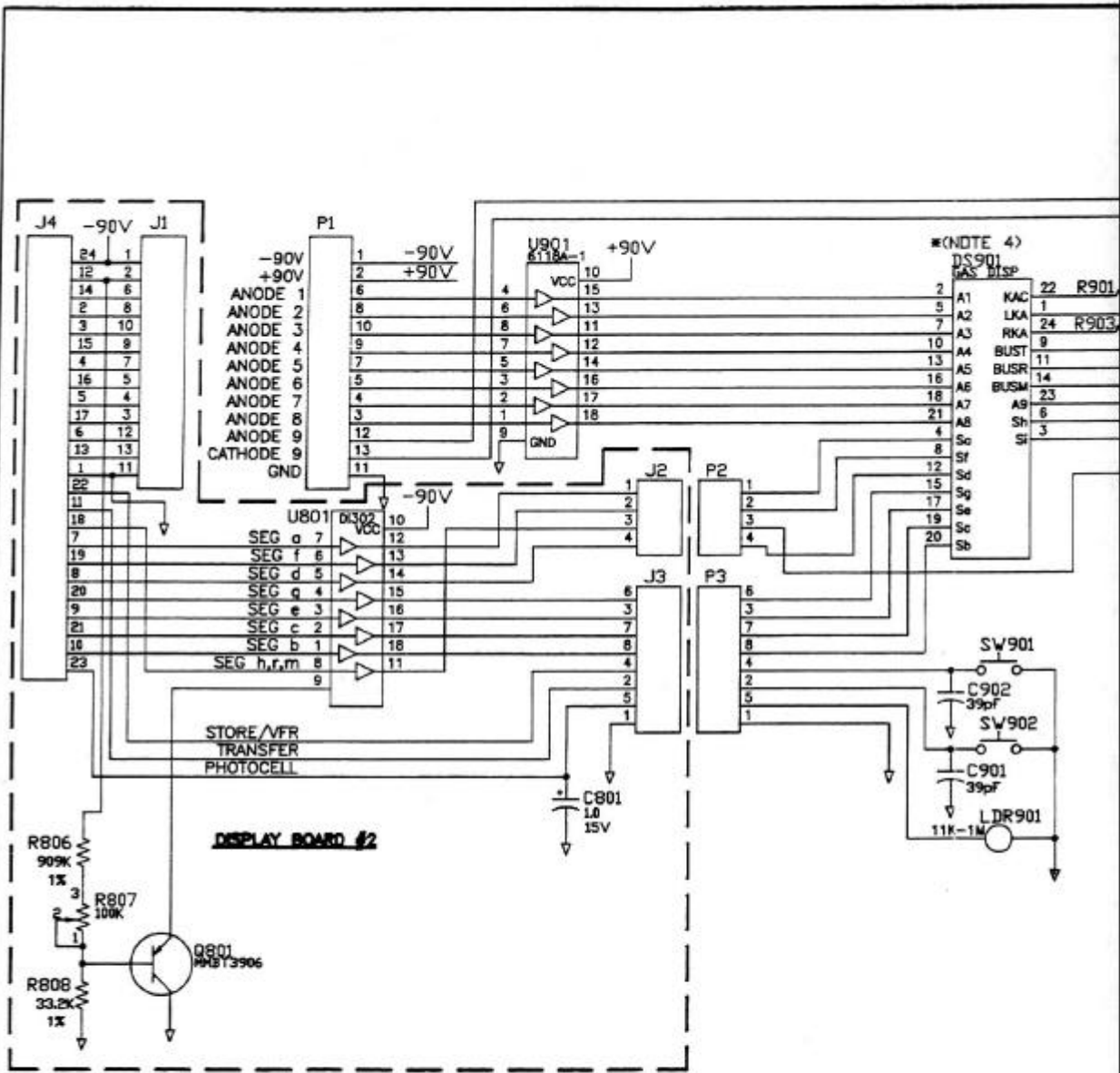
REVISIONS				
LTR.	DESCRIPTION	DATE	CHG'D BY	APPR.
A	RELEASE FOR MANUF.	8/8/90	RMN	JRF
B	REVISED, ECO # 724	11/16/90	RMN	JRF
C	REVISED, ECO # 1195	7/18/95	RAF	JPG
D	REVISED, ECO # 1202	8/1/95	EIA	JPG

DISPLAY BD #2

PART USED	NOT USED
D1	R801-
D1	R805
D8	
D1	

UNLESS SPECIFIED: DIMENSIONS ARE IN INCHES. TOLERANCES:		TERRA CORP. ALBUQUERQUE, N.M. U.S.A.	
.XX ± .015 .XXX ± .005 ANGLES ± 1/2°		TITLE: SMART PANEL DISPLAY	
DTL BY: RMN CHK'D: RMN	DATE: 12/22/89	DWG. NO. 9-1100-0089-03	REV D
APPR.: JRF		USED ON:	SHEET 1 OF 1

Smart Panel Display, Page 2 of 2



- NOTES: (UNLESS OTHERWISE SPECIFIED)
- 1. RESISTANCE VALUES ARE IN OHMS.
 - 2. ALL RESISTORS ARE 1/8W, 5%.
 - 3. CAPACITANCE VALUES ARE IN MICROFARADS.
 - *4. DS901 MATES WITH J901. (PIN NUMBERS ARE THE SAME)
 - 5. PLUGS AND JACKS ARE NOT CALLED OUT IN THE LAST USED TABLE. USE ALL SCHEMATICS IN COMPLETE UNIT TO DETERMINE NUMBER USAGE.

DISPLAY BD #1				DISPLAY BD #2			
*LAST USED		NOT USED		*LAST USED		NOT USED	
DS901		R905-		C801		R801-	
LDR901		R907		Q801		R805	
Q904		R909-		R808			
R918		R910		U801			
SW902							
U901							

Mode S Mod Board, Page 1 of 2

REVISIONS				
LTR.	DESCRIPTION	DATE	CHG'D BY	APPR.
A	RELEASE FOR MANUFACTURE	3/16/94	RMM	<i>g/f</i>

NOTES: (UNLESS OTHERWISE SPECIFIED)

1. RESISTANCE VALUES ARE IN OHMS.
2. ALL RESISTORS ARE 1/4W, 5%.
3. CAPACITANCE VALUES ARE IN MICROFARADS.
4. REFER TO SCHEMATIC 9-1100-0005-04 FOR 'E' POINT CONNECTIONS.

UNLESS SPECIFIED: DIMENSIONS ARE IN INCHES. TOLERANCES: .XX ± .015 .XXX ± .005 ANGLES ± 1/2°	TERRA CORP.			ALBQ. N.M. U.S.A.
TITLE: SCHEMATIC, MODE 'S' MOD BOARD				
DR. BY: RMM <i>RMM</i>	DATE: 3/15/94	DWG. NO. 9-1100-0128-02	REV A	
CHK'D: <i>JTW</i>	USED ON:		SHEET 1 OF 1	
APPR.: <i>g/f</i>				

Mode S Mod Board, Page 2 of 2

