

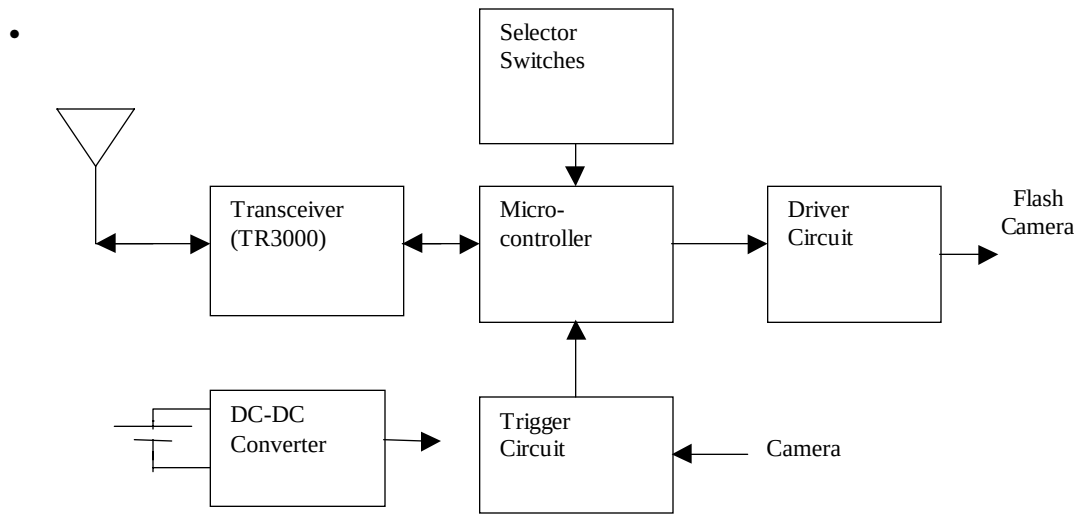


Fig 1. Circuit Block Diagram

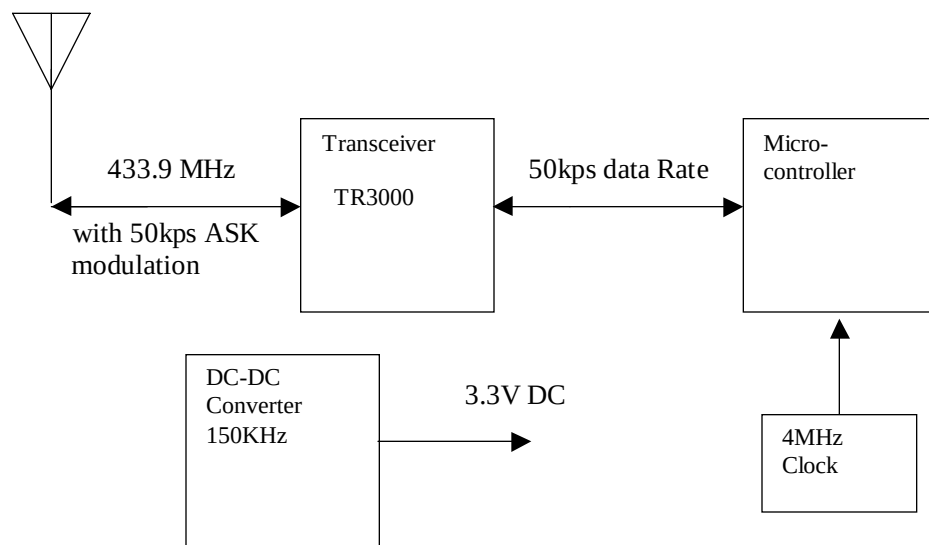


Fig 2. Frequency and Signal Path Block Diagram