

SECTION 2.1033(b)

SECTION 2.1033(b) (2a). For Part 15 a statement describing how the device operates.

The CFS8DL5805BDV-1 uses a common antenna matched to 50 ohms by C29 / C7 etc., this antenna is switched from the transmitter to the receiver by solid state switch SW5 under control of the microprocessor U10. All receive functions, including L.O. are performed by the single chip receiver U11. the L.O.'s FDE is crystal YL4 ($20.89375 \text{ Mhz} \times 16 = 334.3 \text{ Mhz}$ low side injection) All transmit functions are performed by the single chip transmitter U17 etc., who's FDE is YL6 ($10.78125 \text{ Mhz} \times 32 = 345 \text{ Mhz}$) Again it is under control of microprocessor U10. All audio functions are handled by U5, etc.