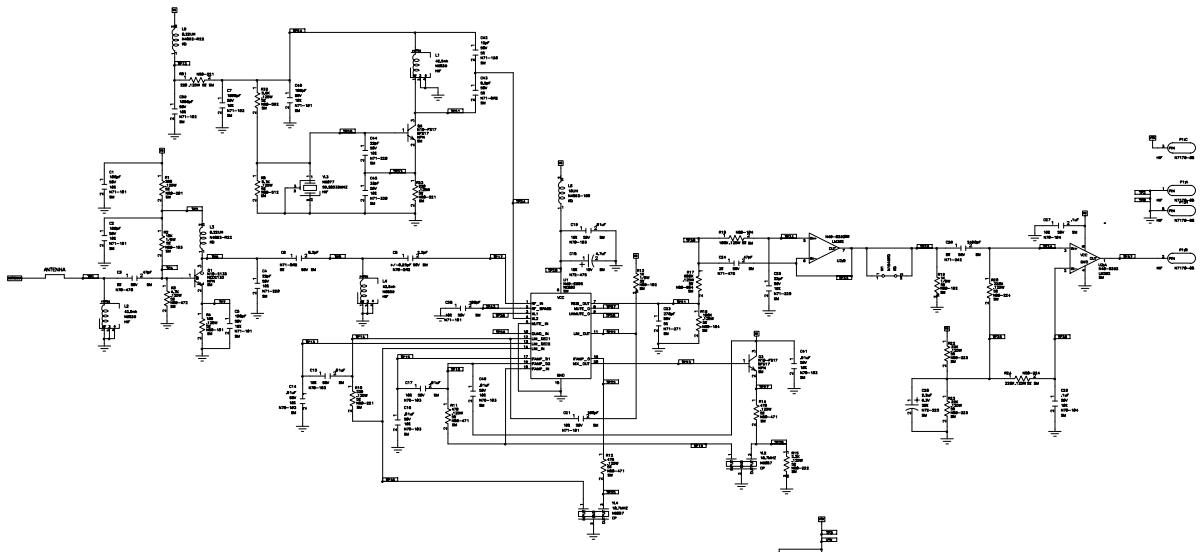
ASSY REV	DESCRIPTION	DATE	APPROVED
A	ECN27228 CHN. PCB TO V4L C42 WAS 8PF AND C43 WAS 15PF.	07-14-03	RB.



APPROVALS	DATE
PROJECT ENG.	
ELECTRICAL ENG.	
MFG. ENG.	
IN CIRCUIT TEST ENGR.	
FUNCTIONAL TEST ENGR.	
CREATED BY: L.STEINSTEIN	3/31/03

ADEMCO
165 EILEEN WAY
SYOSSET, NEW YORK 11791

PCB (REF)	N6739V6 REV.A
TITLE	SCHEMATIC
DRAWING NUMBER	SA5827BDR-1
COMPUTER FILE NAME	SA5827BDR-1.SCH
DATE:	3/31/03
SHEET	1 OF 1