

LIST OF EXHIBITS

CFS8DL6128WL

- 1) Description of the 6128WL
- 2) Circuit description and schematic
- 3) Installation instruction including FCC compliance statement
and FCC user's warning
- 4) Photographs showing FCC ID label and position
- 5) Report of measurements
- 6) Test equipment list

EXHIBIT 1

CFS8DL6128WL

Functional Description.

The **6128WL** fixed receiver is part of Ademco's wireless 5800 alarm system.
It is used in conjunction with portable security transmitters.
This allows remote control of the alarm system.

EXHIBIT 2

Circuit Description.

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The 6128WL is constructed on a single PCB.

The PCB contains microcontrollers U4 and U8, LED indicators CR12 and CR13, sounder, keypad switches, Low Noise Amplifier Q1, IF circuitry U1, Local Oscillator Y1 (refer to circuit schematic for component designations).

The receiver is a superhet with a single intermediate frequency at 10.7MHz. Q1 etc. is the low noise amplifier which is connected to the PCB mounted antennas. Diodes CR1/CR2 under the control of a microcontroller are periodically switched to provide system antenna diversity. The IC U1 includes a balanced mixer which converts the incoming signal down from 345MHz to 10.7MHz. This IC also includes the required IF gain and detected output. FL1 and FL2 are ceramic IF filters. IC U9 performs video filtering and processing and provides a data signal. The local oscillator (Y1) is a 355.7 Mhz SAW oscillator which has its active element contained within U1.