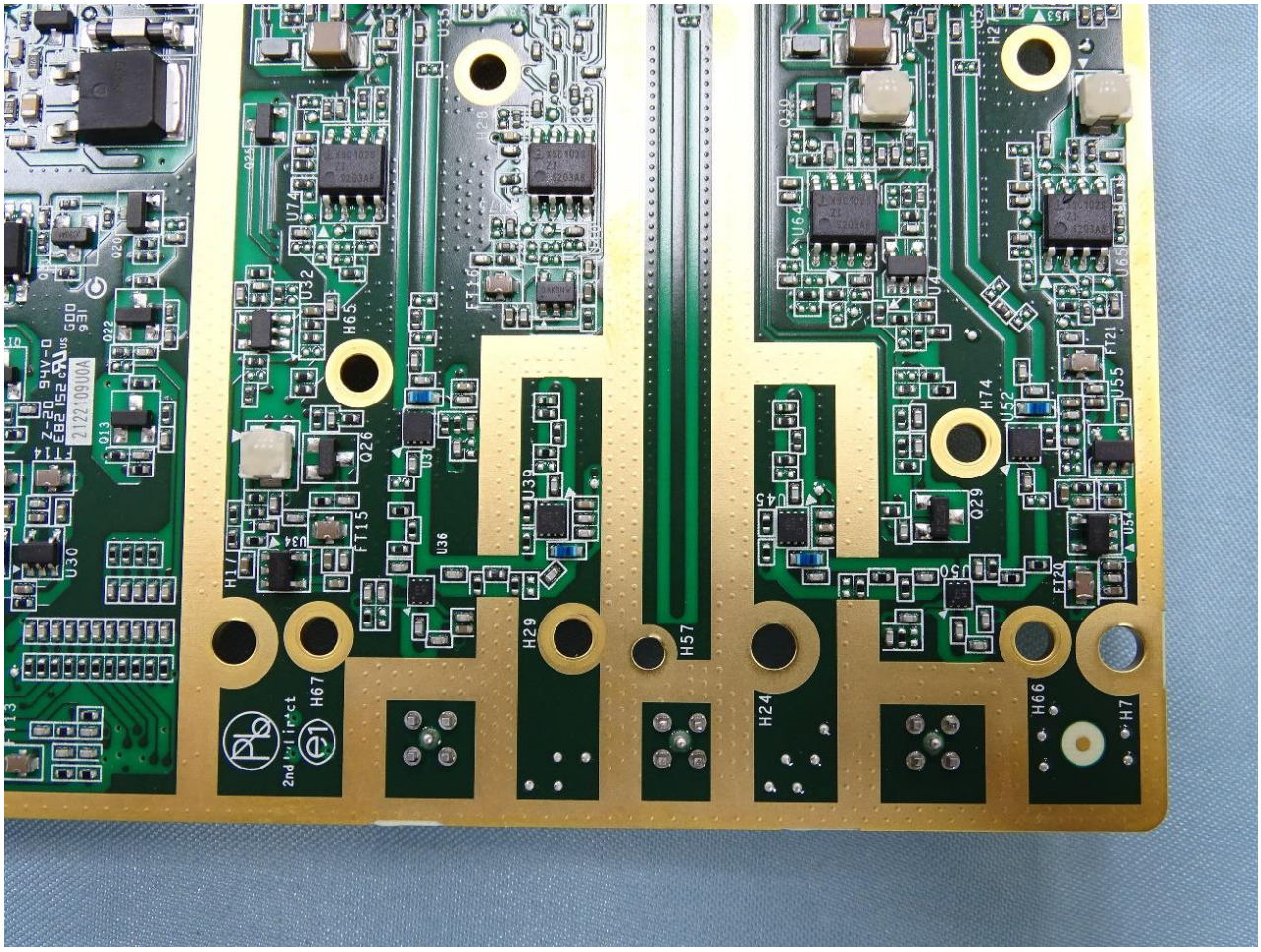
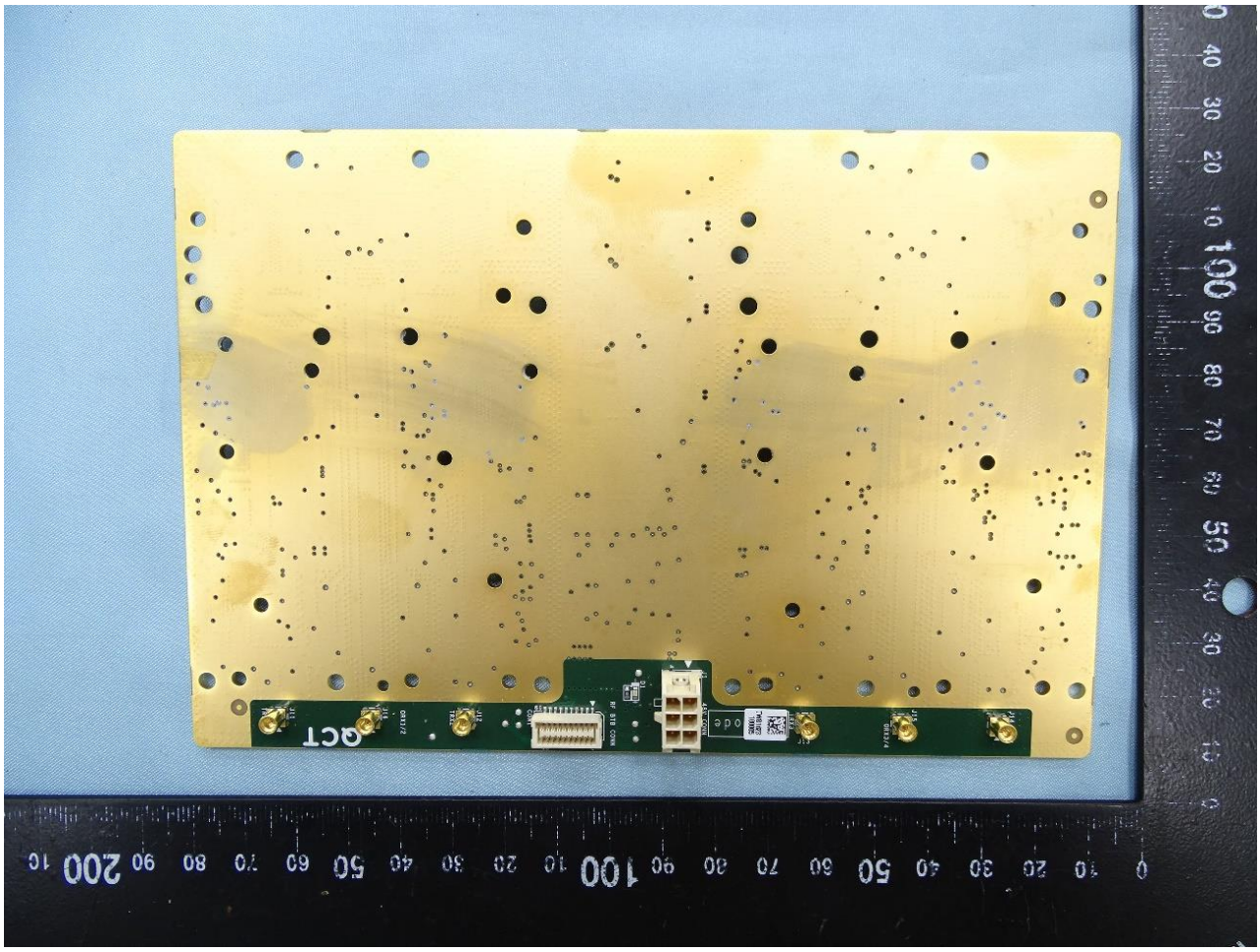
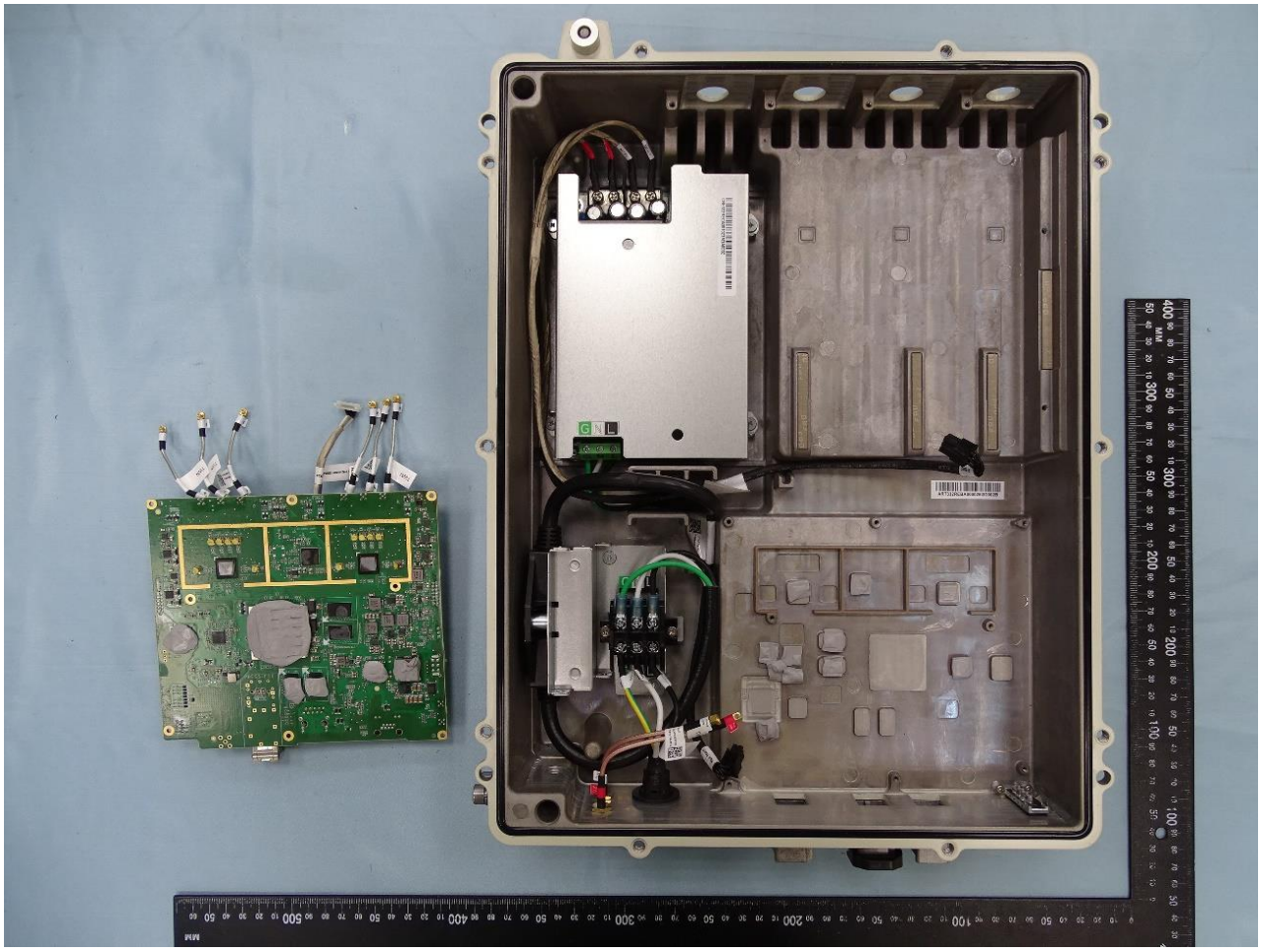
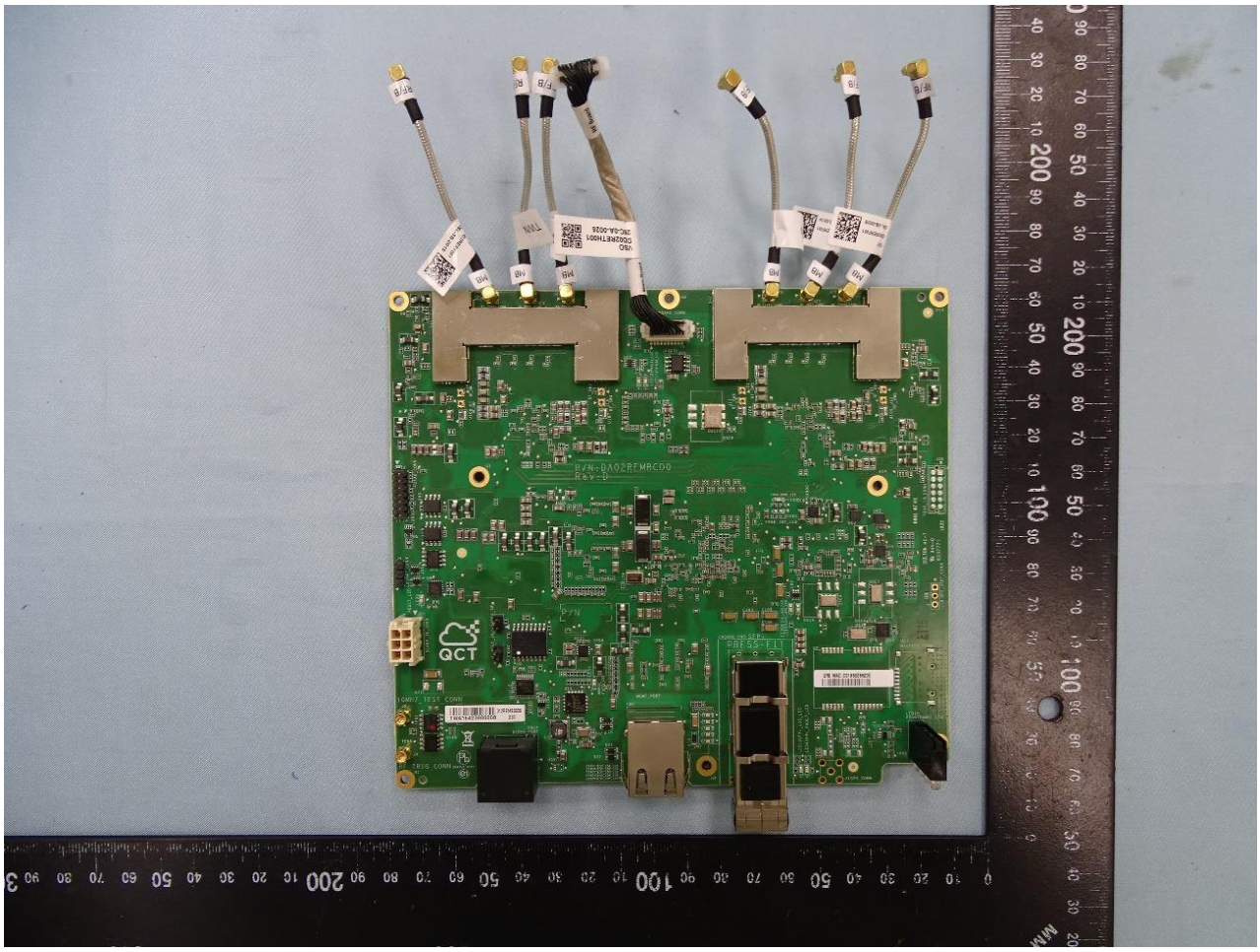
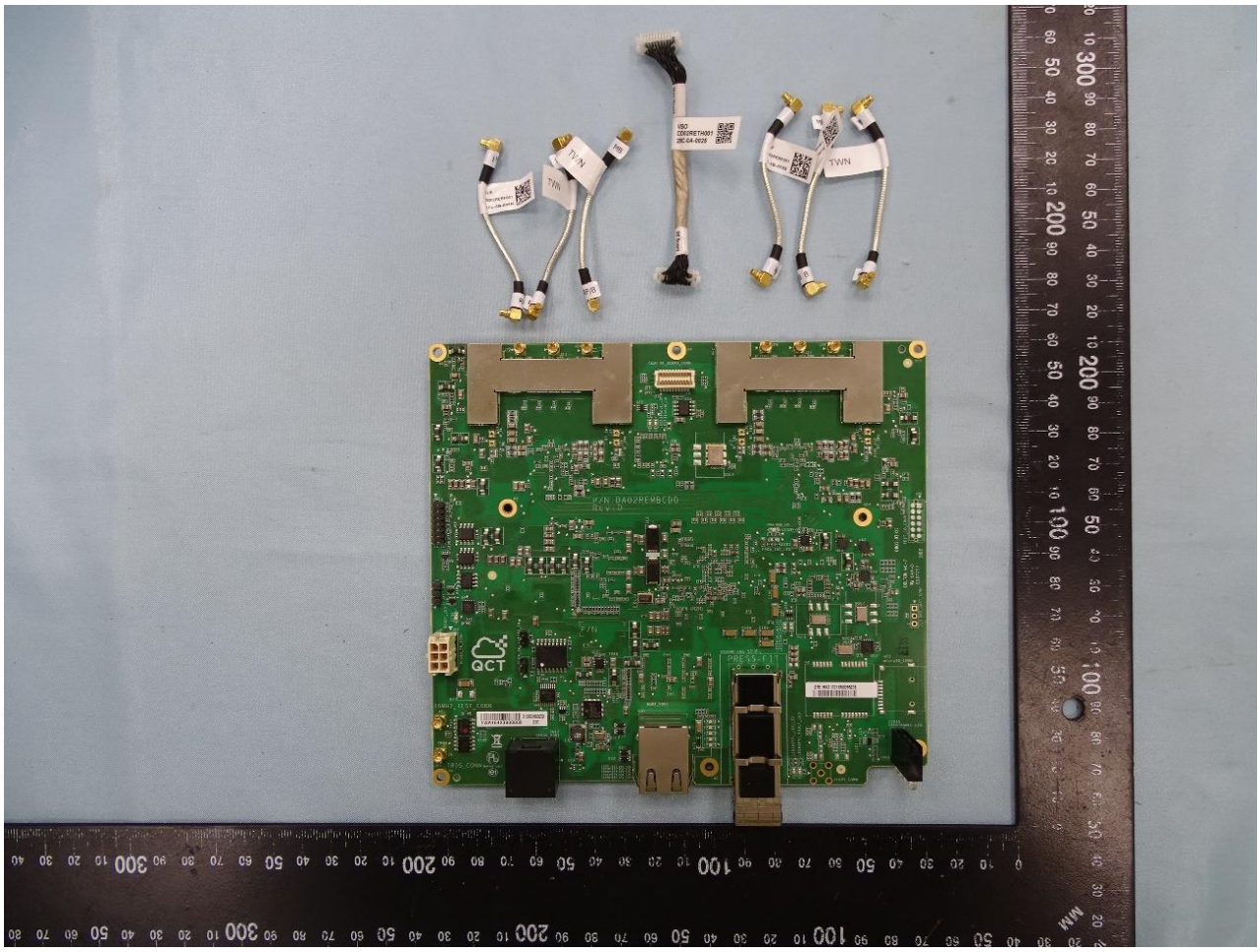
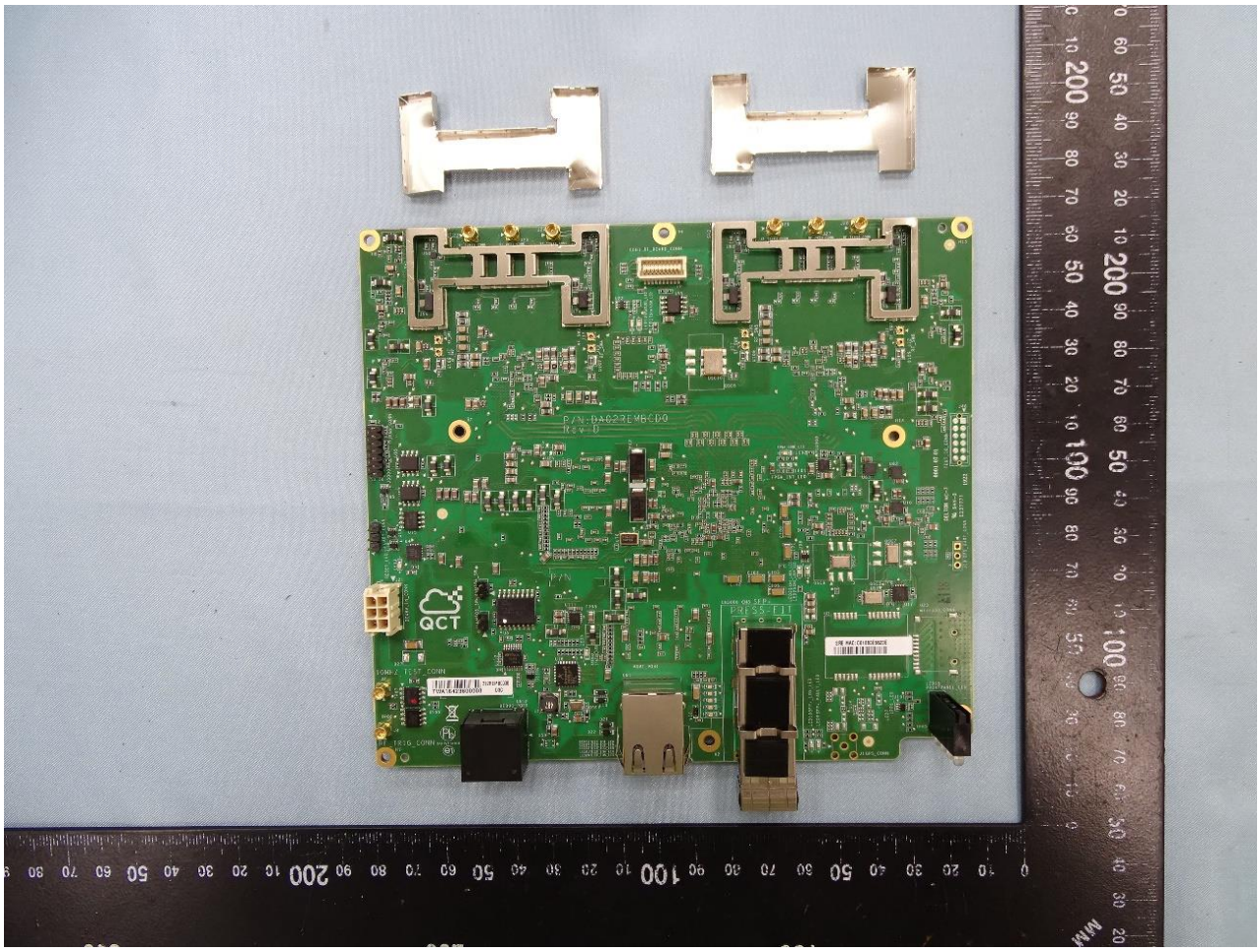

H67
H57
H29
H24
H66
H7
U30
U39
U38
U50
U52
U55
Q26
Q29
FI15
FI20
FI21

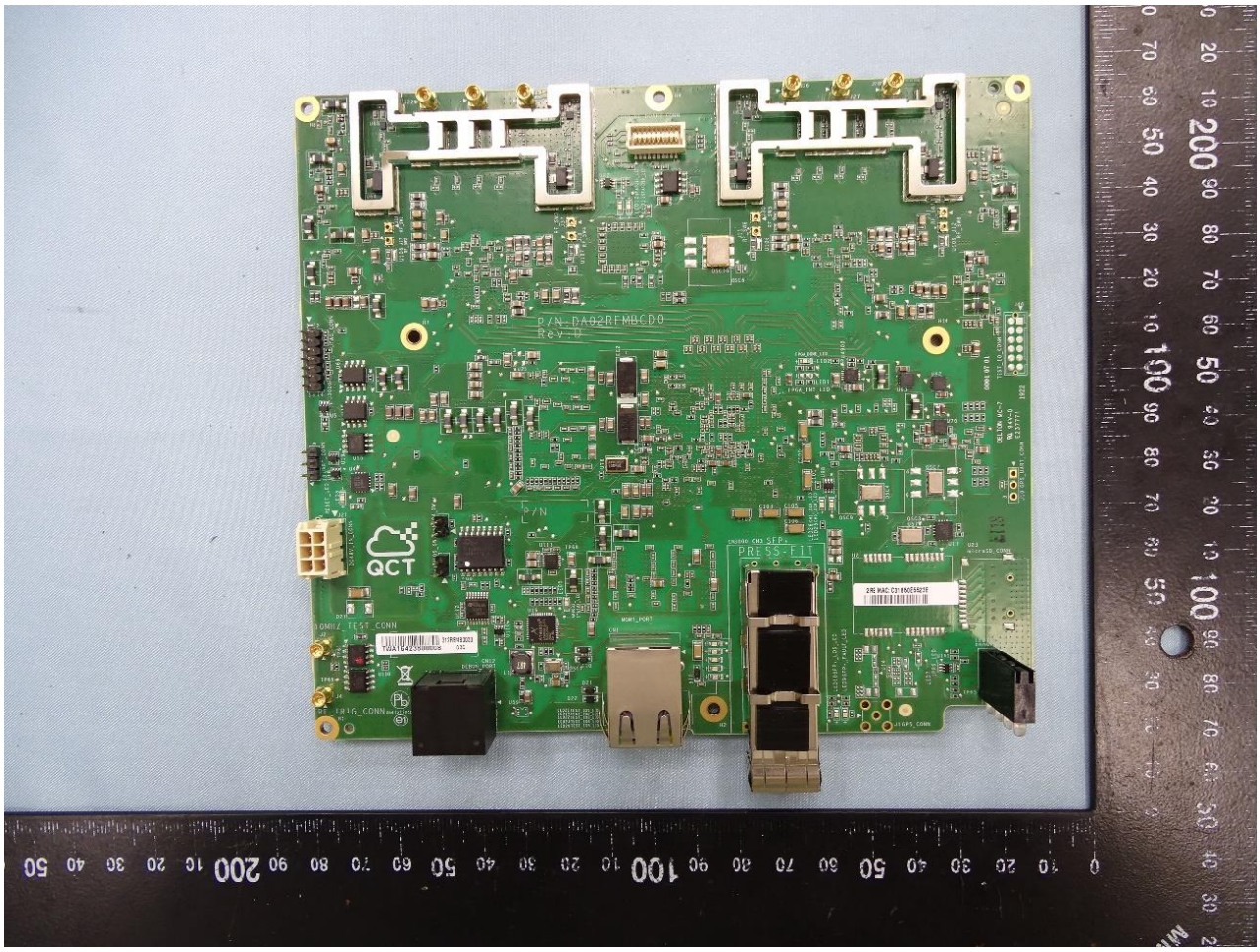


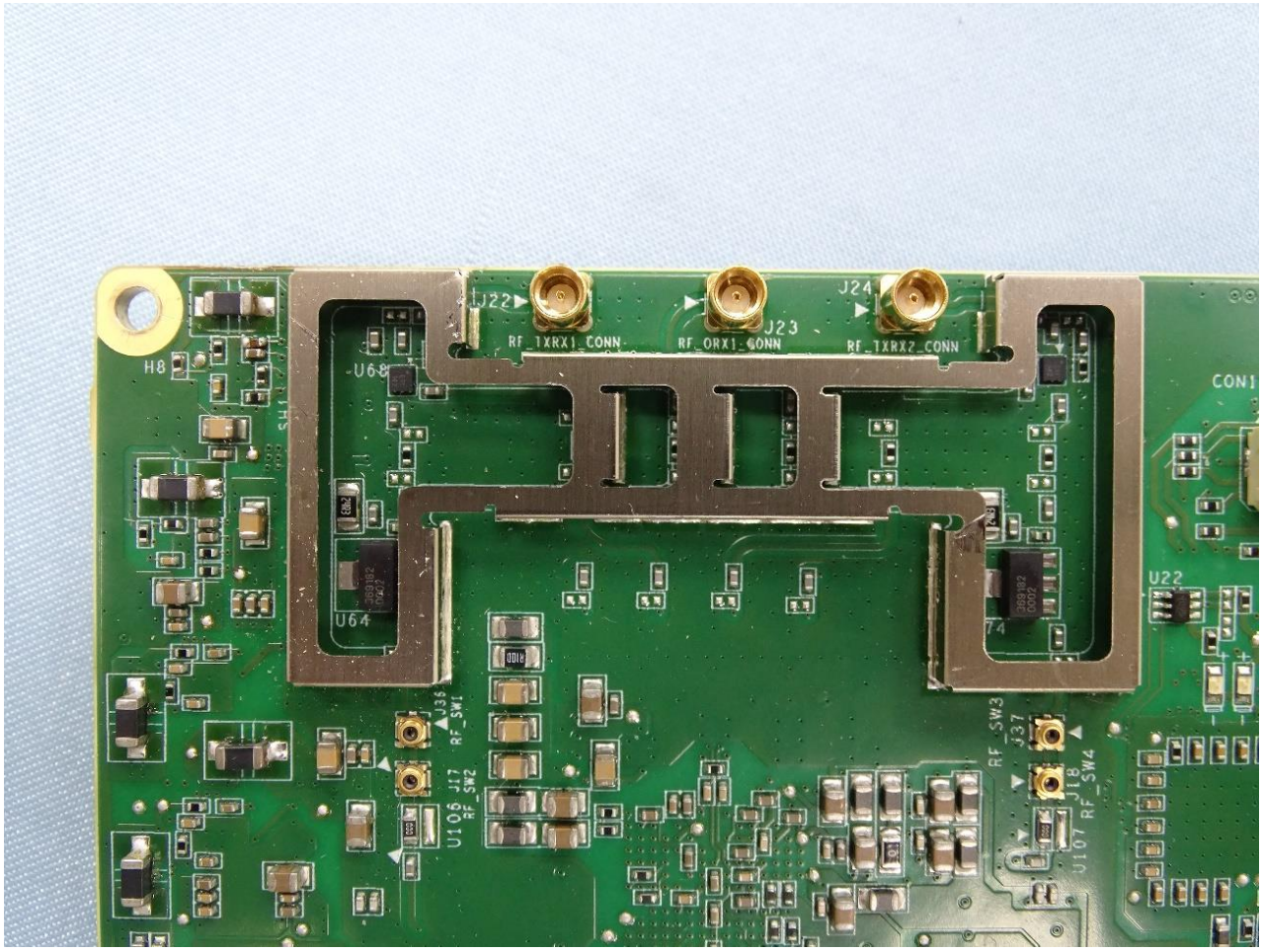


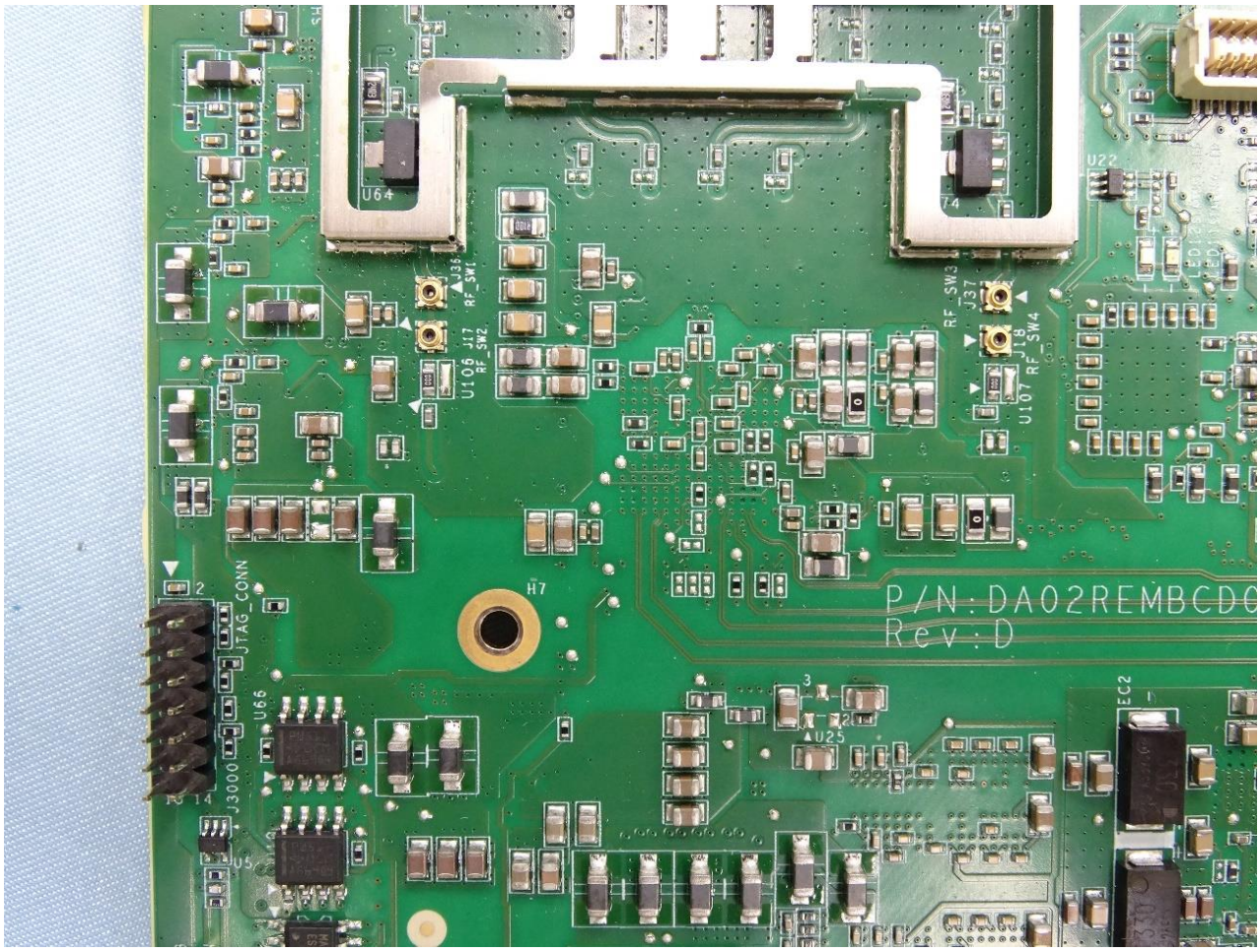


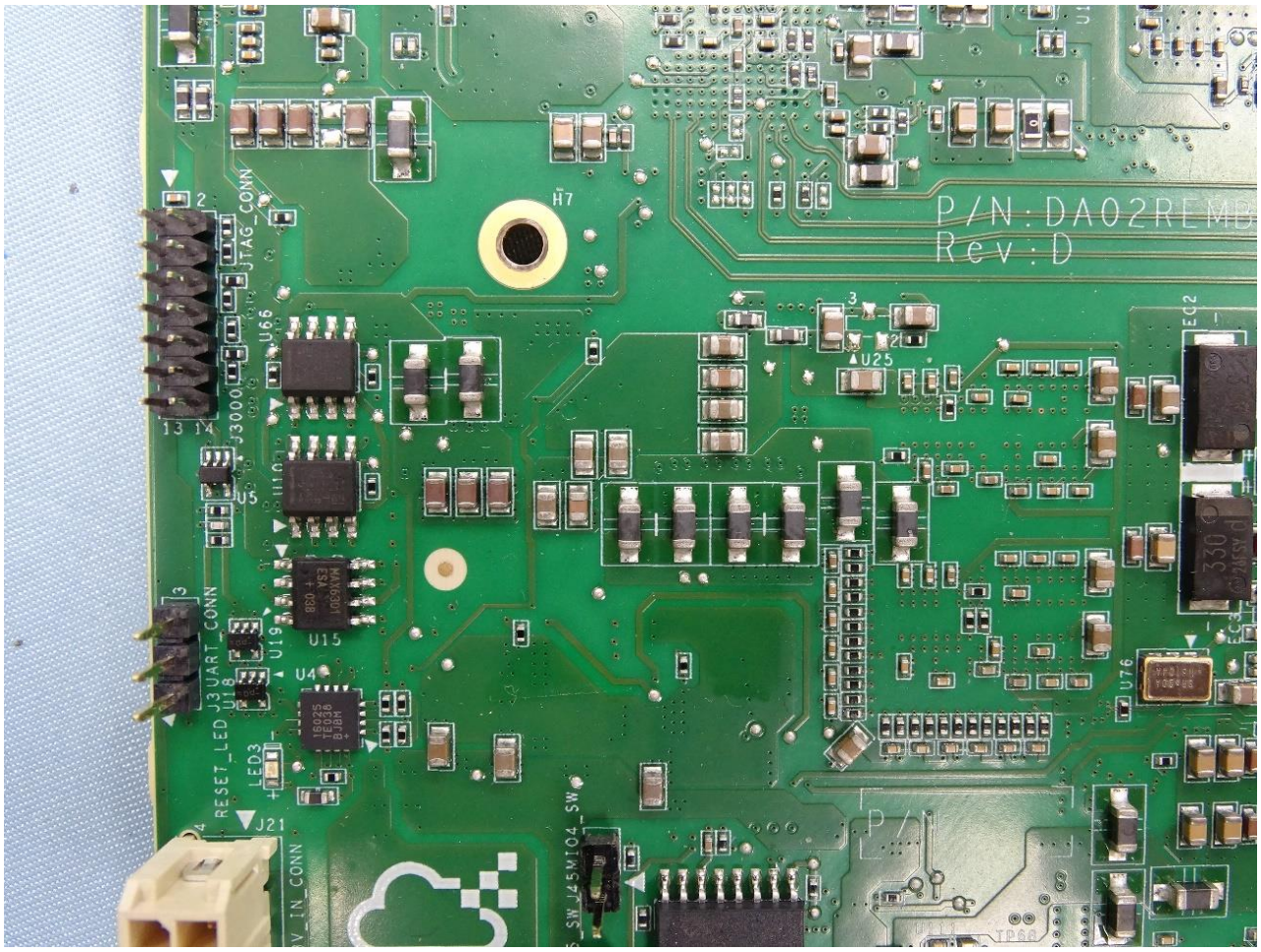

QCT

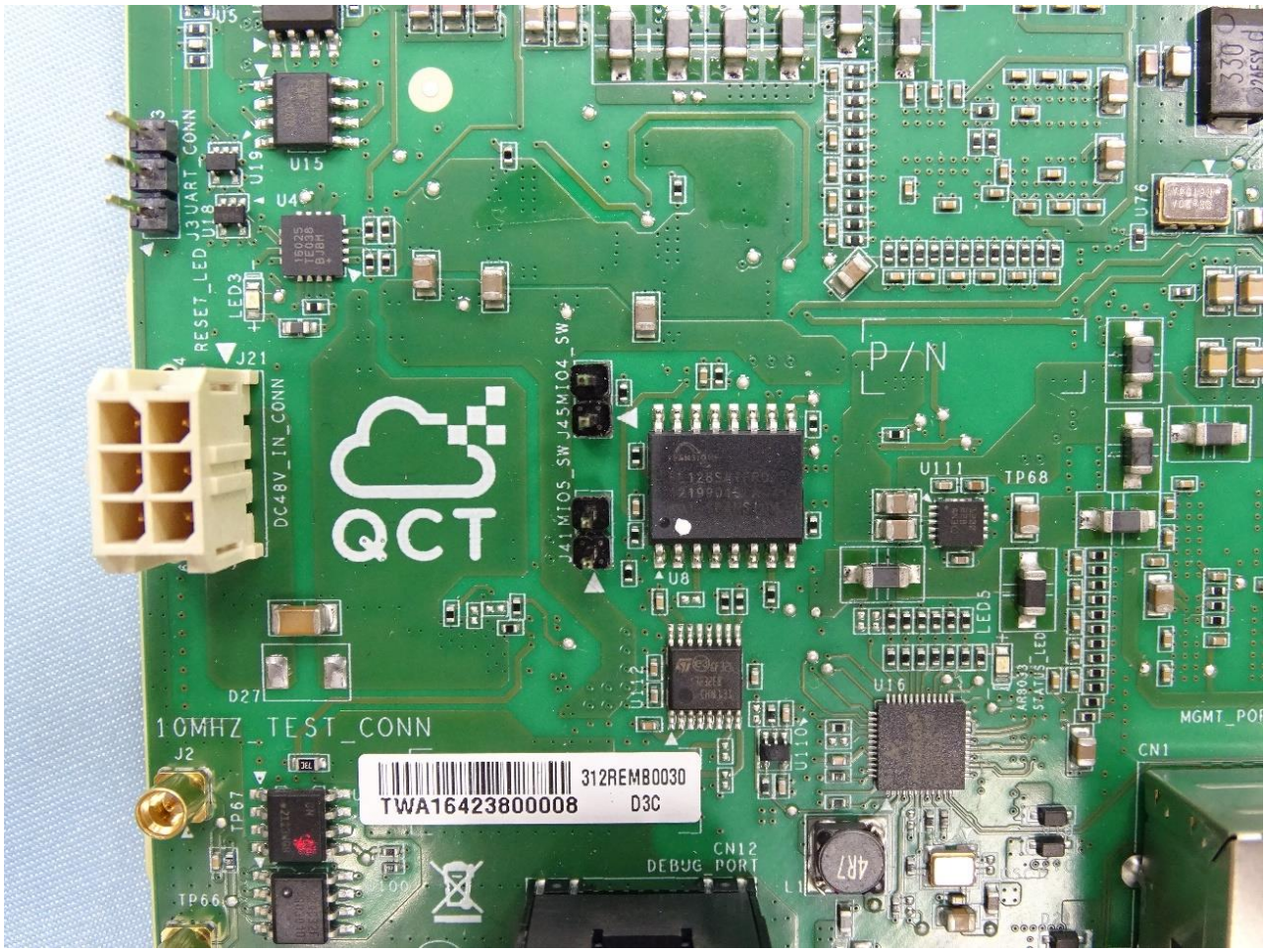

QCT


QCT
PRESS-FIT

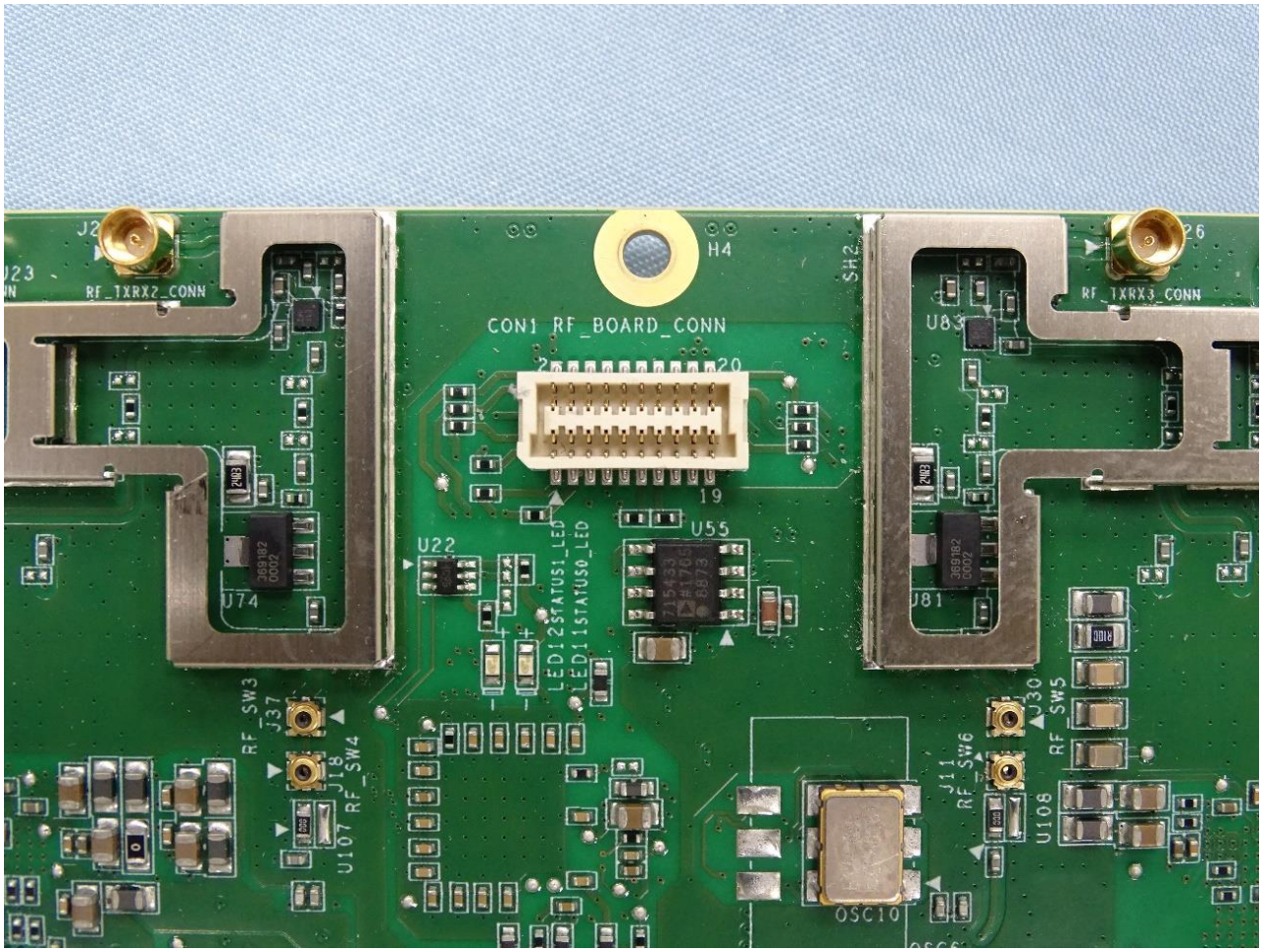

J22
RF_TXRX1_CONN
J23
RF_ORX1_CONN
J24
RF_TXRX2_CONN
H8
U68
U64
U22
CON1
J36
RF_SW1
J17
RF_SW2
U106
RF_SW3
J37
J18
RF_SW4
U107

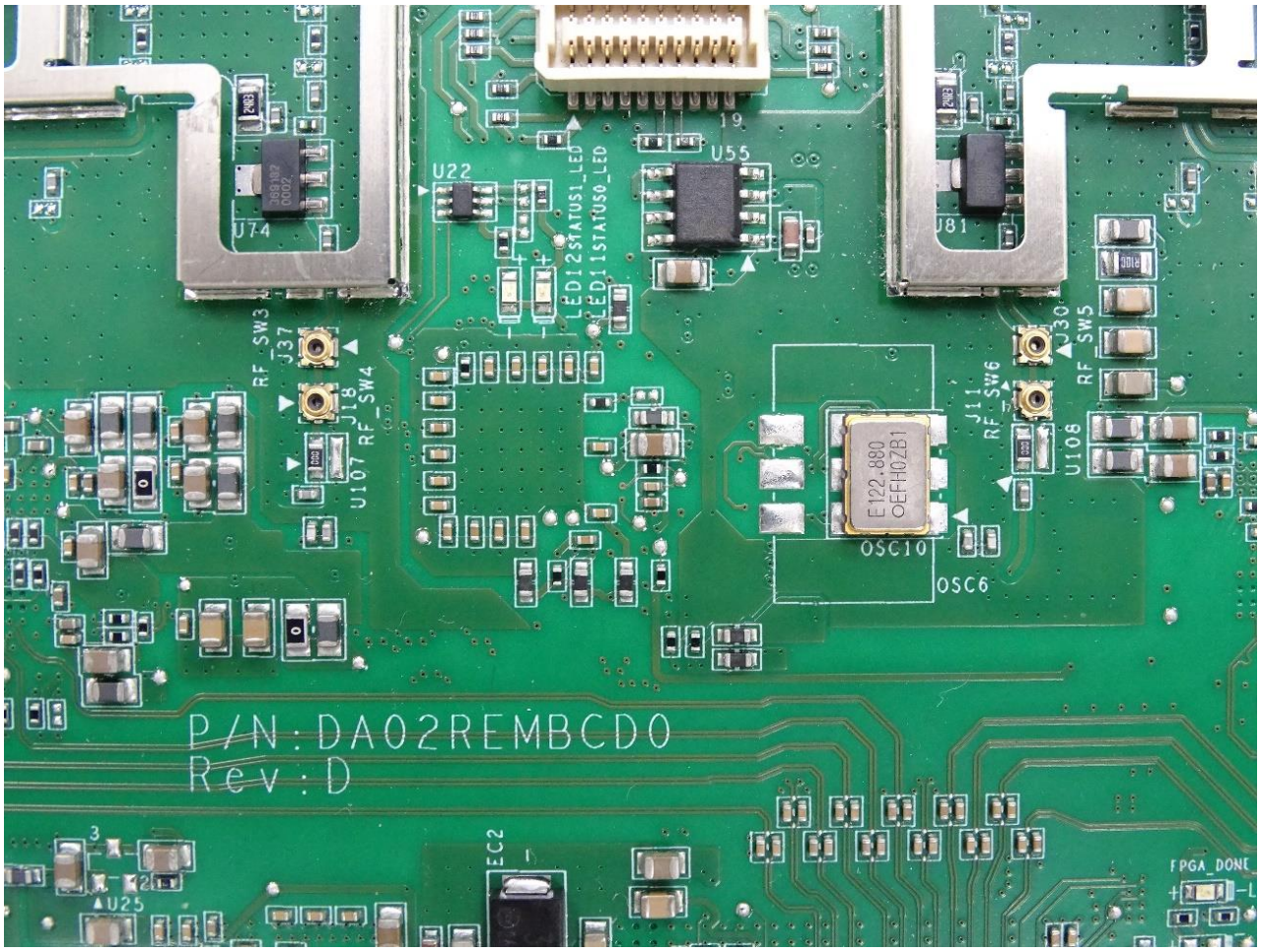

U64
U22
RF_SW1
RF_SW2
RF_SW3
RF_SW4
U106
U107
H7
P/N:DA02REMBCD0
Rev:D
JTAG_CONN
J3000
U66
U25
EC2

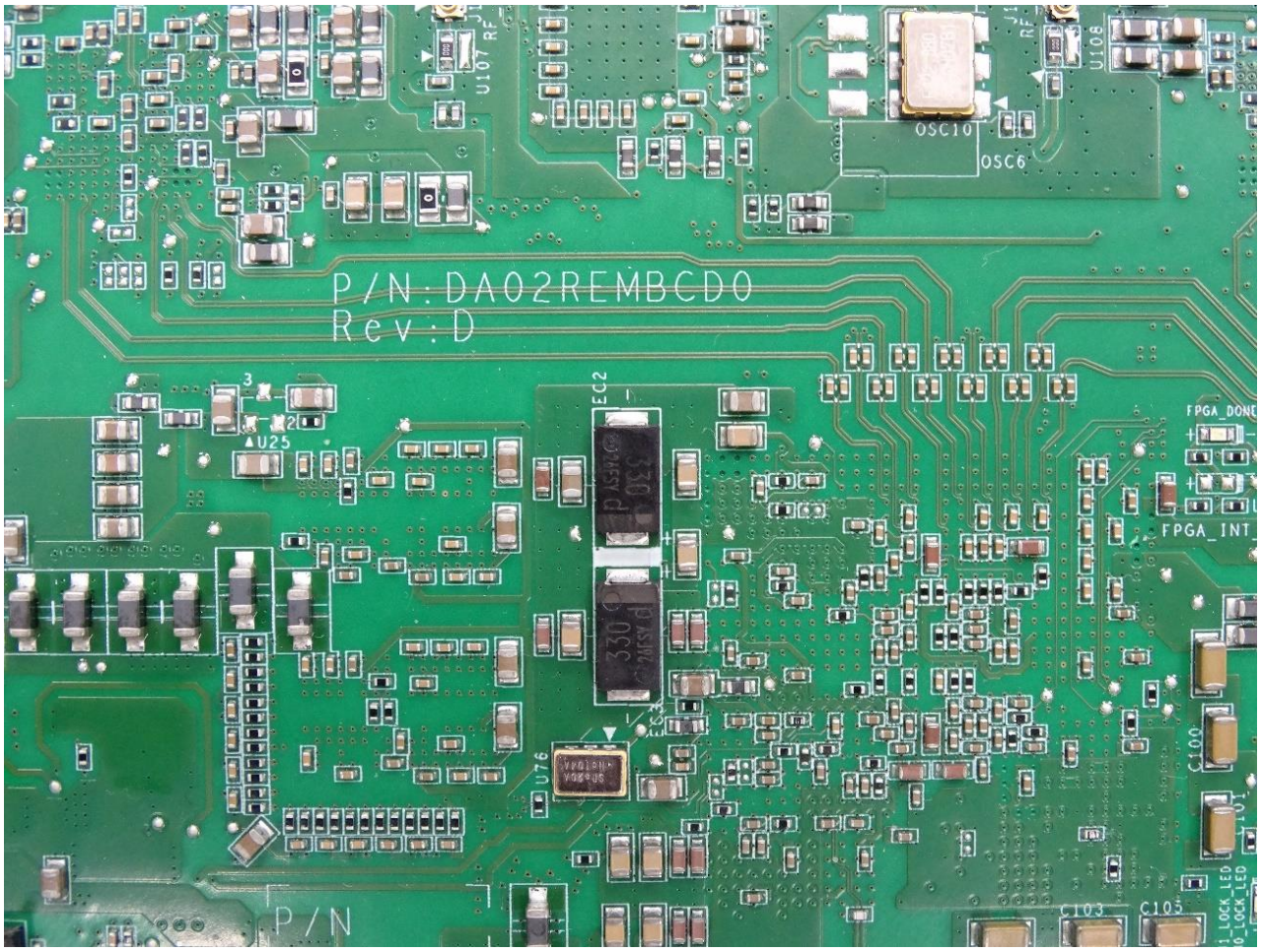

P/N:DA02REMB
Rev:D
JTAG_CONN
J3000
U66
U5
U15
U4
U19
U18
J3
UART_CONN
RESET_LED
LED3
J21
U25
U76
H7
330

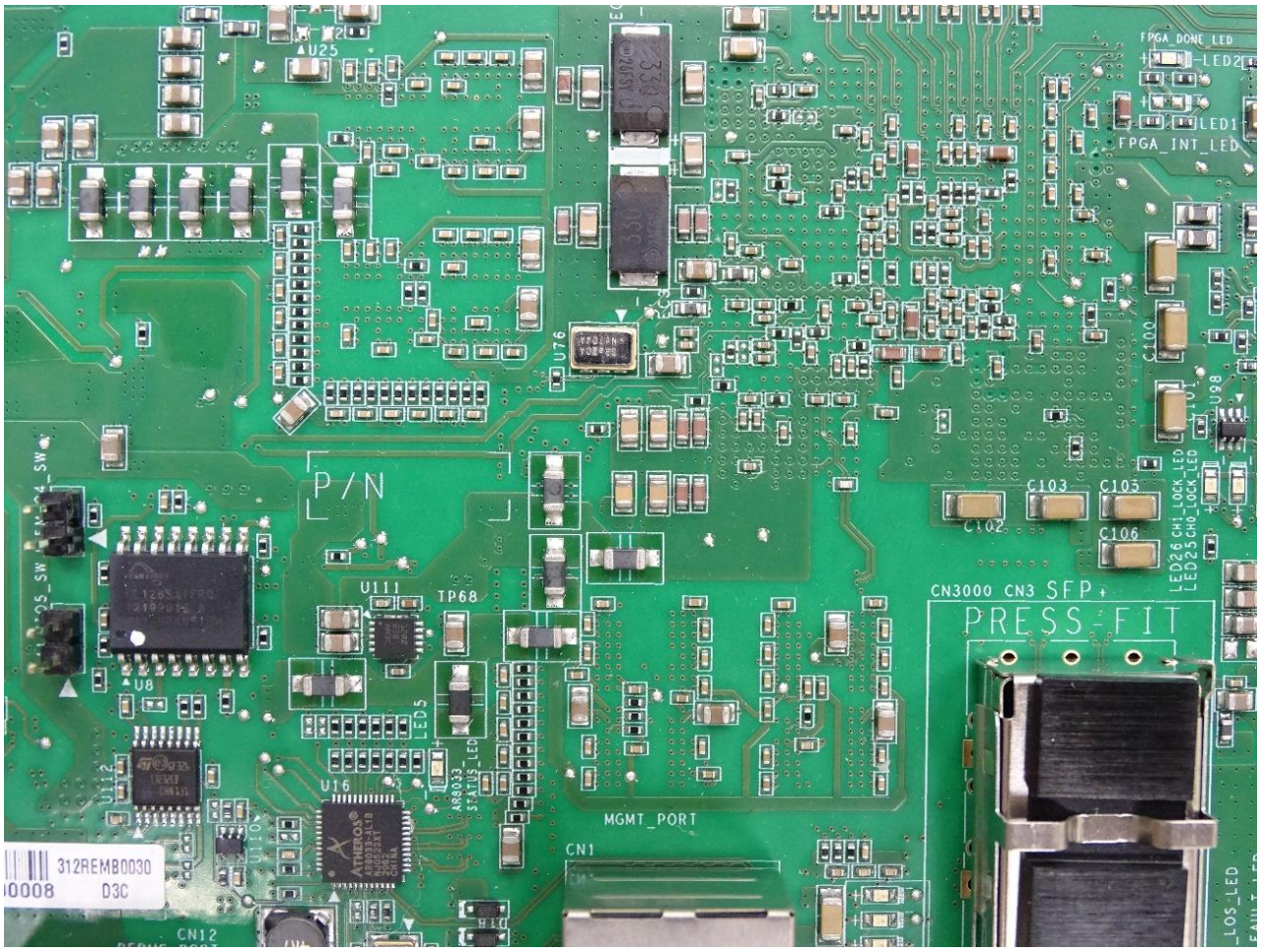

U5
U15
U4
U19
U18
J3_UART_CONN
RESET_LED
LED3
J21
DC48V_IN_CONN
J41_MIO5_SW
J45_MIO4_SW
P/N
U111
TP68
U8
LED5
U12
U16
D27
10MHZ_TEST_CONN
J2
TP67
TP66
312REMB0030
TWA16423800008
D3C
CN12
DEBUG_PORT
4R7
CN1
MGMT_POR

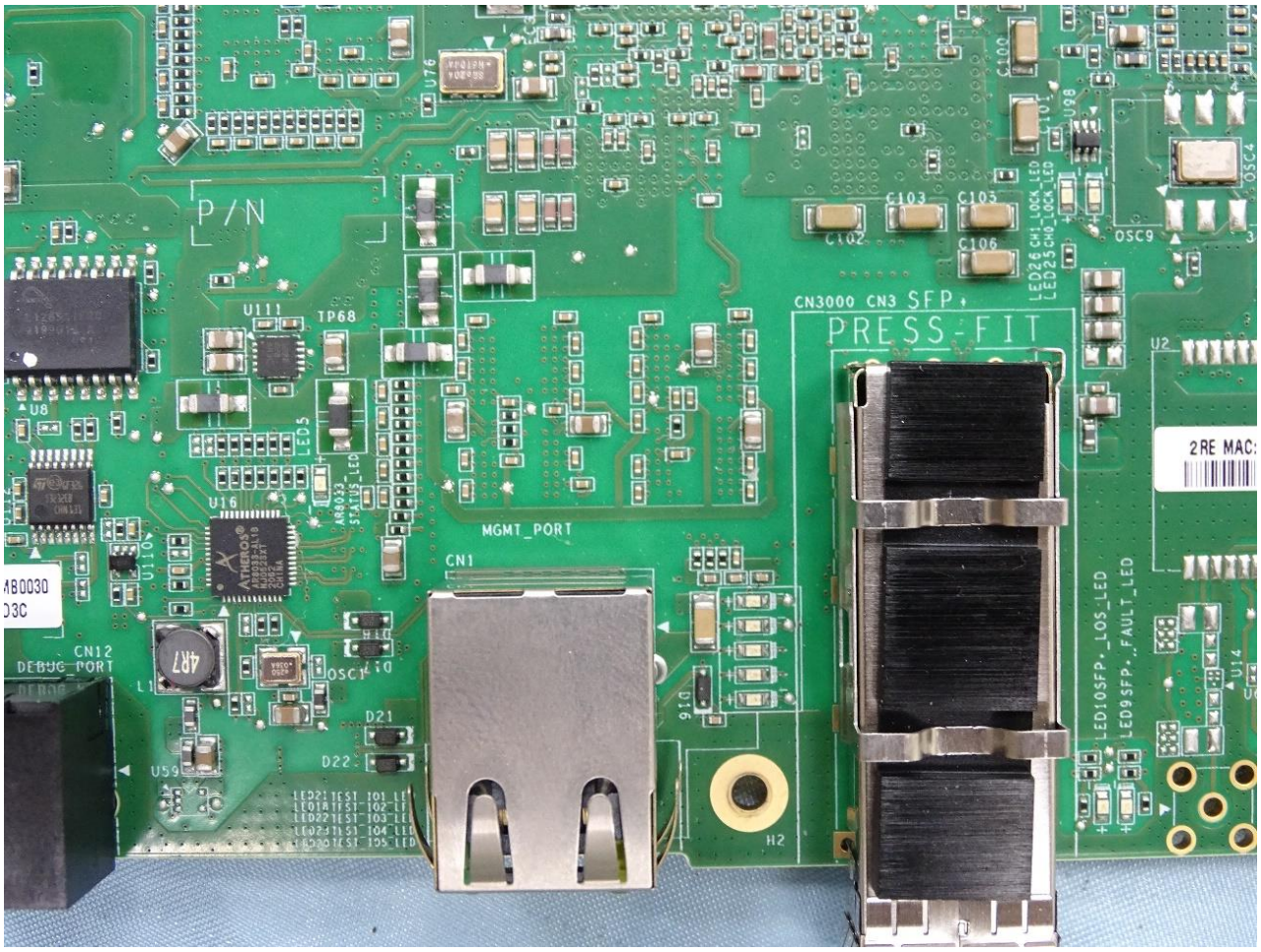

QCT
P/N
DC48V_IN_CONN
J41MIOS_SW_J45MIO4_SW
U111
TP68
U8
LED5
U112
U16
MGMT_PORT
CN1
D27
10MHZ_TEST_CONN
J2
312REMB0030
TWA16423800008
D3C
TP67
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DEBUG_PORT
TP66
U100
J4
RF_TRIG_CONN
L1
OSC
D21
D22
U59

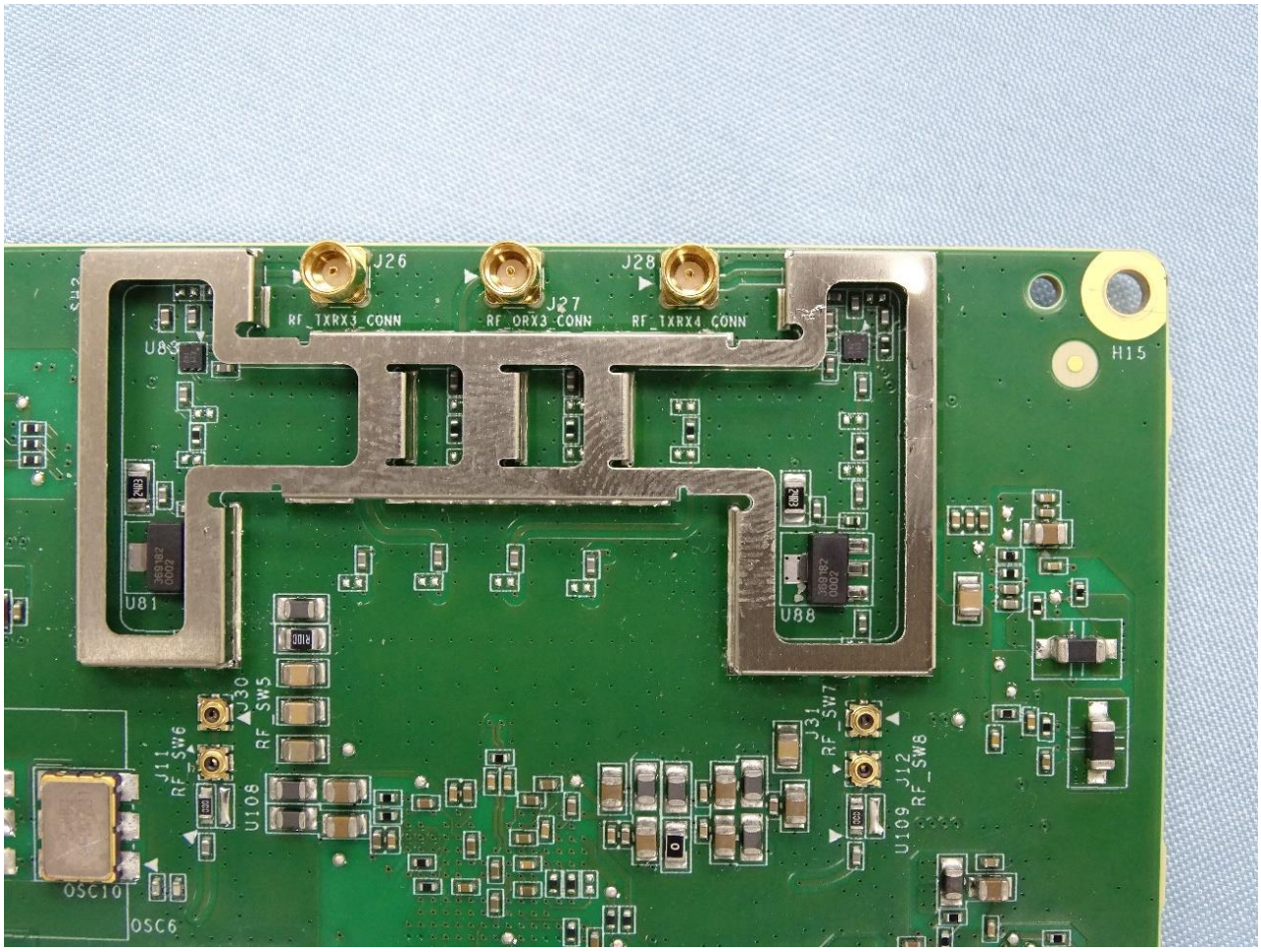

RF_TXRX2_CONN
H4
CON1 RF_BOARD_CONN
RF_TXRX3_CONN
U74
U22
U55
U83
U81
RF_SW3
RF_SW4
RF_SW5
RF_SW6
U107
U108
OSC10

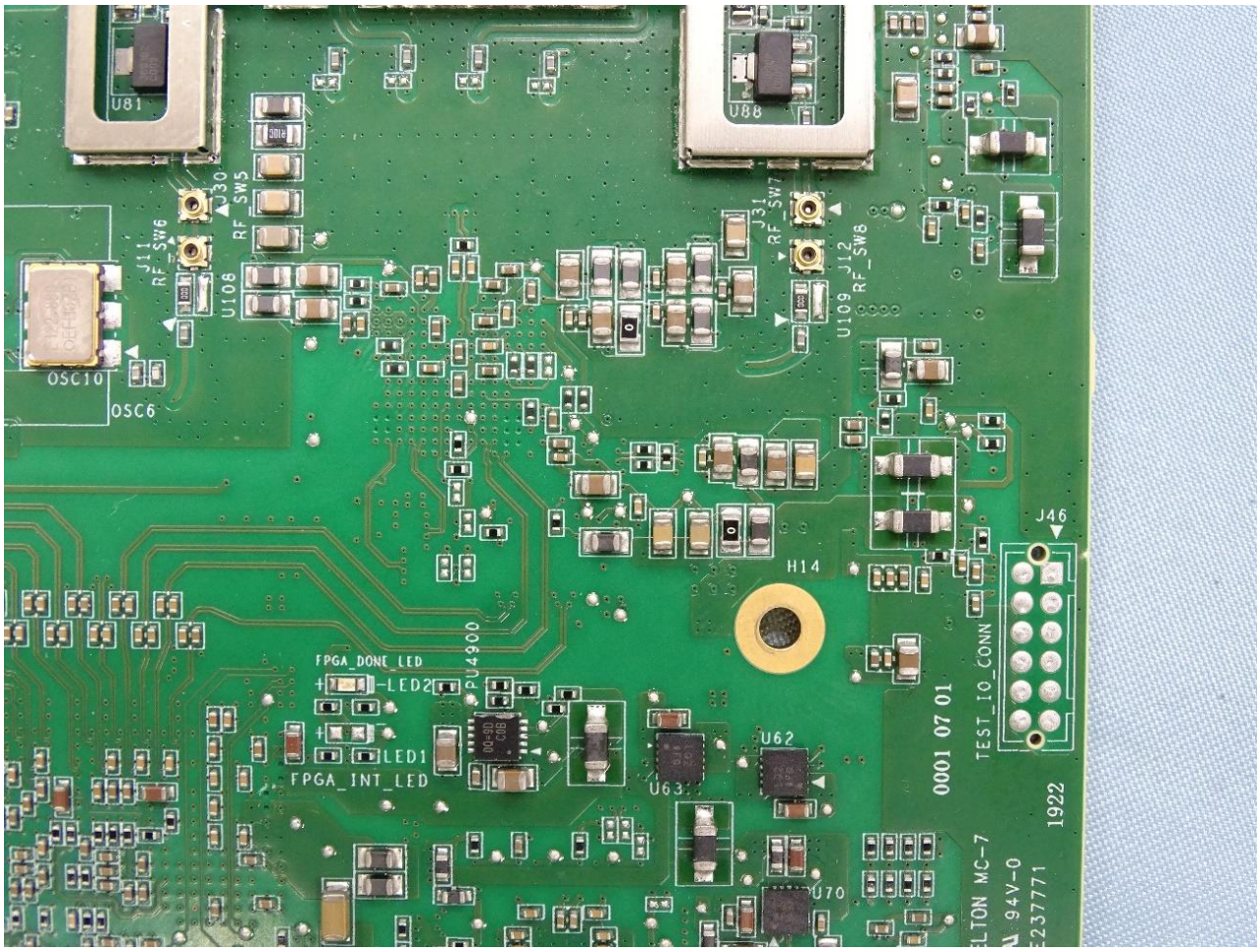

U74
U22
U55
U81
LED2_STATUS1_LED
LED1_STATUS0_LED
RF_SW3
J37
RF_SW4
J18
U107
RF_SW5
U30
RF_SW6
J11
U108
E122.880
OEFH07B1
OSC10
OSC6
P/N:DA02REMBCD0
Rev:D
EC2
U25
FPGA_DONE


P/N:DA02REMBCD0
Rev:D
U107 RF
U108
OSC10
OSC6
U25
EC2
330
FPGA_DONE
FPGA_INT
C100
C103
C105
P/N


FPGA_DONE_LED
LED2
LED1
FPGA_INT_LED
P/N
U111
TP68
U8
U16
MGMT_PORT
CN1
CN3000 CN3 SFP+
PRESS-FIT
C102
C103
C105
C106
312REMB0030
D3C
CN12


P/N
U111
TP68
U8
U16
MGMT_PORT
CN1
CN12
DEBUG_PORT
CN3000 CN3 SFP+
PRESS-FIT
D21
D22
U59
H2
U2
OSC9
OSC4
C102
C103
C105
C106
U98
2RE MAC:


J26
J27
J28
RF_TXRX3_CONN
RF_ORX3_CONN
RF_TXRX4_CONN
H15
U83
U81
U88
J30
RF_SW5
J11
RF_SW6
U108
J31
RF_SW7
J12
RF_SW8
U109
OSC10
OSC6


U81
U88
J30
RF_SW5
J11
RF_SW6
U108
J31
RF_SW7
J12
RF_SW8
U109
OSC10
OSC6
J46
H14
FPGA_DONE_LED
LED2
LED1
FPGA_INT_LED
U62
U63
U70
TEST_IO_CONN
0001 07 01
1922