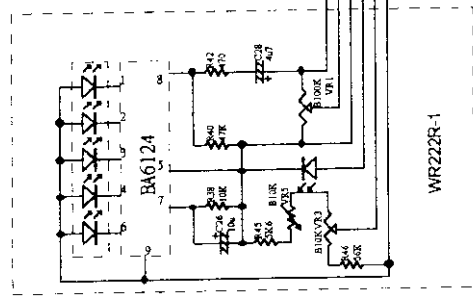
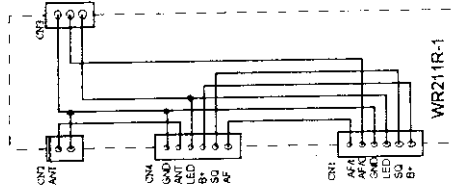
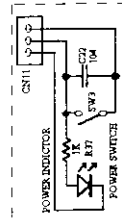
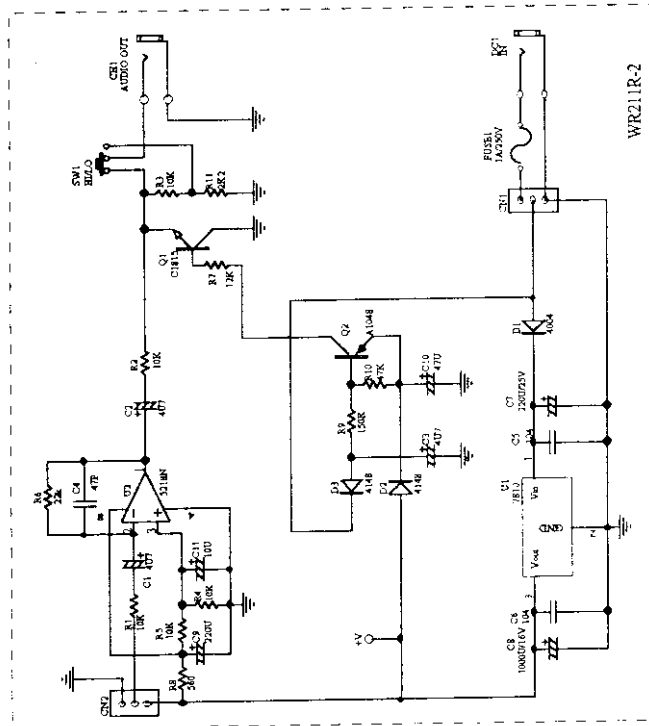


EXHIBIT D

Circuit Diagram



WR 211R SCHEMATIC DIAGRAM			
Rev	Number	Revision	
A3	06.07.97		
Date	31 Dec 1997	Sheet of	708
By	CIA VERNER, JINRIR, SKE	Drawn By	