

Figure 76: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 16QAM_TM3.2, Low Channel, TX Port 12

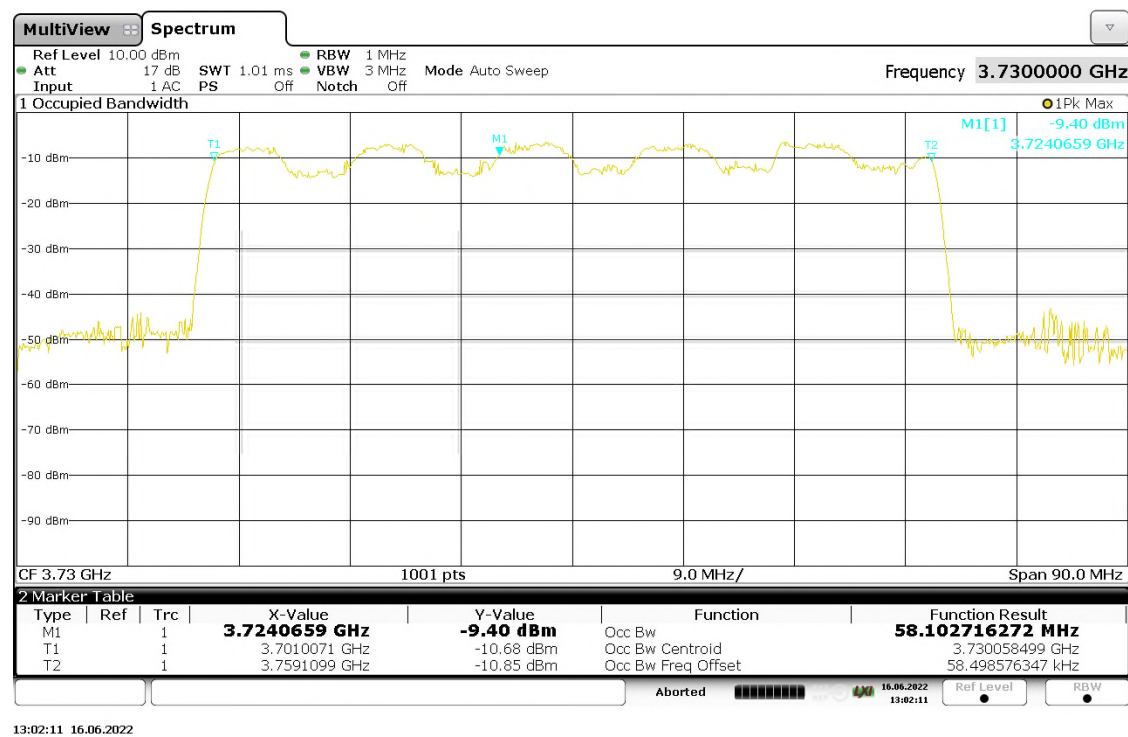
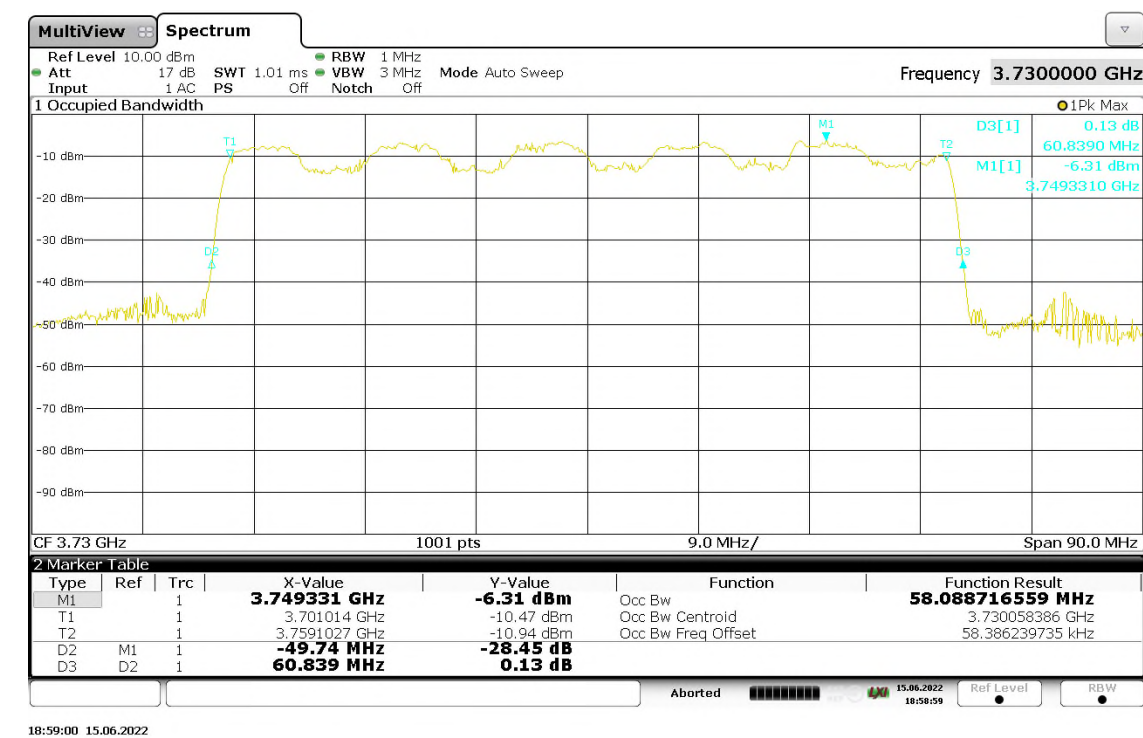


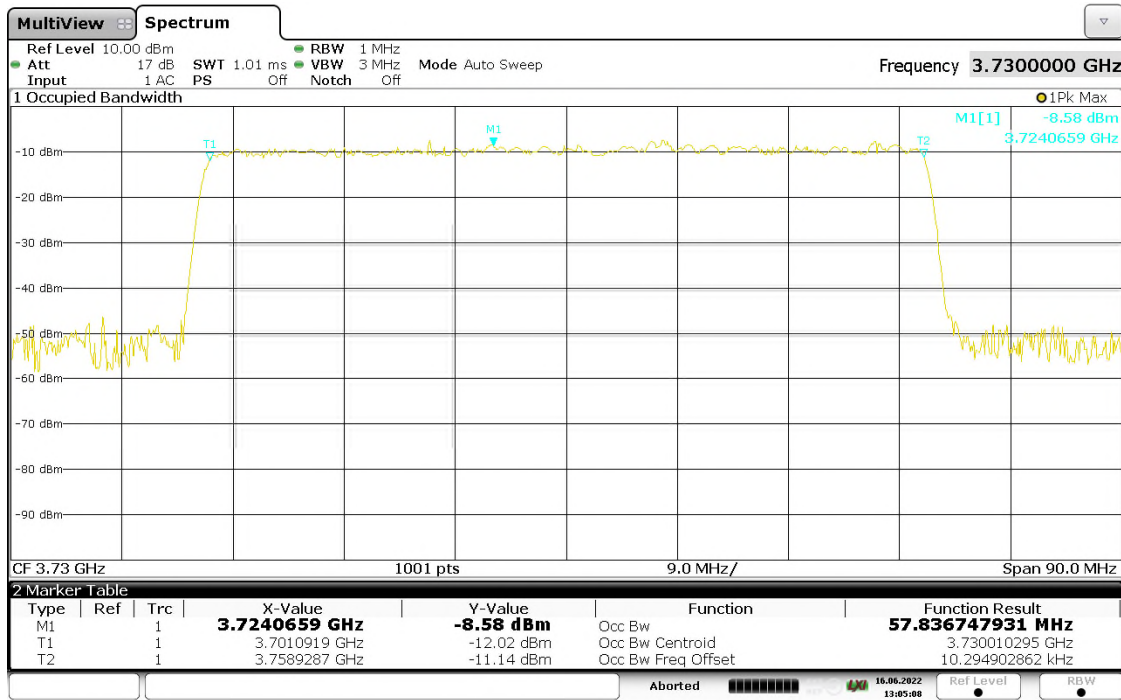
Figure 77: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 16QAM_TM3.2, Low Channel, TX Port 28



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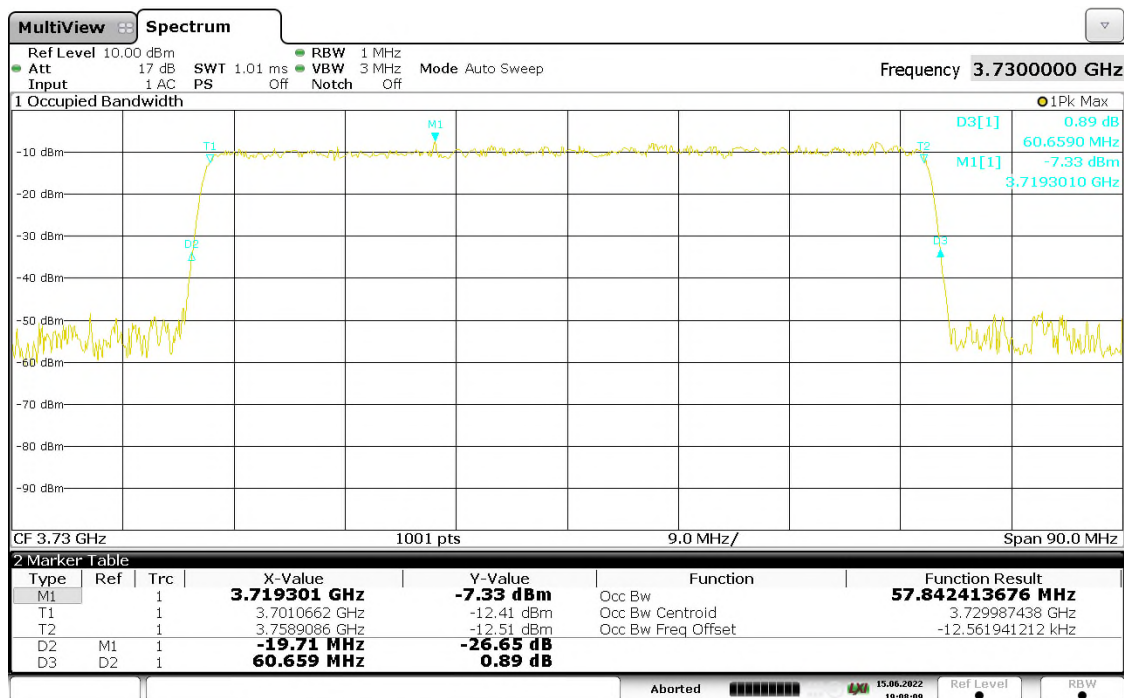
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Figure 78: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 64QAM_TM3.1, Low Channel, TX Port 12



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Figure 79: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 64QAM_TM3.1, Low Channel, TX Port 28



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Figure 80: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 256QAM_TM3.1a, Low Channel, TX Port 12

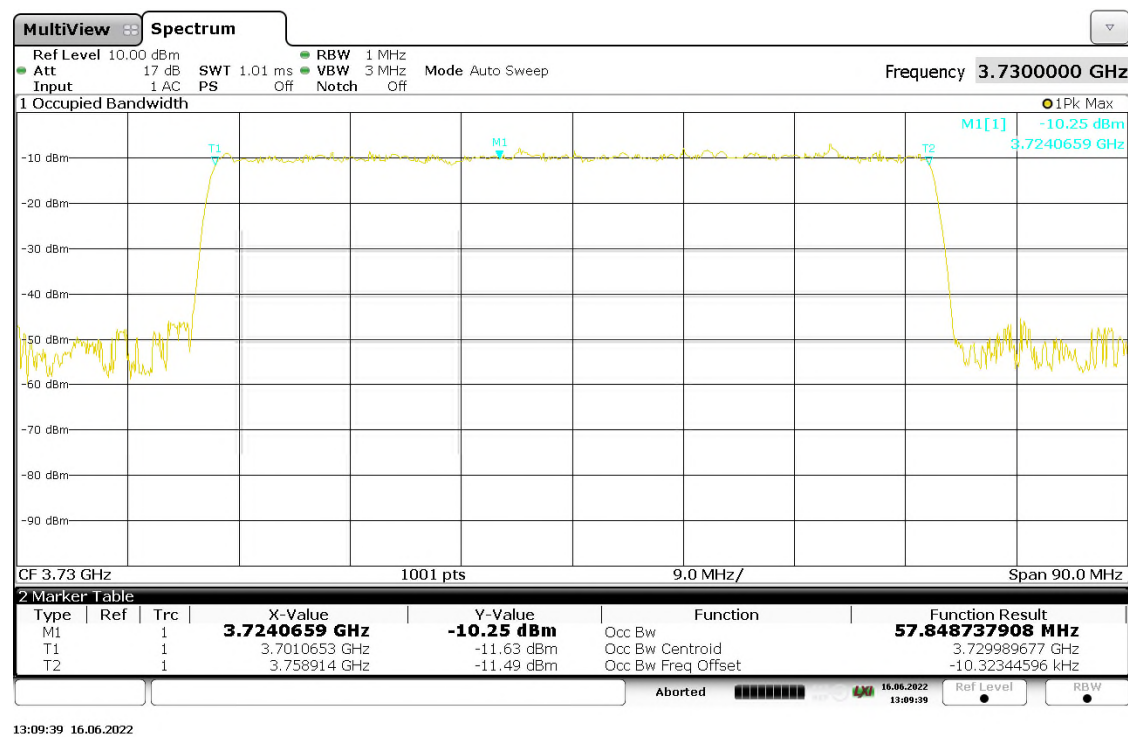
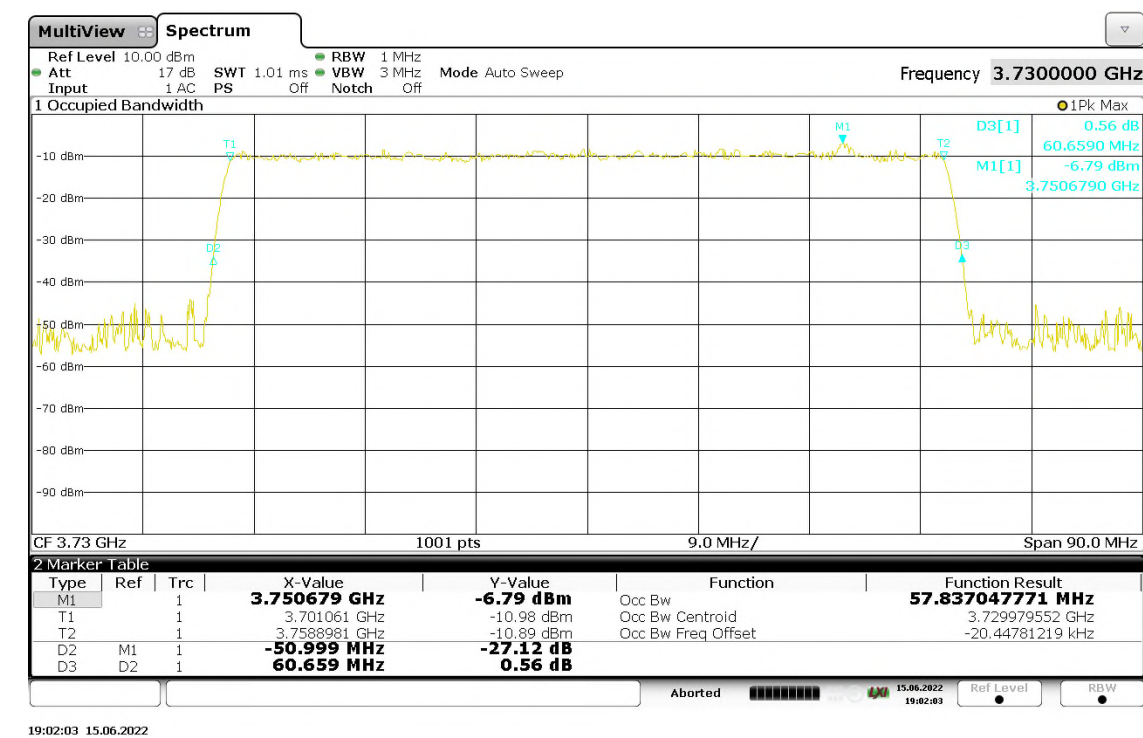


Figure 81: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 256QAM_TM3.1a, Low Channel, TX Port 28



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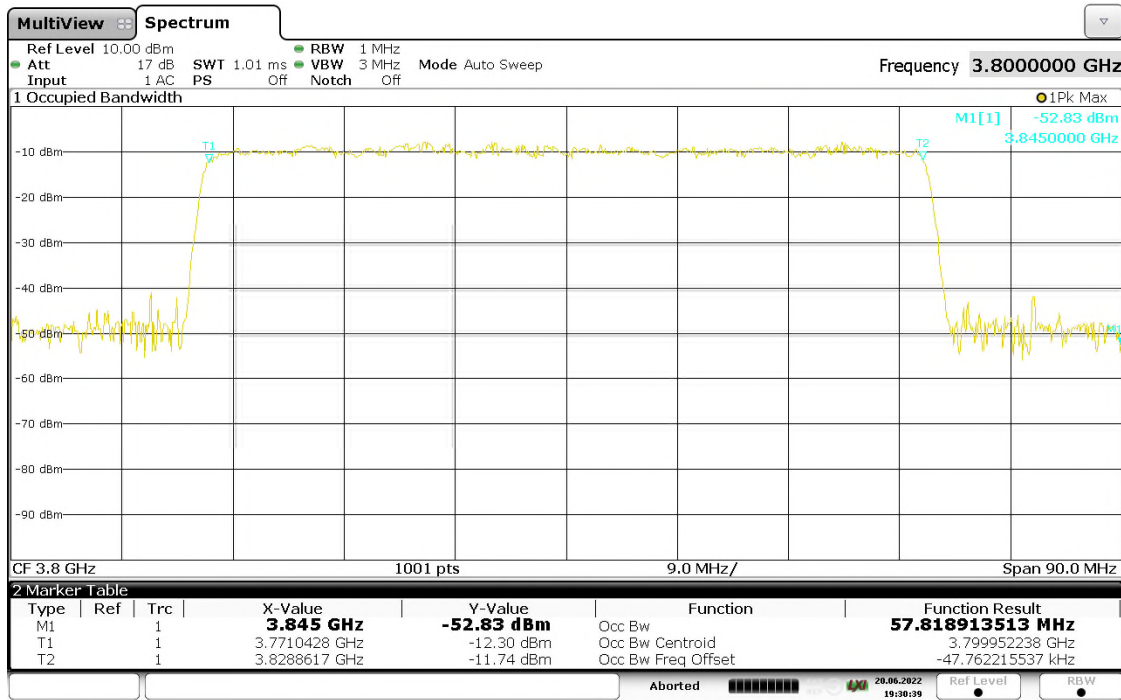
Table 80: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), Middle Channel

TX Port	99% Occupied Bandwidth [MHz]			
	QPSK_TM1.1	16QAM_TM3.2	64QAM_TM3.1	256QAM_TM3.1a
23	57.819	58.072	57.867	57.866
31	57.836	58.099	57.888	57.886

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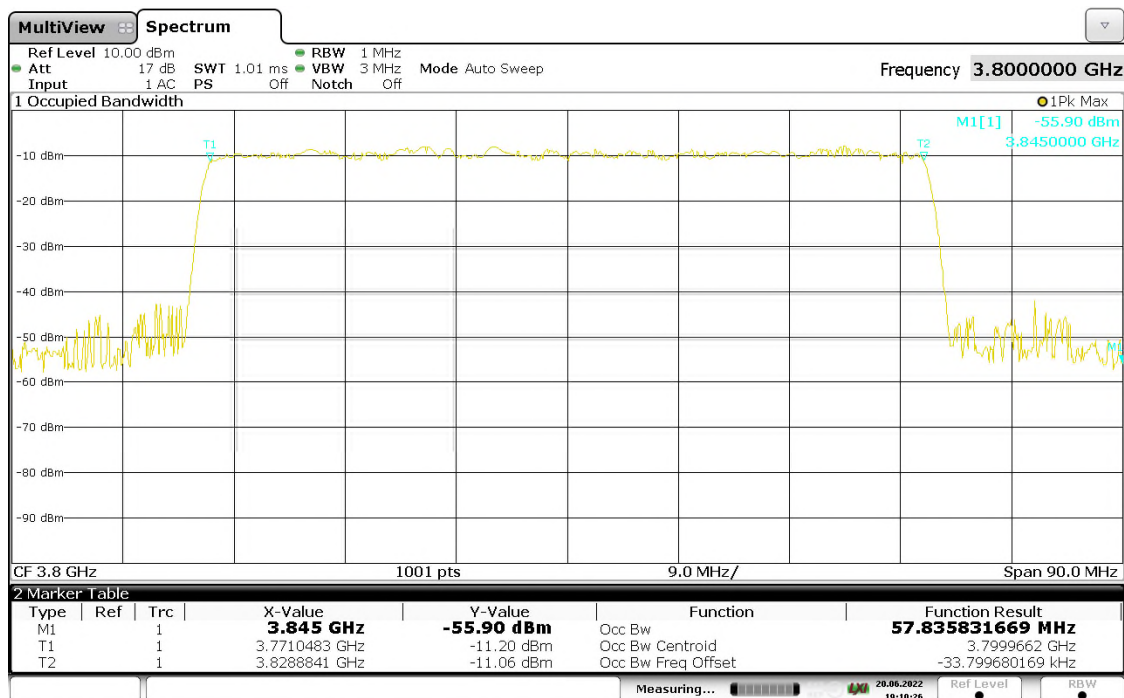
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Figure 82: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), QPSK_TM1.1, Middle Channel, TX Port 23



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Figure 83: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), QPSK_TM1.1, Middle Channel, TX Port 31

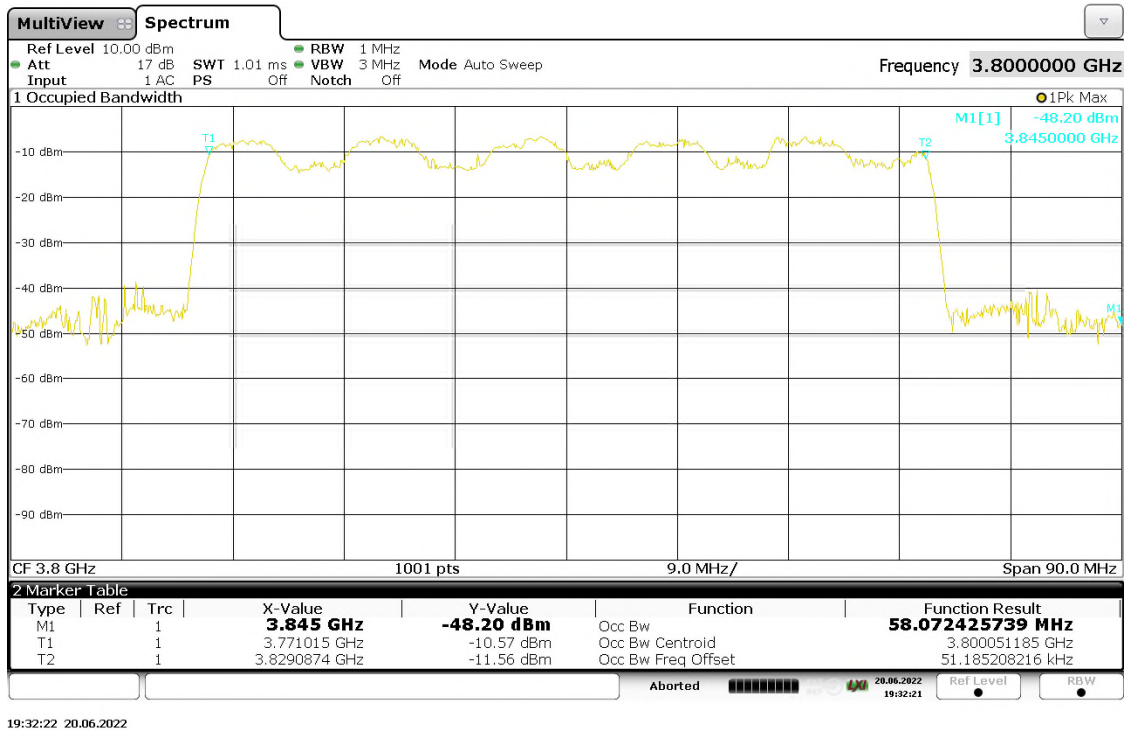


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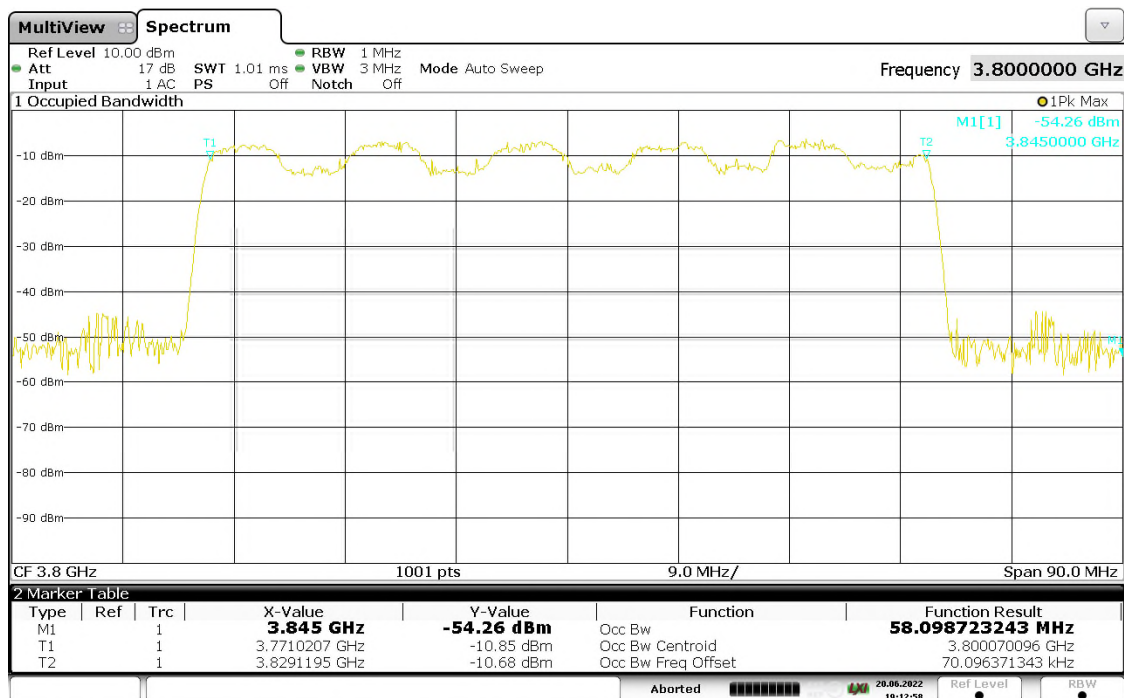
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Figure 84: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 16QAM_TM3.2, Middle Channel, TX Port 23



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Figure 85: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 16QAM_TM3.2, Middle Channel, TX Port 31

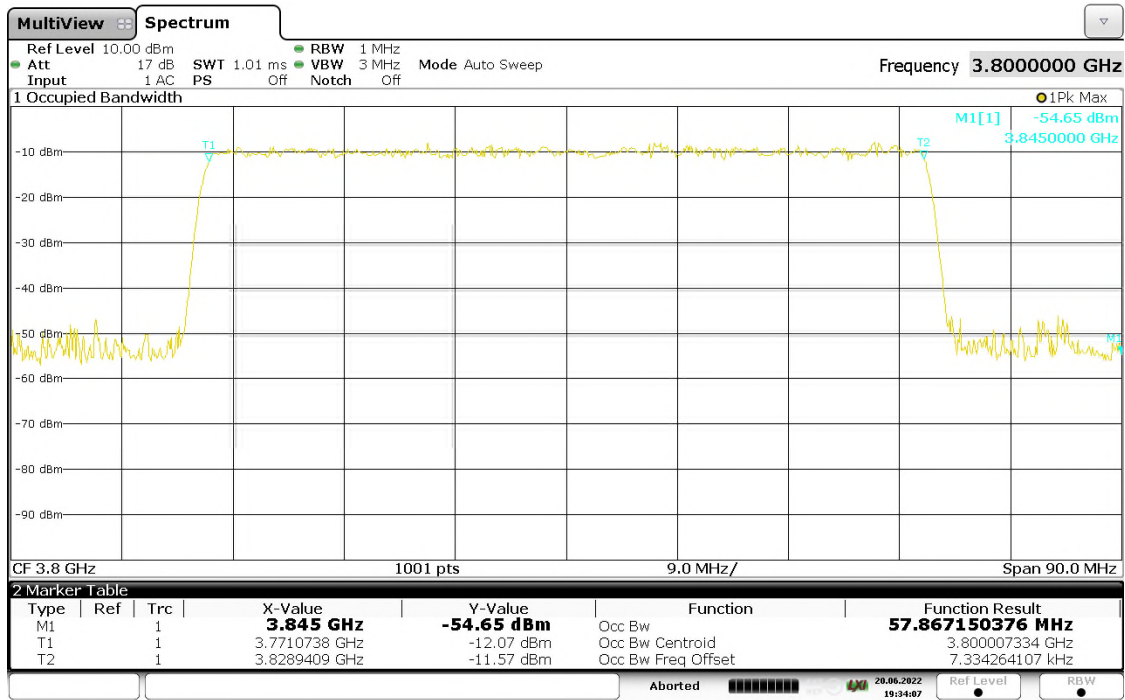


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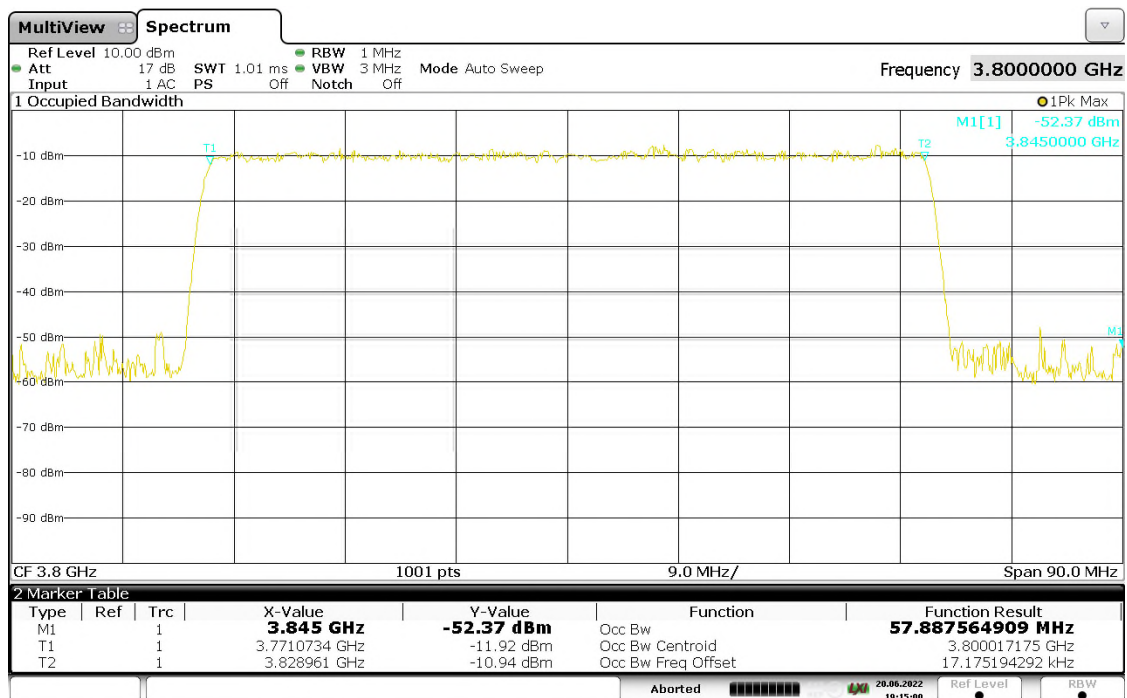
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Figure 86: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 64QAM_TM3.1, Middle Channel, TX Port 23



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Figure 87: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 64QAM_TM3.1, Middle Channel, TX Port 31



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Figure 88: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 256QAM_TM3.1a, Middle Channel, TX Port 23

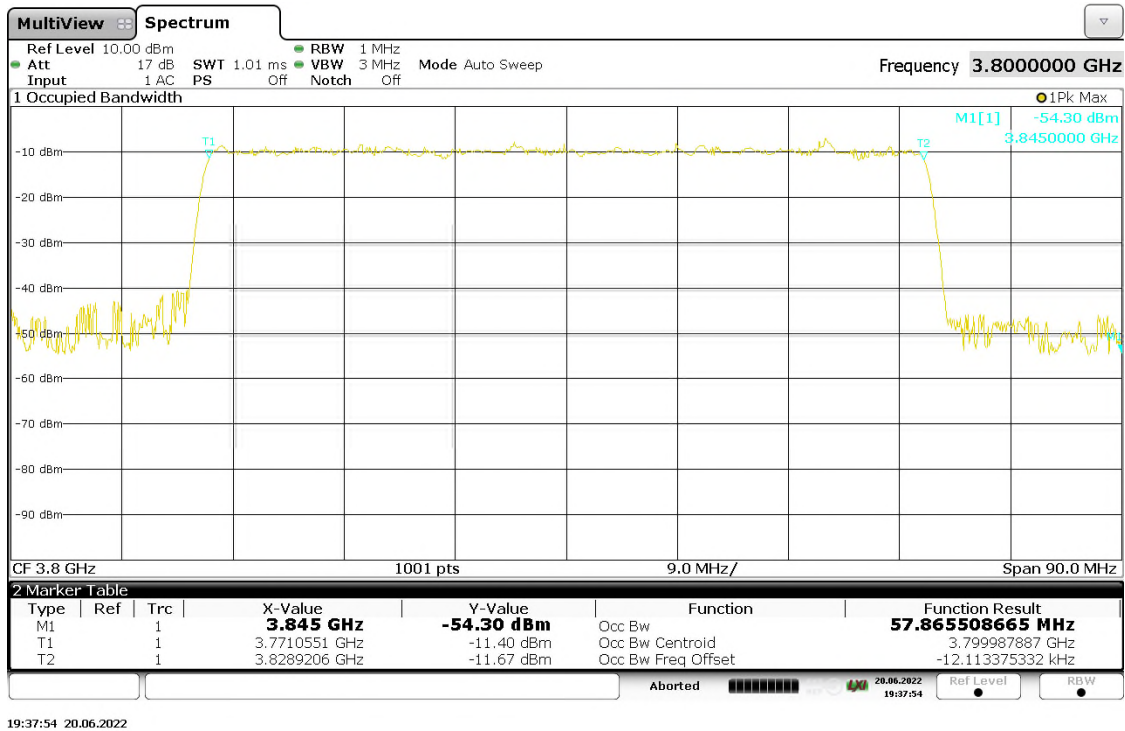
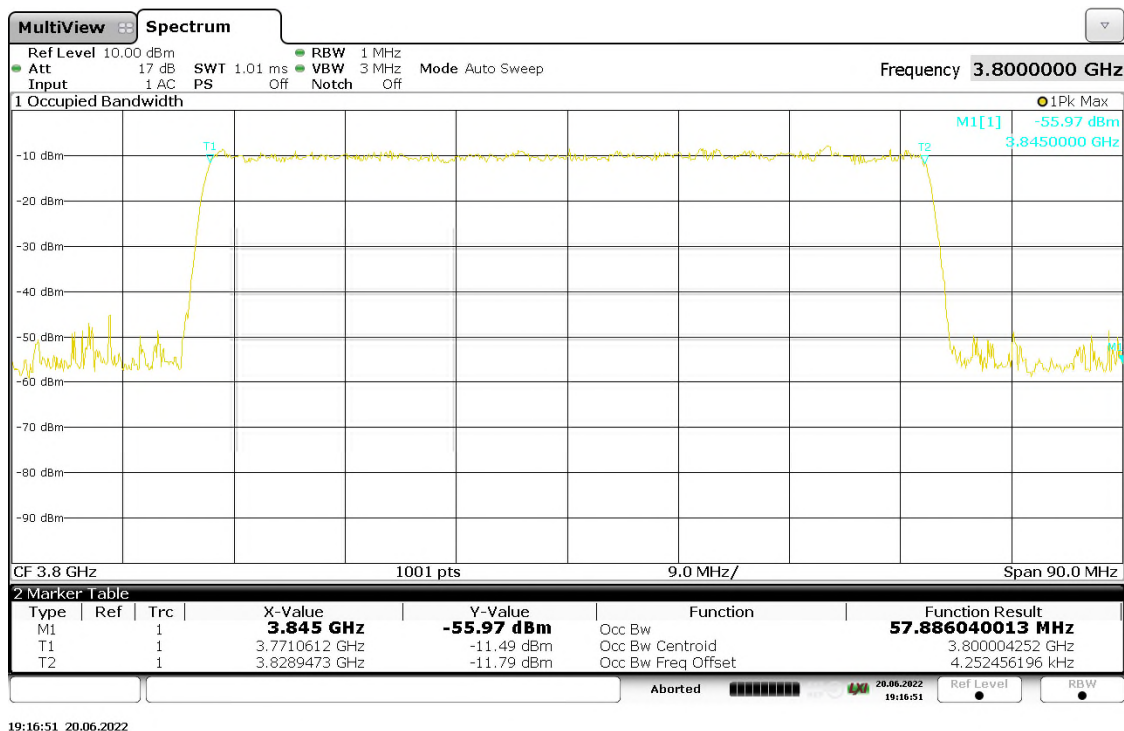


Figure 89: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 256QAM_TM3.1a, Middle Channel, TX Port 31



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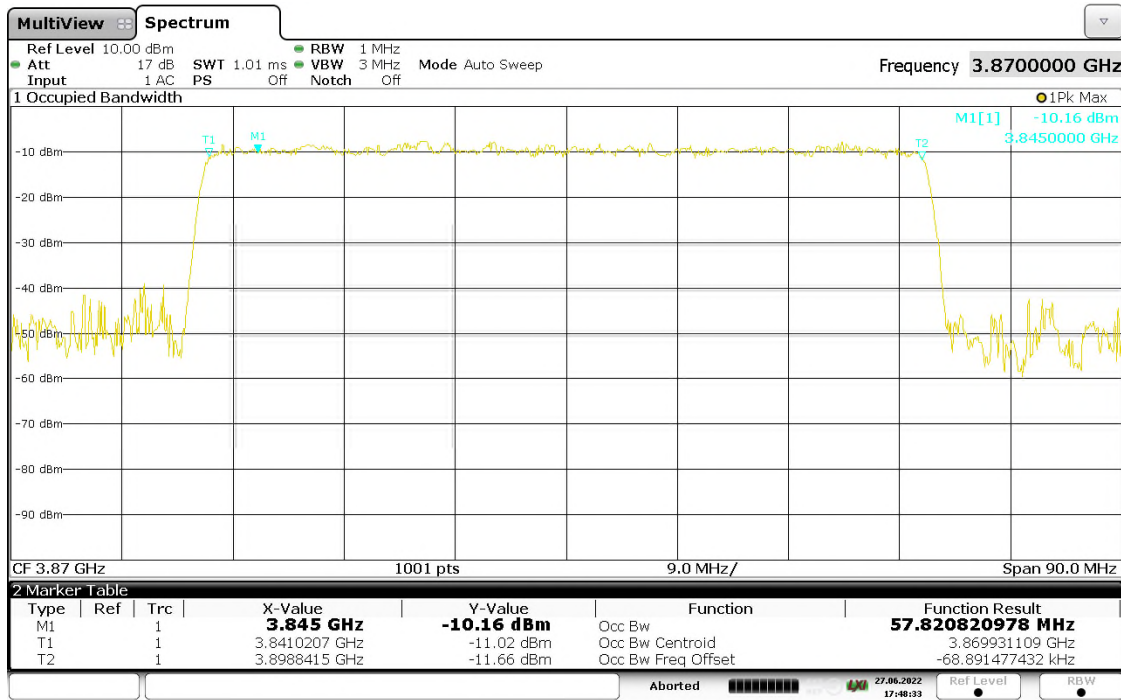
Table 81: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), High Channel

TX Port	99% Occupied Bandwidth [MHz]			
	QPSK_TM1.1	16QAM_TM3.2	64QAM_TM3.1	256QAM_TM3.1a
17	57.821	58.092	57.889	57.855
25	57.835	58.070	57.886	57.871

Prüfbericht-Nr.: **JP223Y4L 001**
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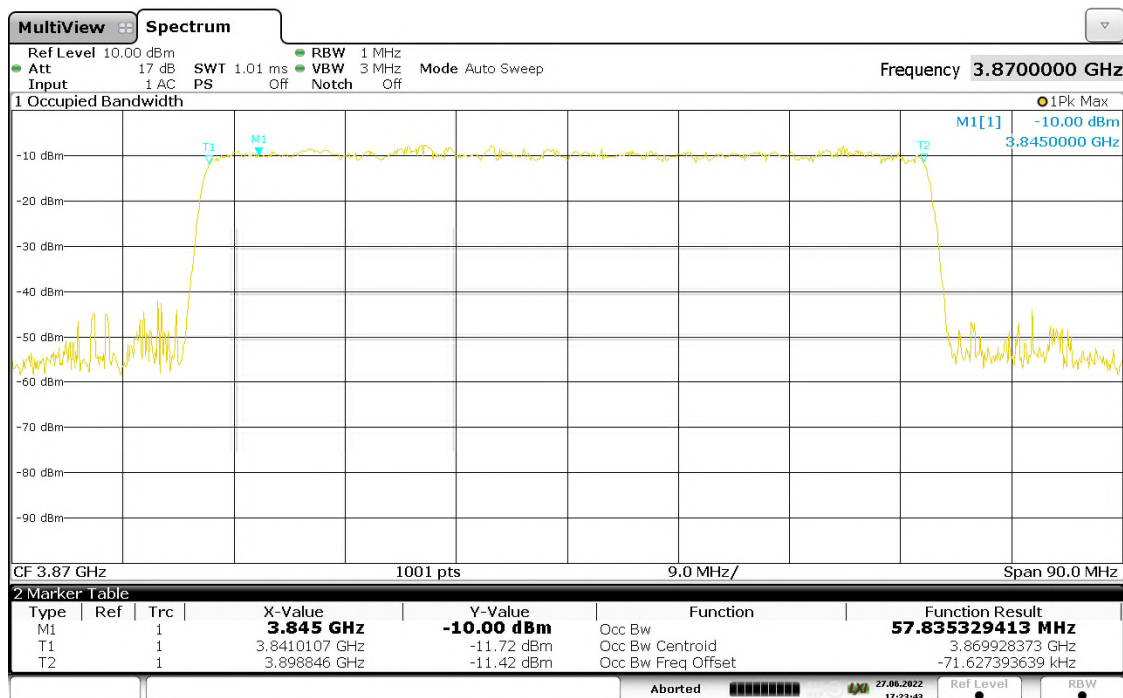
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Figure 90: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), QPSK_TM1.1, High Channel, TX Port 17



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Figure 91: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), QPSK_TM1.1, High Channel, TX Port 25

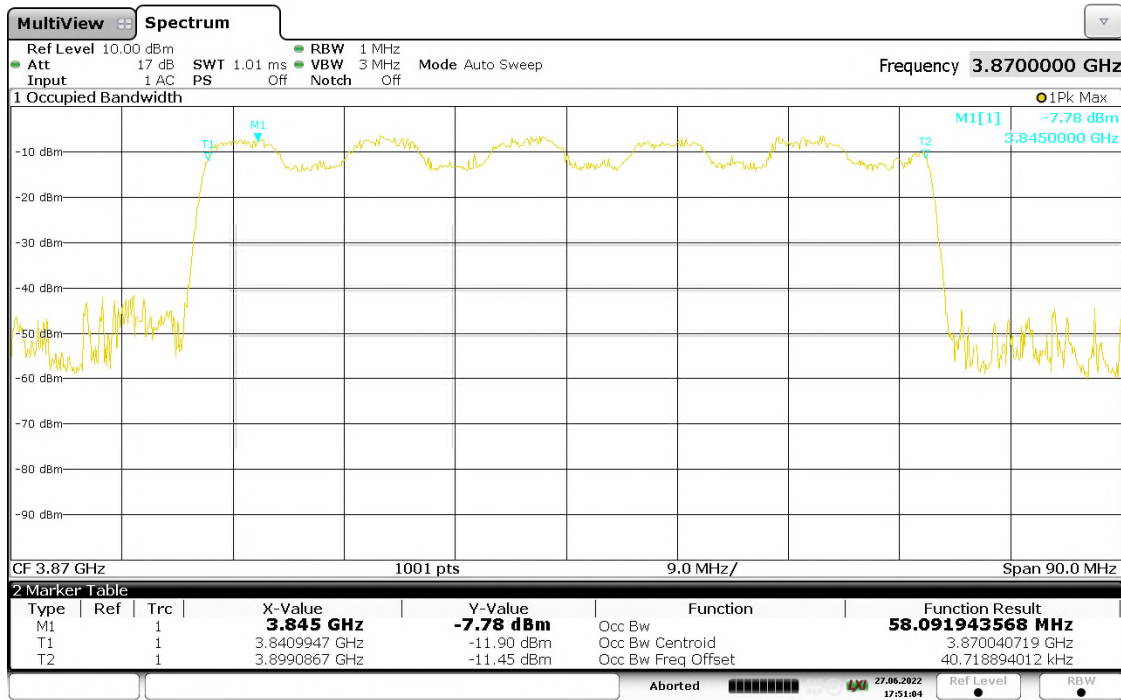


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Prüfbericht-Nr.: **JP223Y4L 001**
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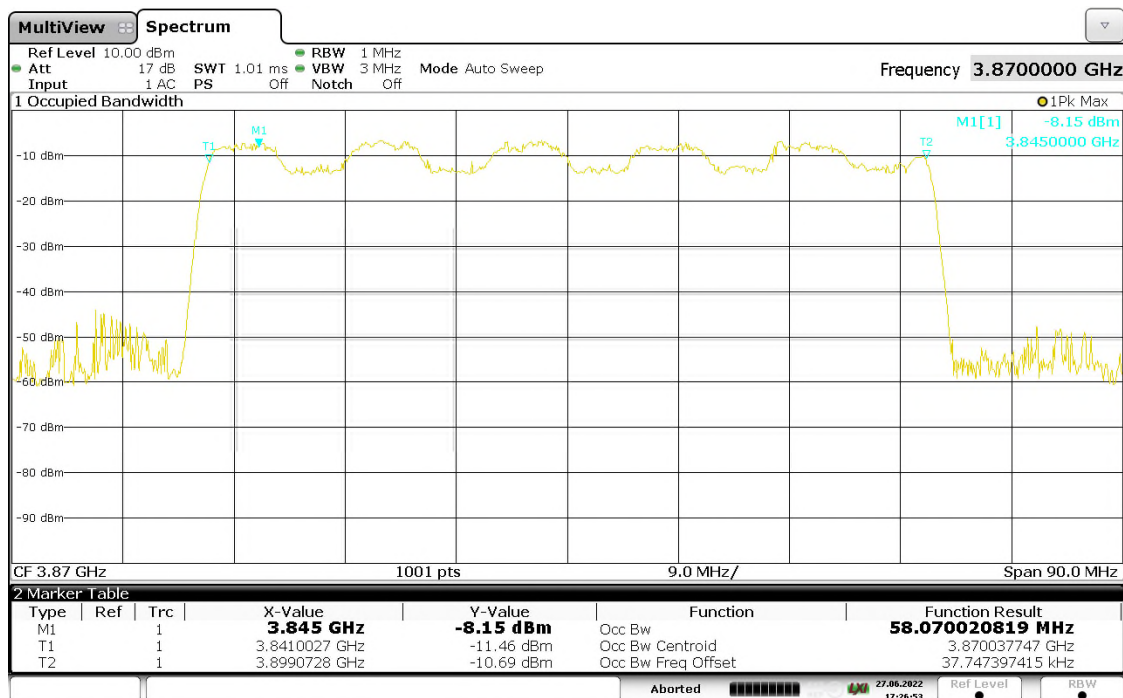
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Figure 92: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 16QAM_TM3.2, High Channel, TX Port 17



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Figure 93: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 16QAM_TM3.2, High Channel, TX Port 25

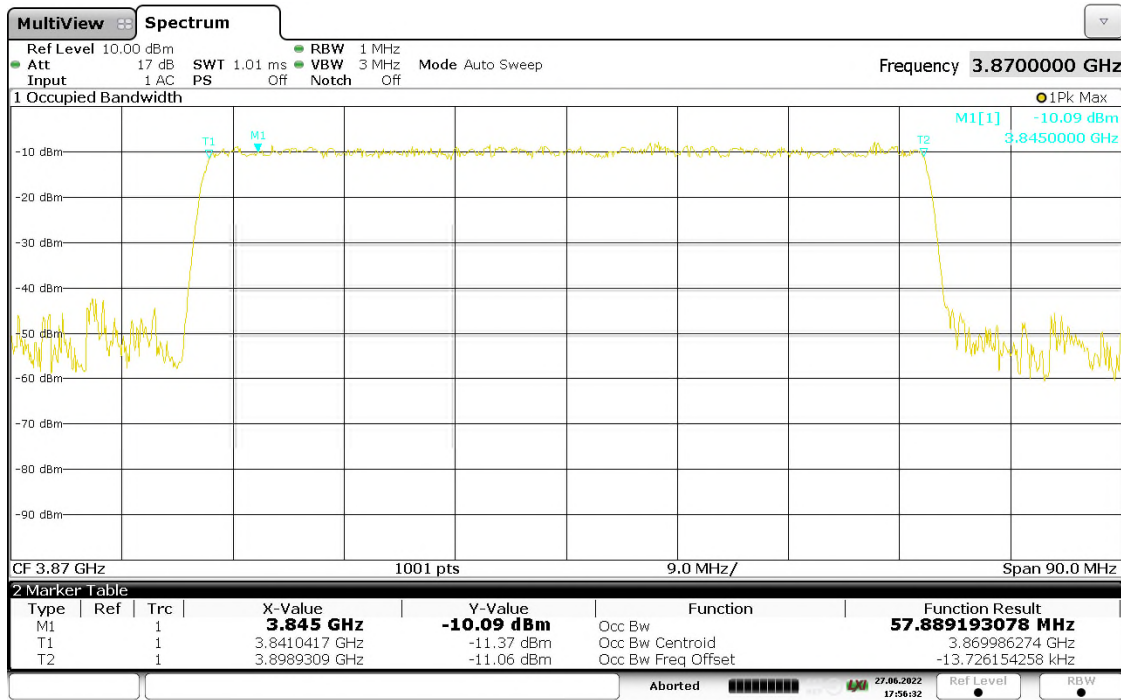


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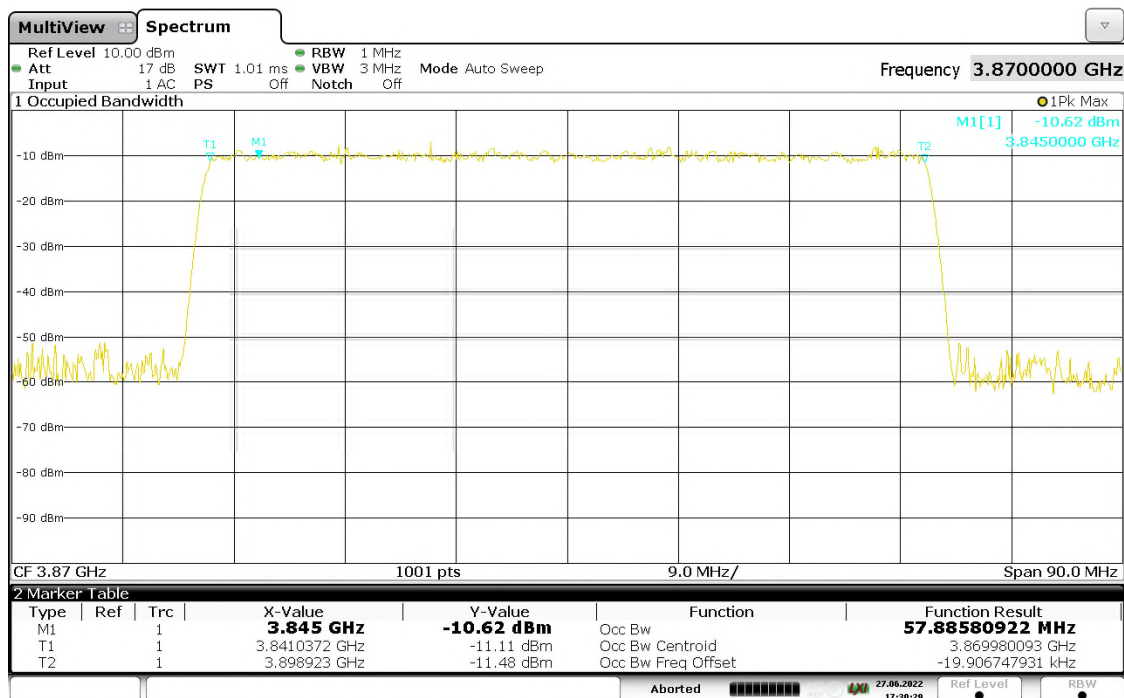
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Figure 94: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 64QAM_TM3.1, High Channel, TX Port 17



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Figure 95: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 64QAM_TM3.1, High Channel, TX Port 25

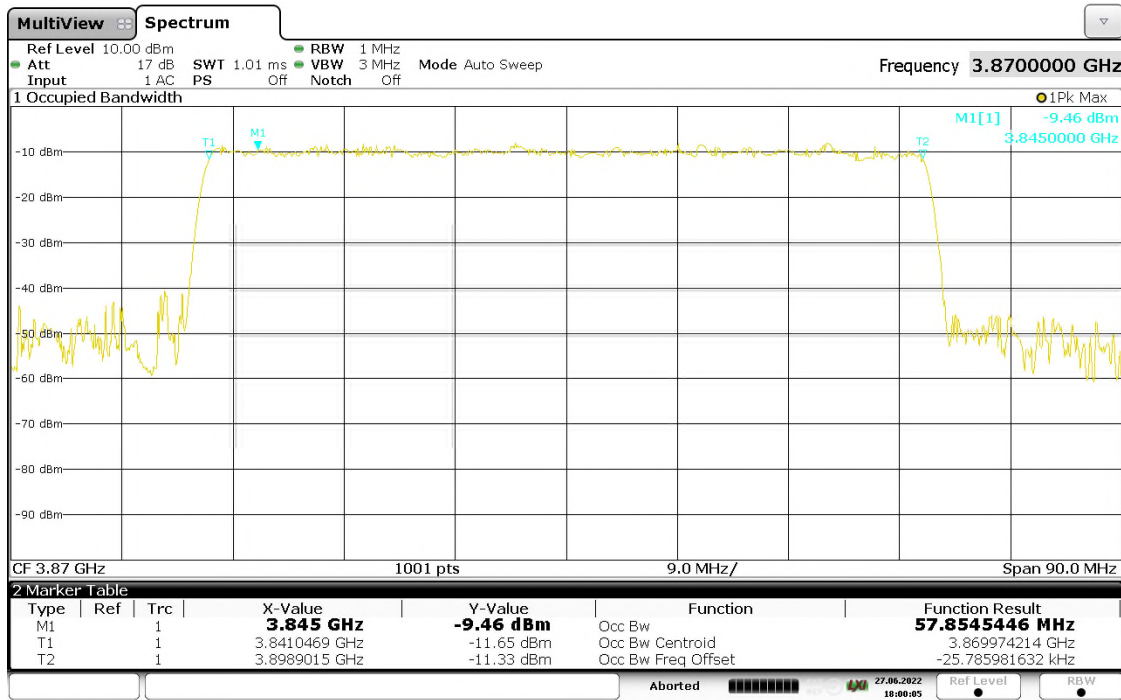


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Prüfbericht-Nr.: **JP223Y4L 001**
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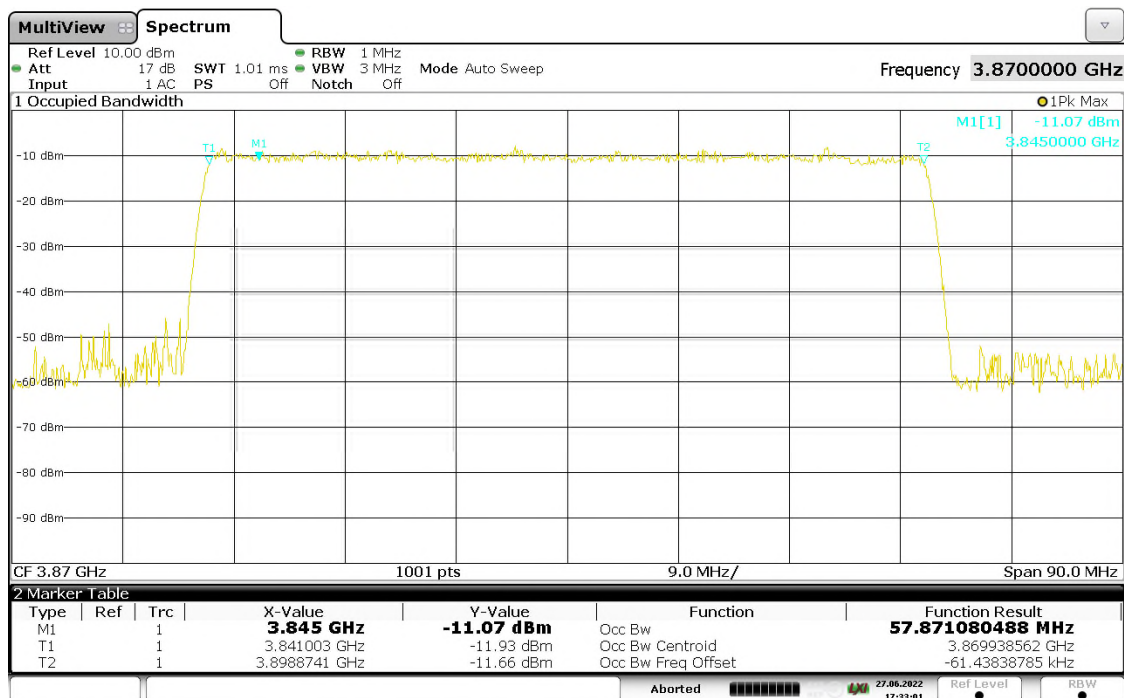
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Figure 96: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 256QAM_TM3.1a, High Channel, TX Port 17



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Figure 97: 99% Occupied Bandwidth, 1cc-2 (OBW=60MHz), 256QAM_TM3.1a, High Channel, TX Port 25



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Table 82: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), Low Channel

TX Port	99% Occupied Bandwidth [MHz]			
	QPSK_TM1.1	16QAM_TM3.2	64QAM_TM3.1	256QAM_TM3.1a
12	77.350	77.541	77.333	77.372
28	78.575	77.586	77.354	77.423

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Figure 98: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), QPSK_TM1.1, Low Channel, TX Port 12

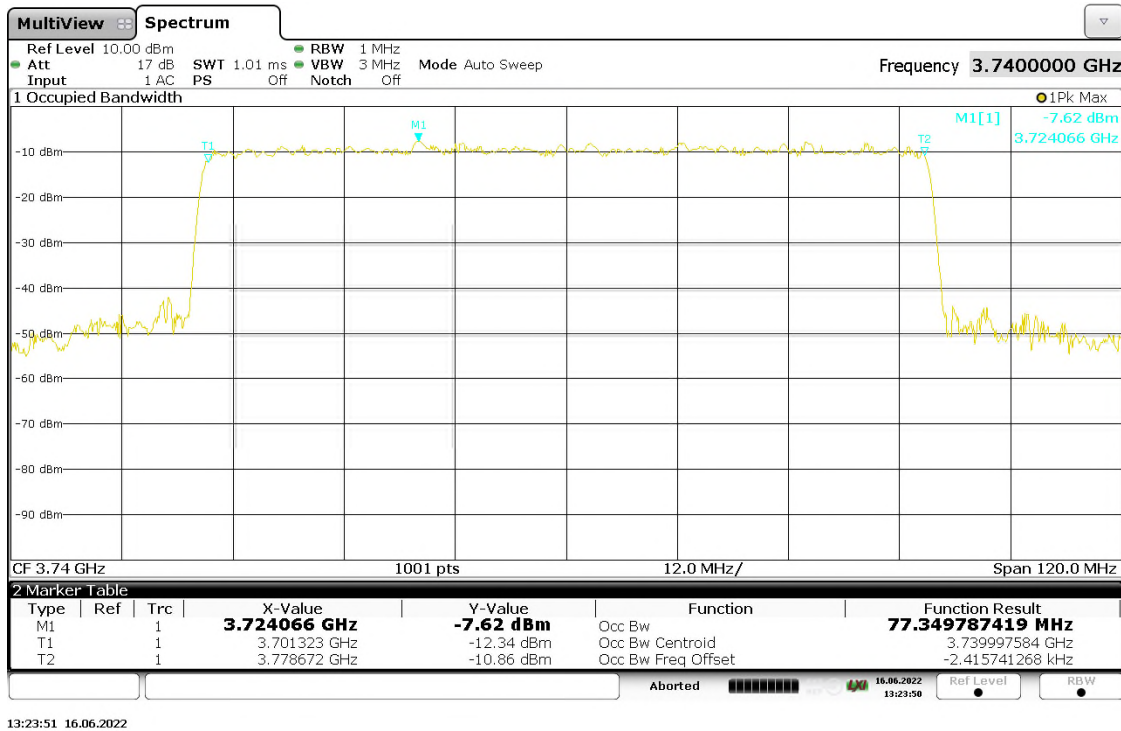
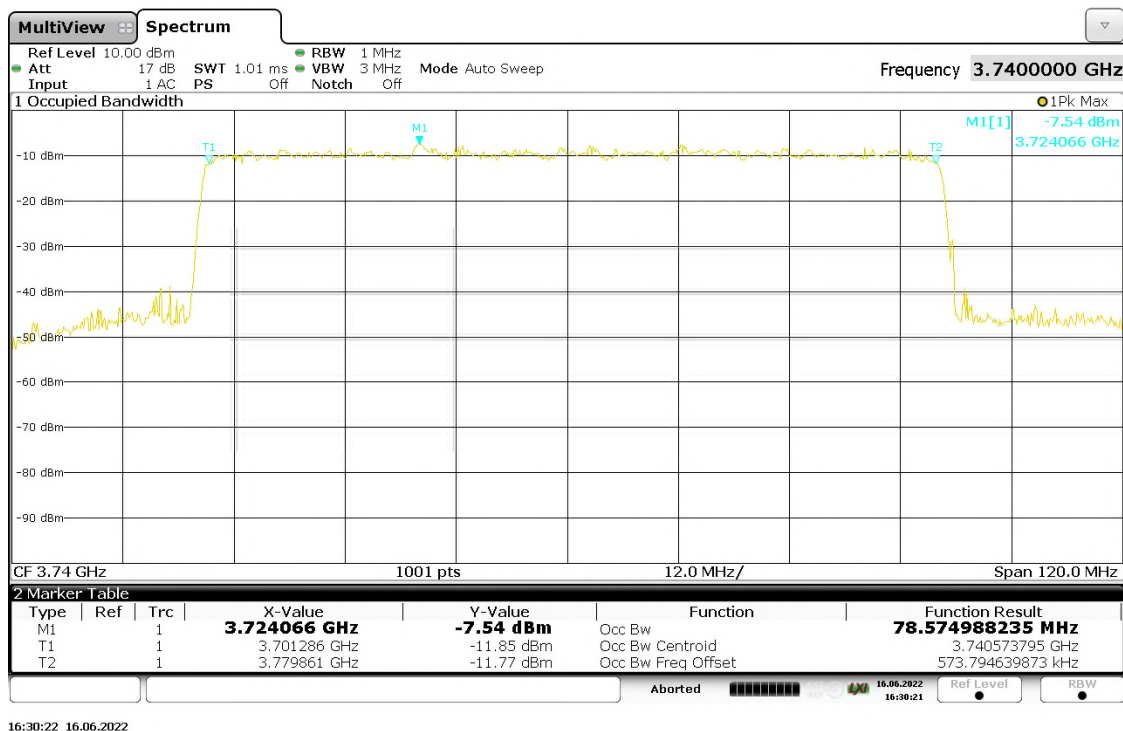


Figure 99: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), QPSK_TM1.1, Low Channel, TX Port 28



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Figure 100: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 16QAM_TM3.2, Low Channel, TX Port 12

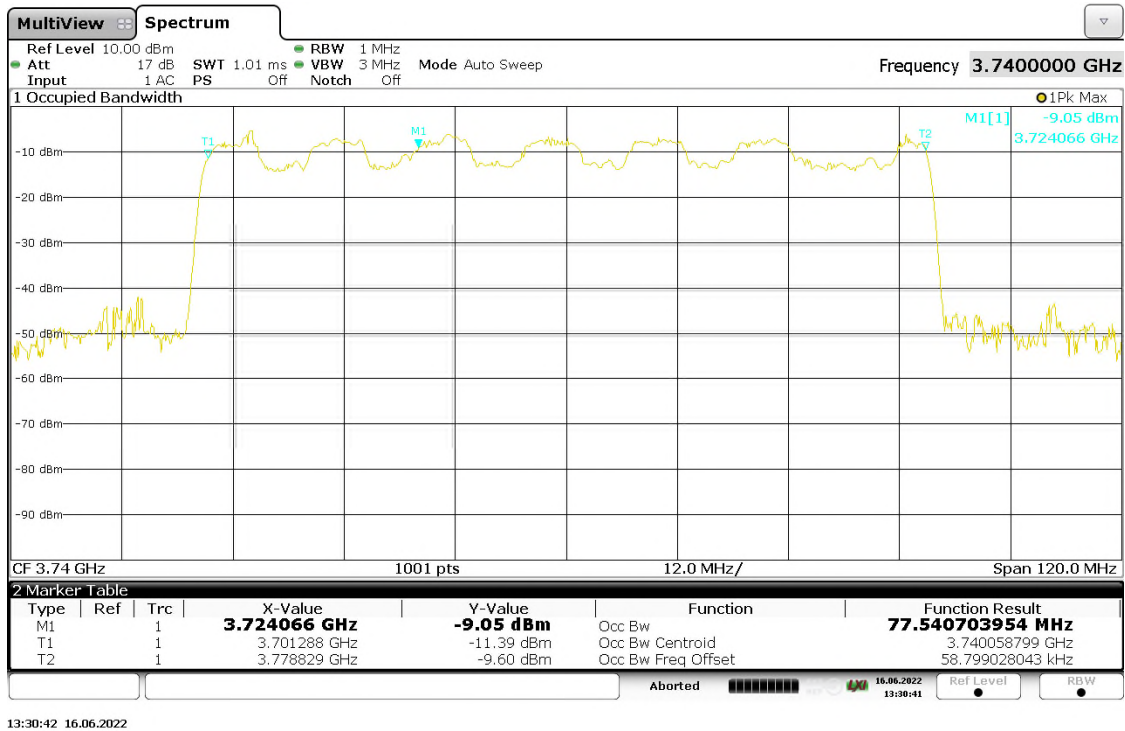
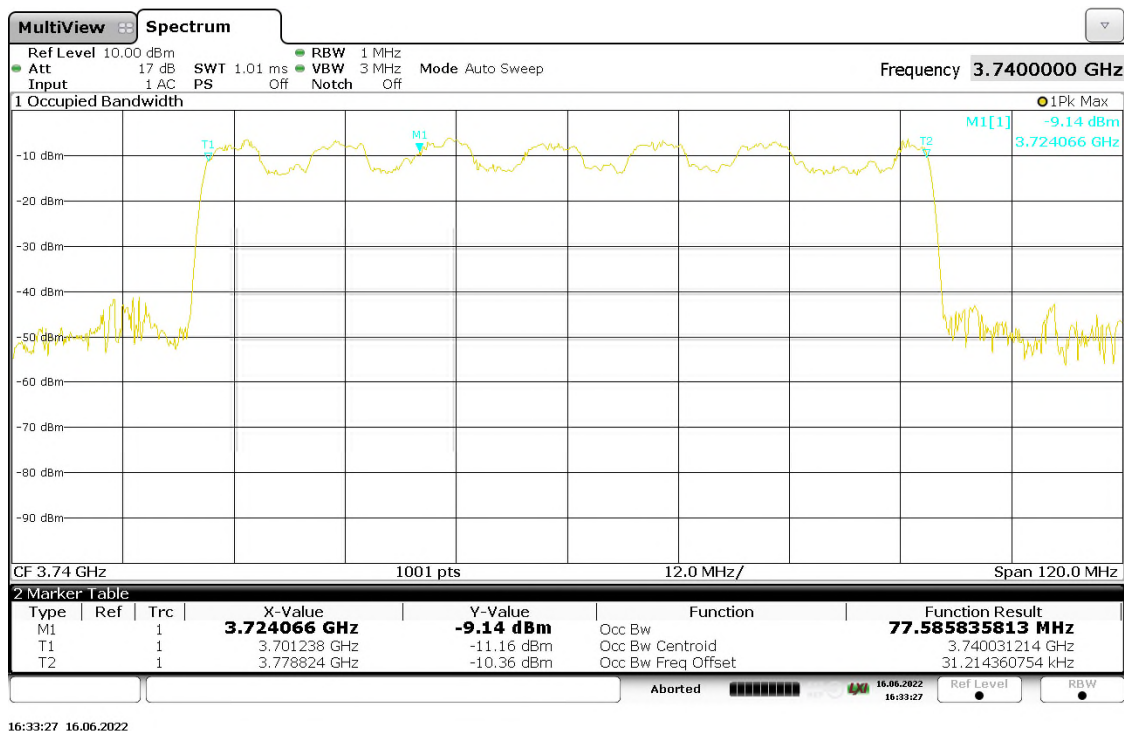


Figure 101: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 16QAM_TM3.2, Low Channel, TX Port 28



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Figure 102: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 64QAM_TM3.1, Low Channel, TX Port 12

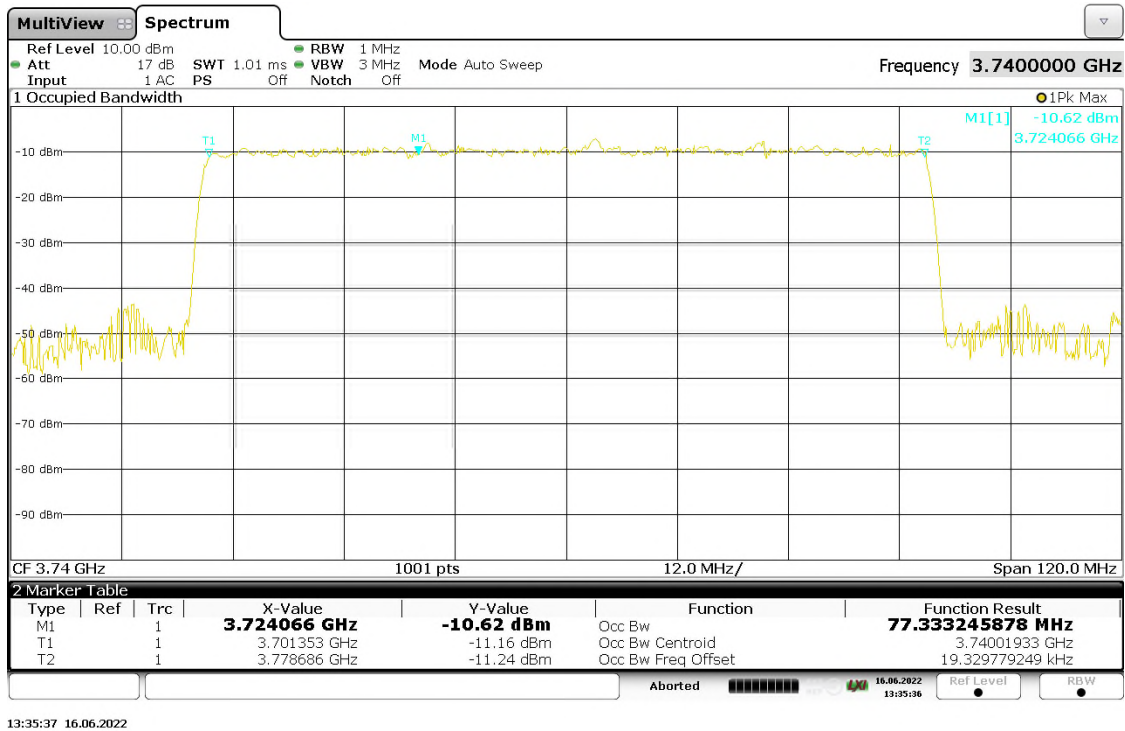
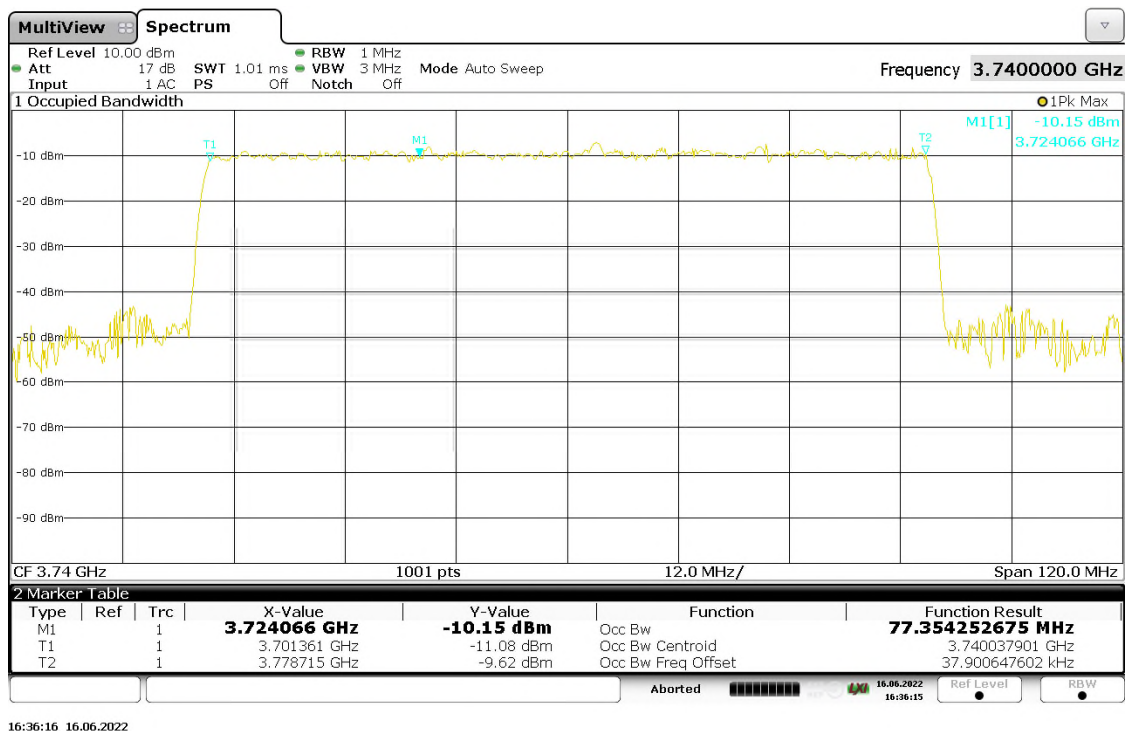


Figure 103: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 64QAM_TM3.1, Low Channel, TX Port 28



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Figure 104: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 256QAM_TM3.1a, Low Channel, TX Port 12

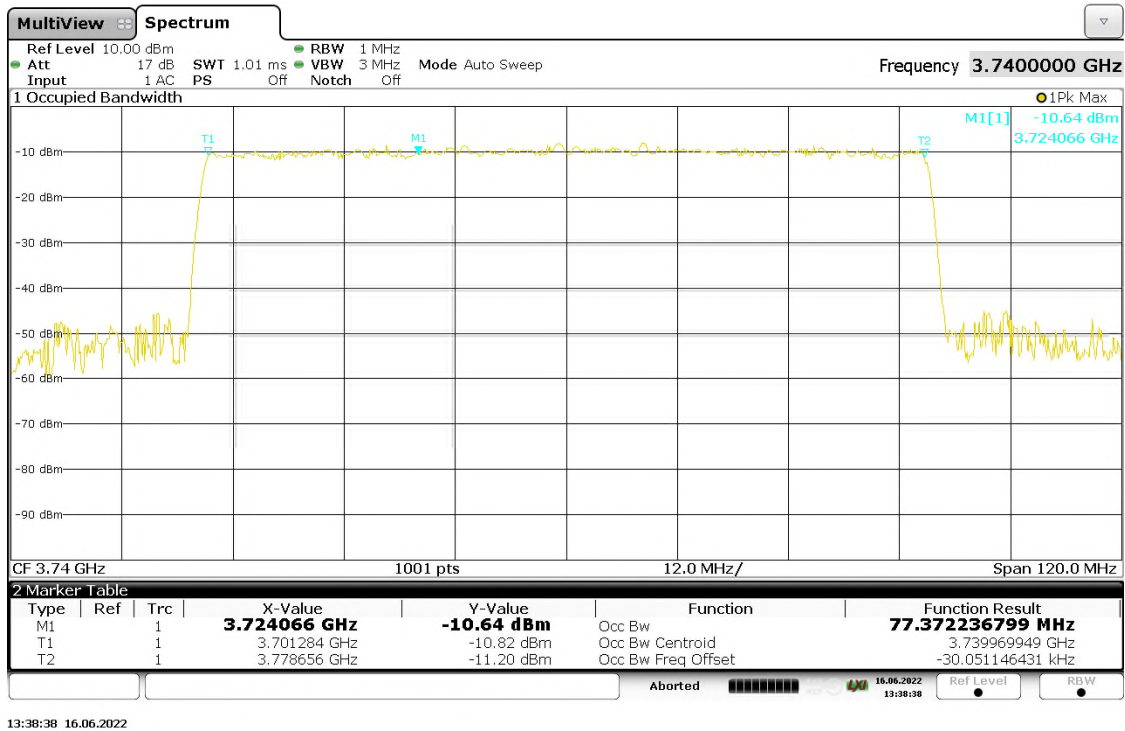
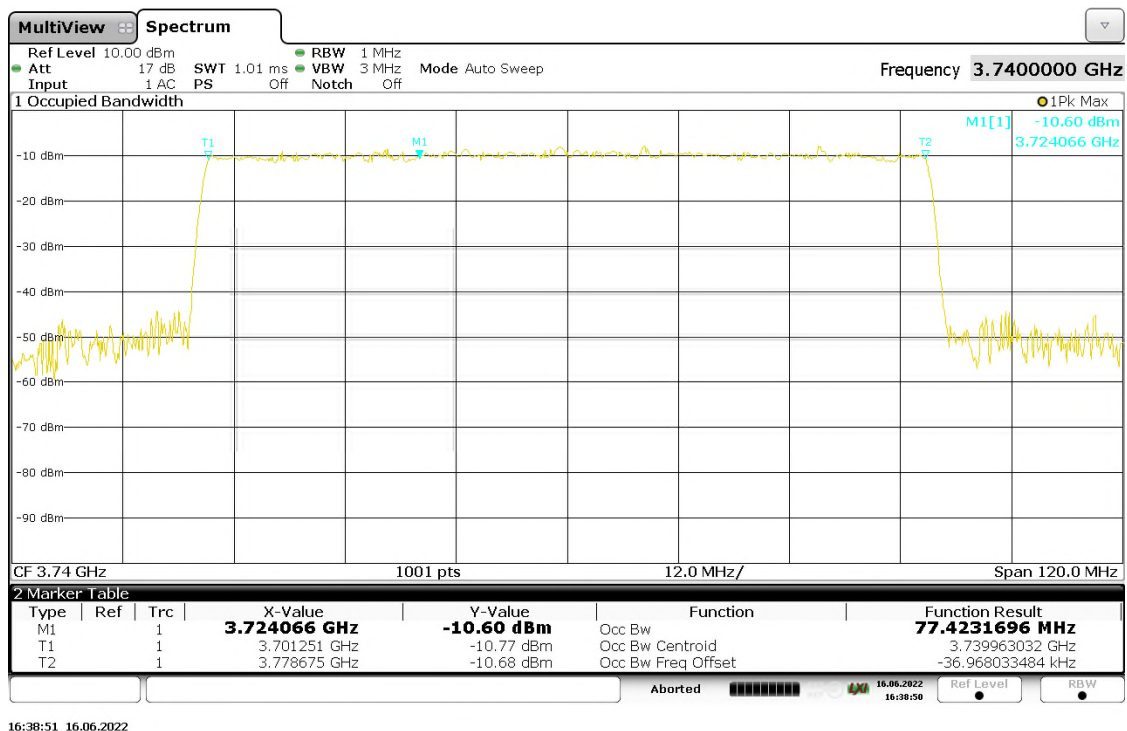


Figure 105: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 256QAM_TM3.1a, Low Channel, TX Port 28



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Table 83: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), Middle Channel

TX Port	99% Occupied Bandwidth [MHz]			
	QPSK_TM1.1	16QAM_TM3.2	64QAM_TM3.1	256QAM_TM3.1a
23	77.488	77.663	77.465	77.407
31	77.465	77.615	77.326	77.401

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Figure 106: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), QPSK_TM1.1, Middle Channel, TX Port 23

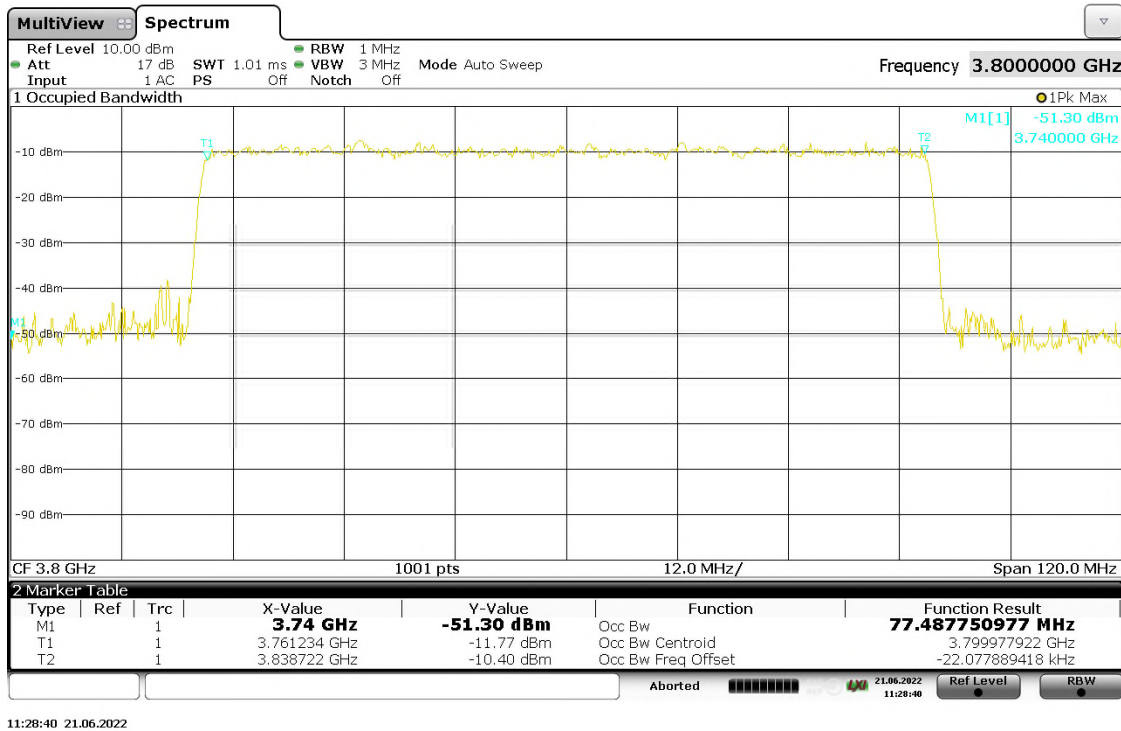
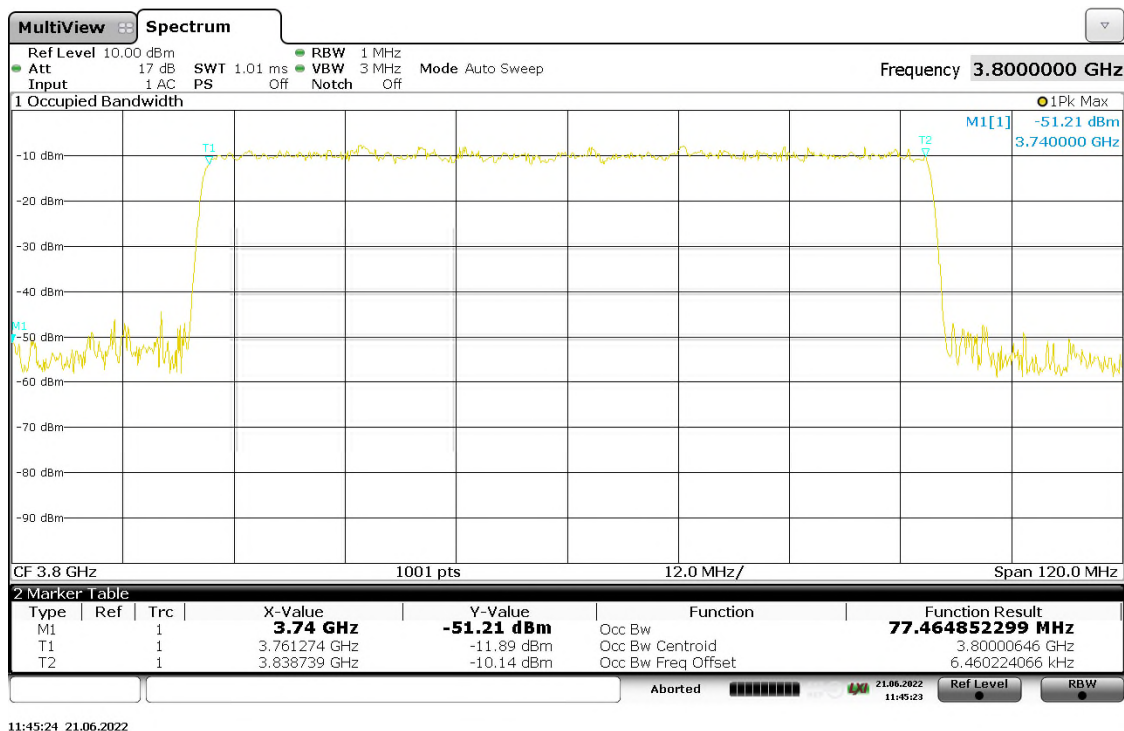


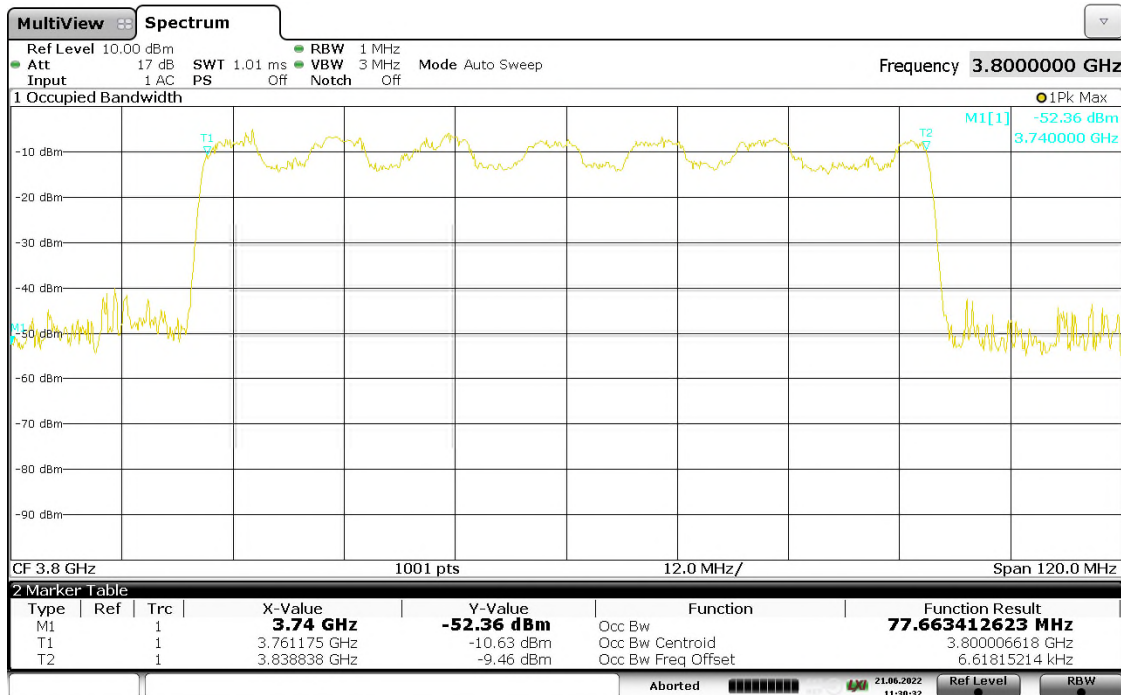
Figure 107: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), QPSK_TM1.1, Middle Channel, TX Port 31



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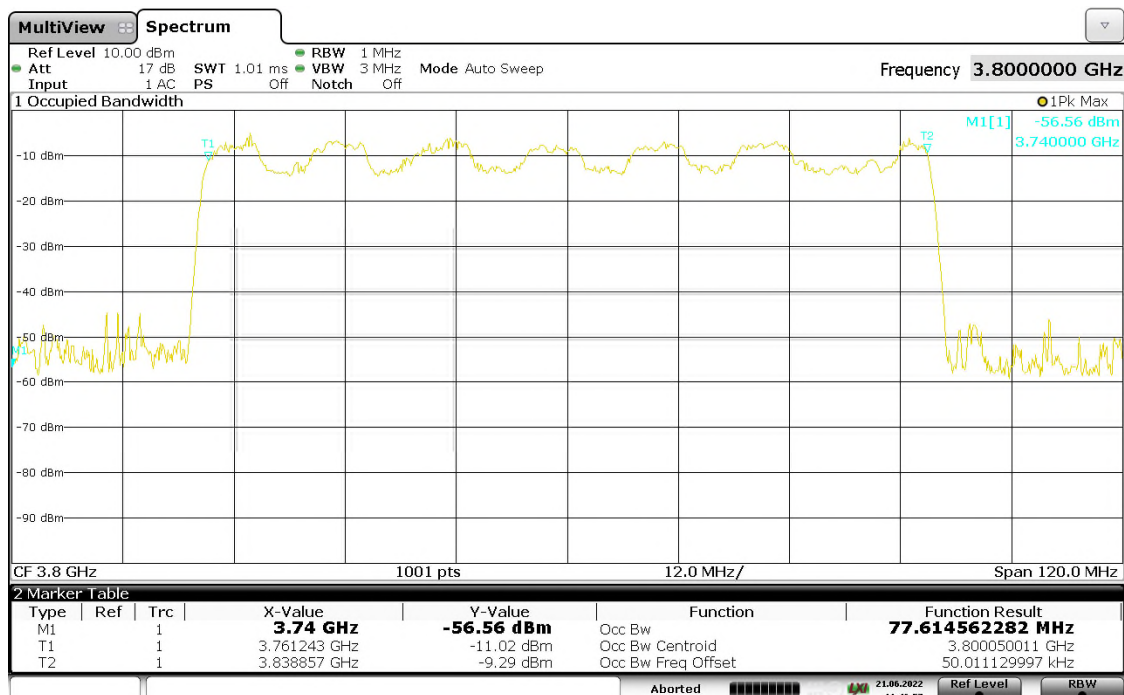
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Figure 108: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 16QAM_TM3.2, Middle Channel, TX Port 23



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Figure 109: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 16QAM_TM3.2, Middle Channel, TX Port 31



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Figure 110: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 64QAM_TM3.1, Middle Channel, TX Port 23

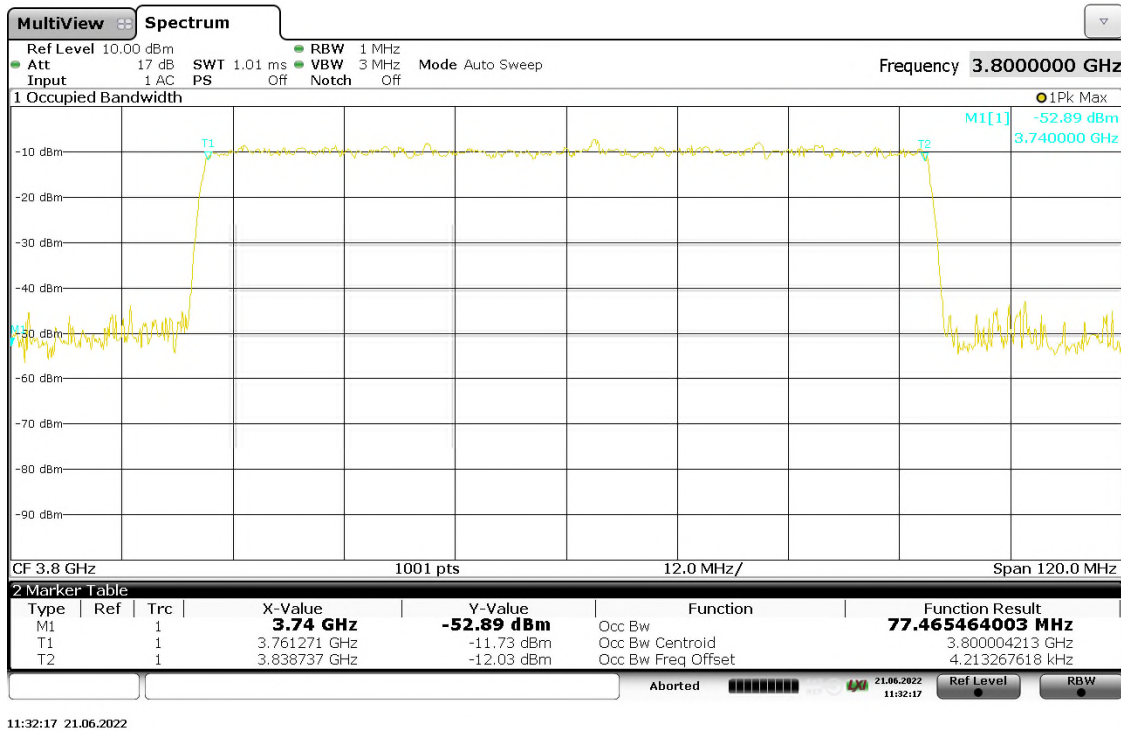
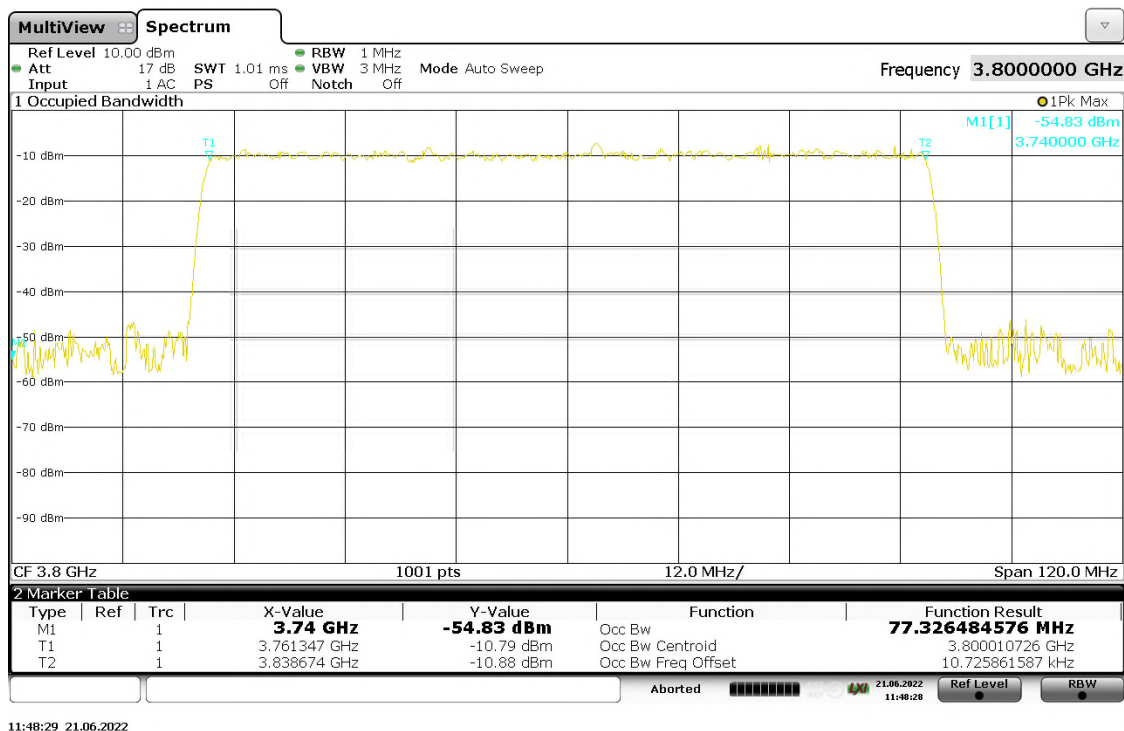


Figure 111: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 64QAM_TM3.1, Middle Channel, TX Port 31



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Figure 112: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 256QAM_TM3.1a, Middle Channel, TX Port 23

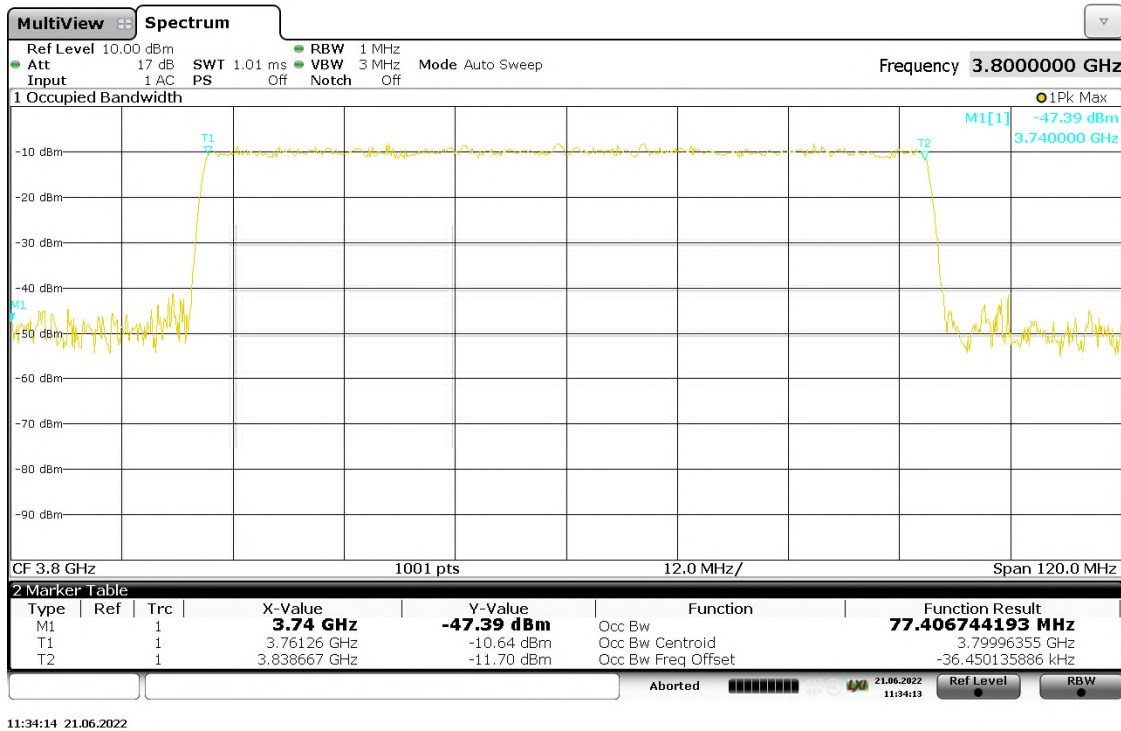
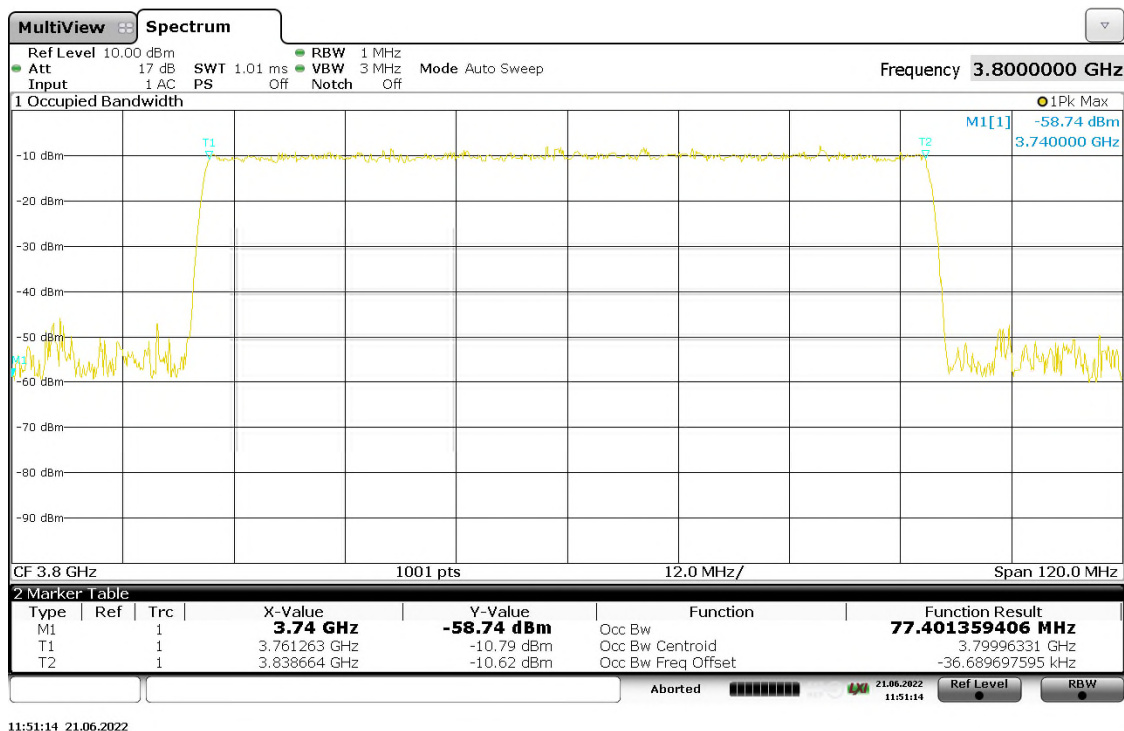


Figure 113: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 256QAM_TM3.1a, Middle Channel, TX Port 31



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Table 84: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), High Channel

TX Port	99% Occupied Bandwidth [MHz]			
	QPSK_TM1.1	16QAM_TM3.2	64QAM_TM3.1	256QAM_TM3.1a
17	78.086	77.584	77.302	77.362
25	77.362	77.593	77.467	77.341

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Figure 114: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), QPSK_TM1.1, High Channel, TX Port 17

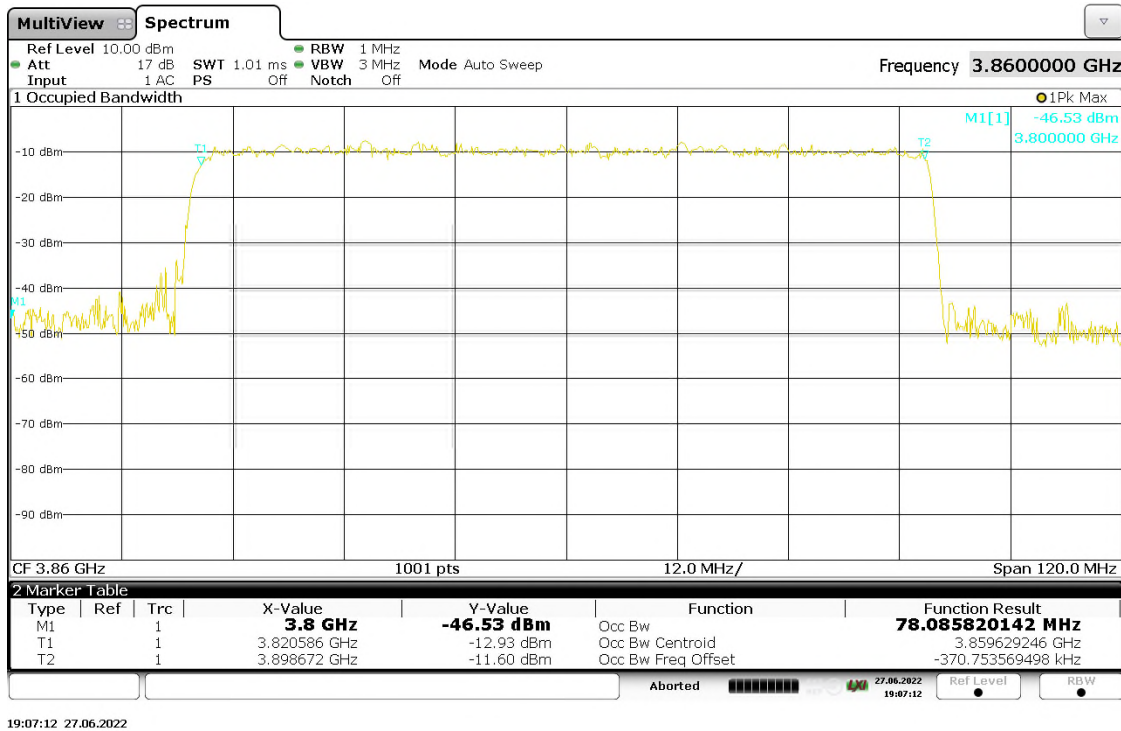
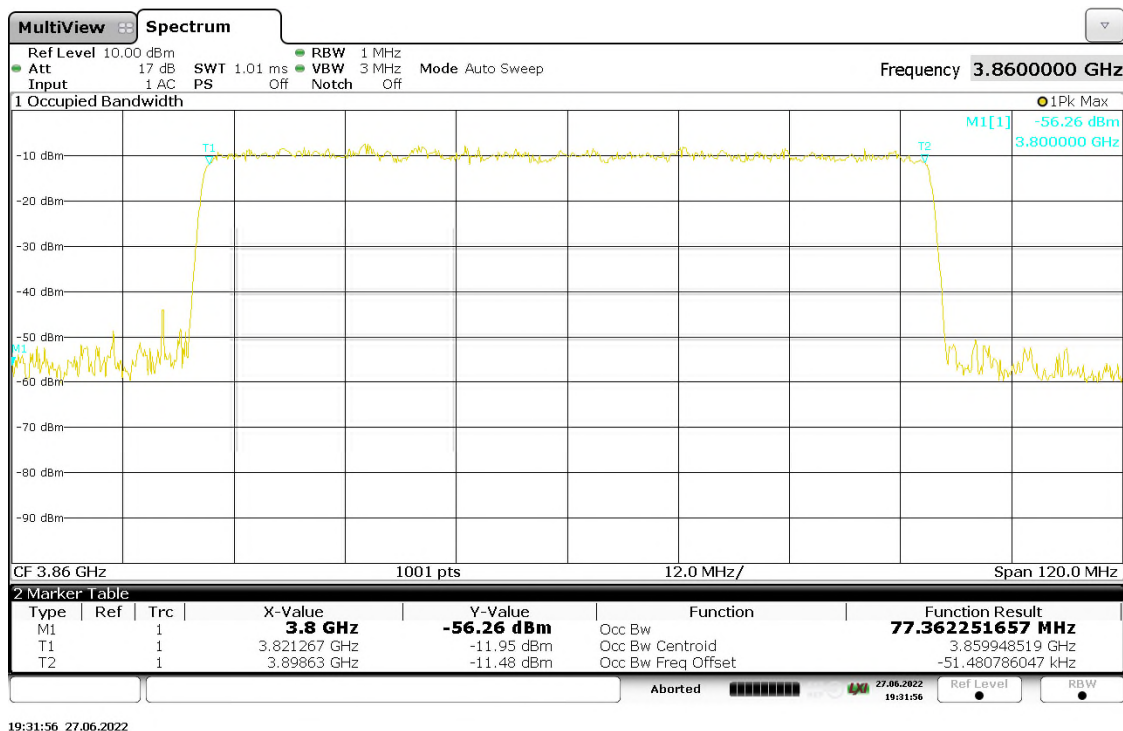


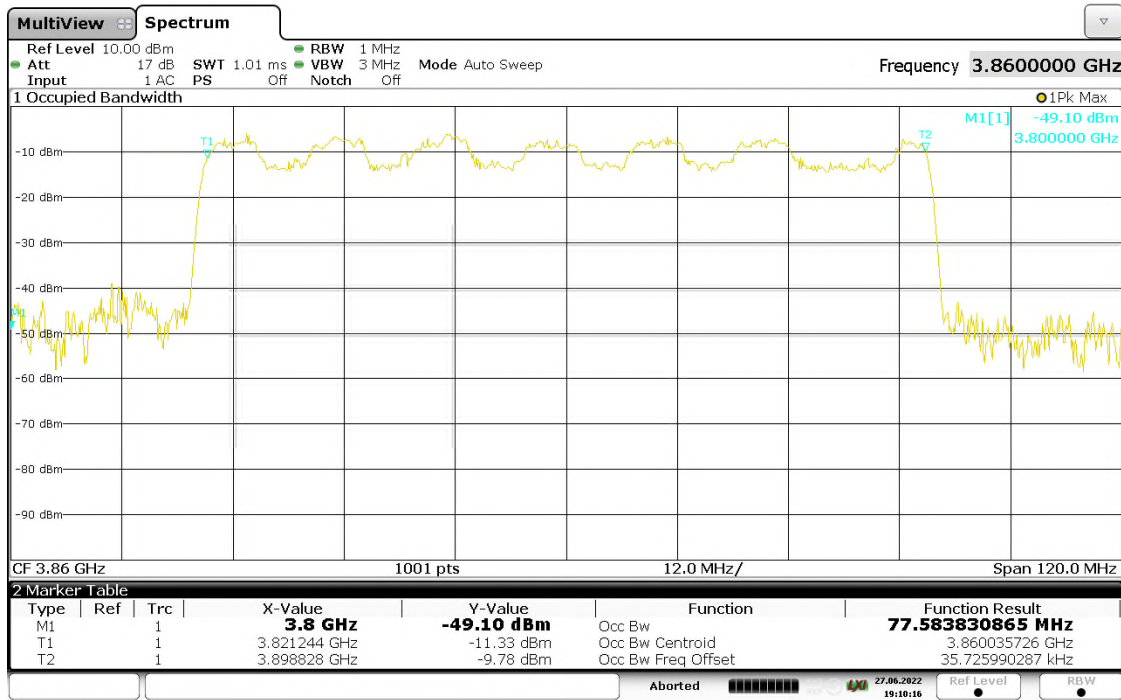
Figure 115: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), QPSK_TM1.1, High Channel, TX Port 25



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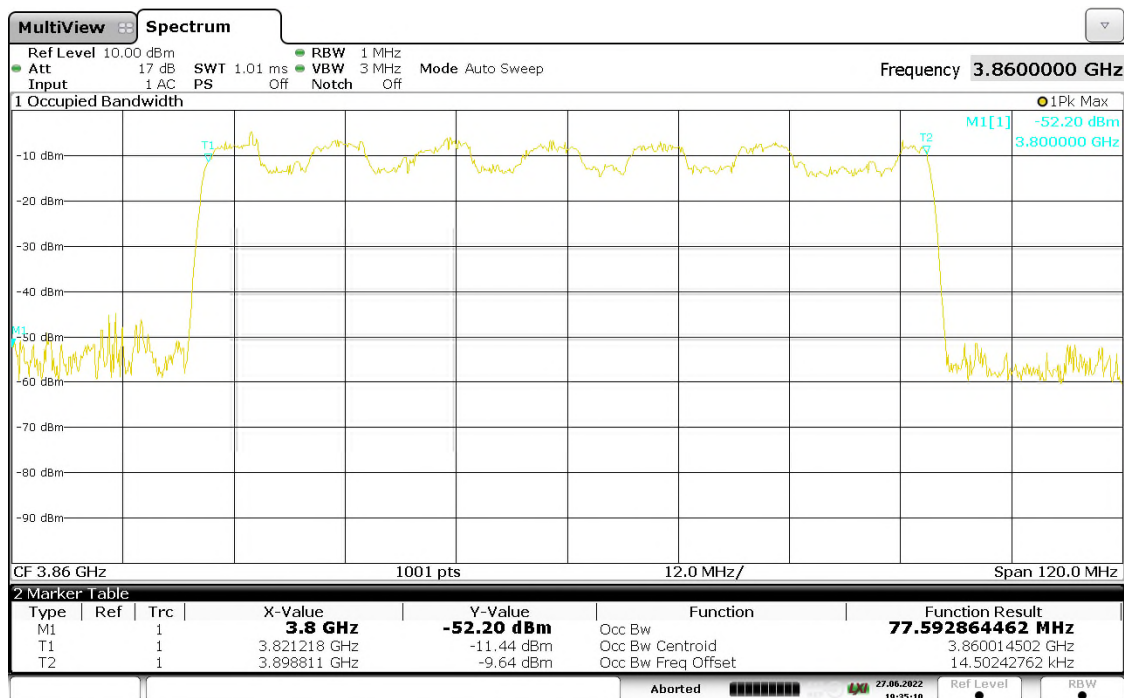
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Figure 116: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 16QAM_TM3.2, High Channel, TX Port 17



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Figure 117: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 16QAM_TM3.2, High Channel, TX Port 25

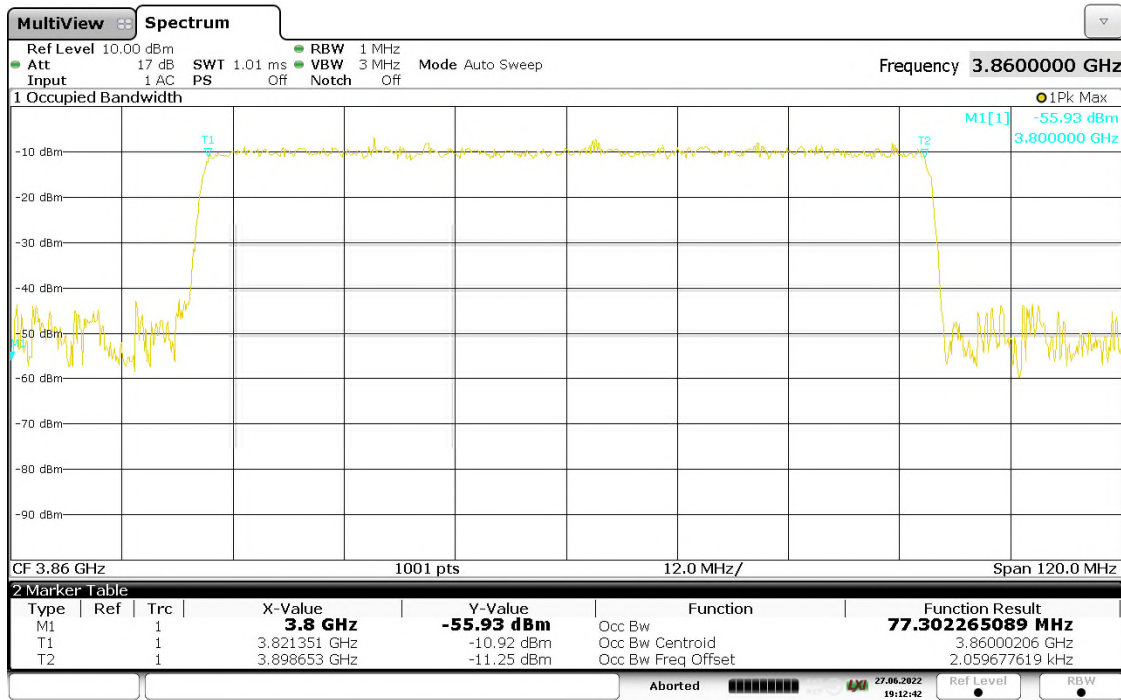


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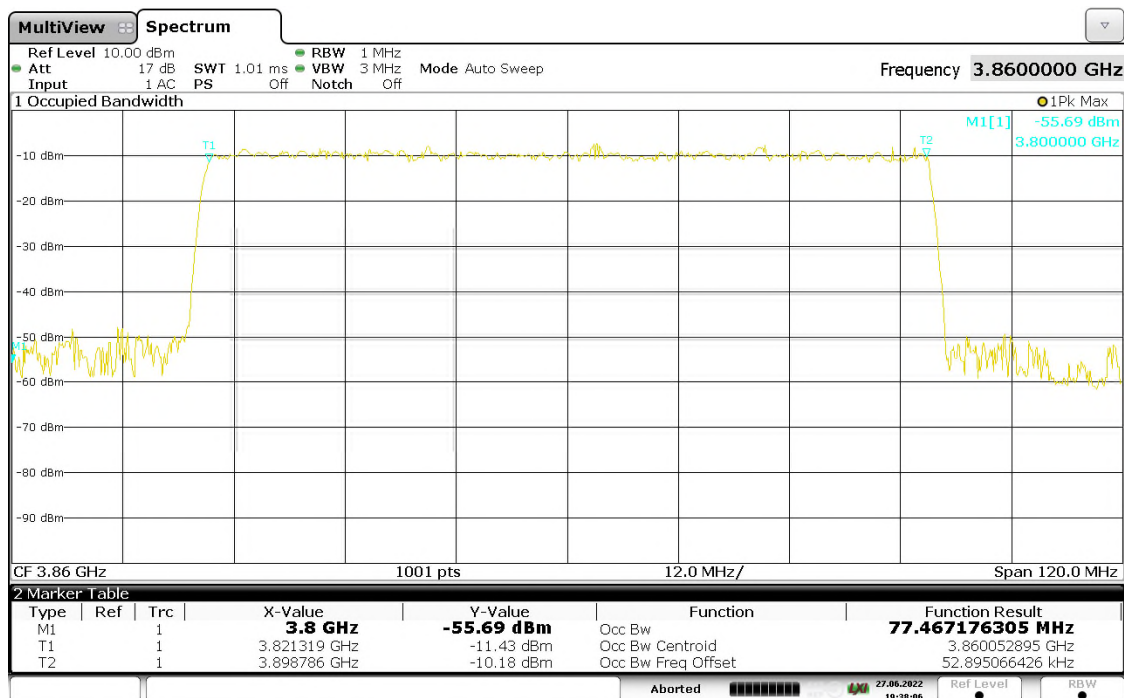
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Figure 118: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 64QAM_TM3.1, High Channel, TX Port 17



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Figure 119: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 64QAM_TM3.1, High Channel, TX Port 25



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Figure 120: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 256QAM_TM3.1a, High Channel, TX Port 17

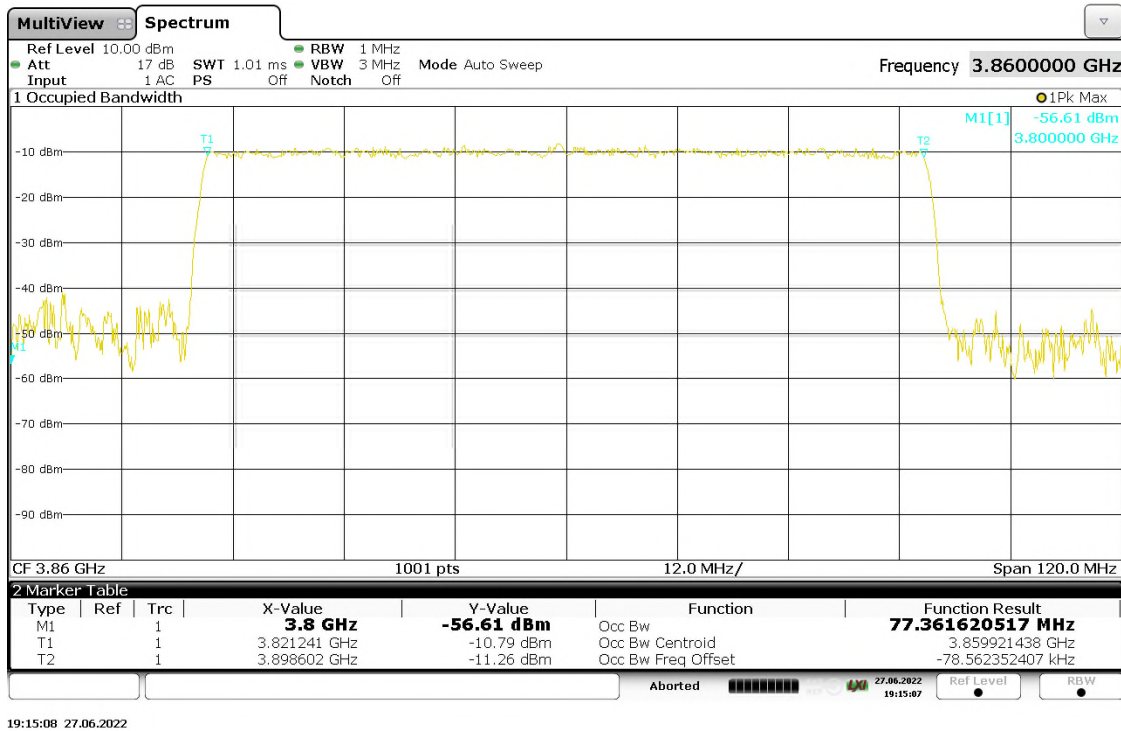
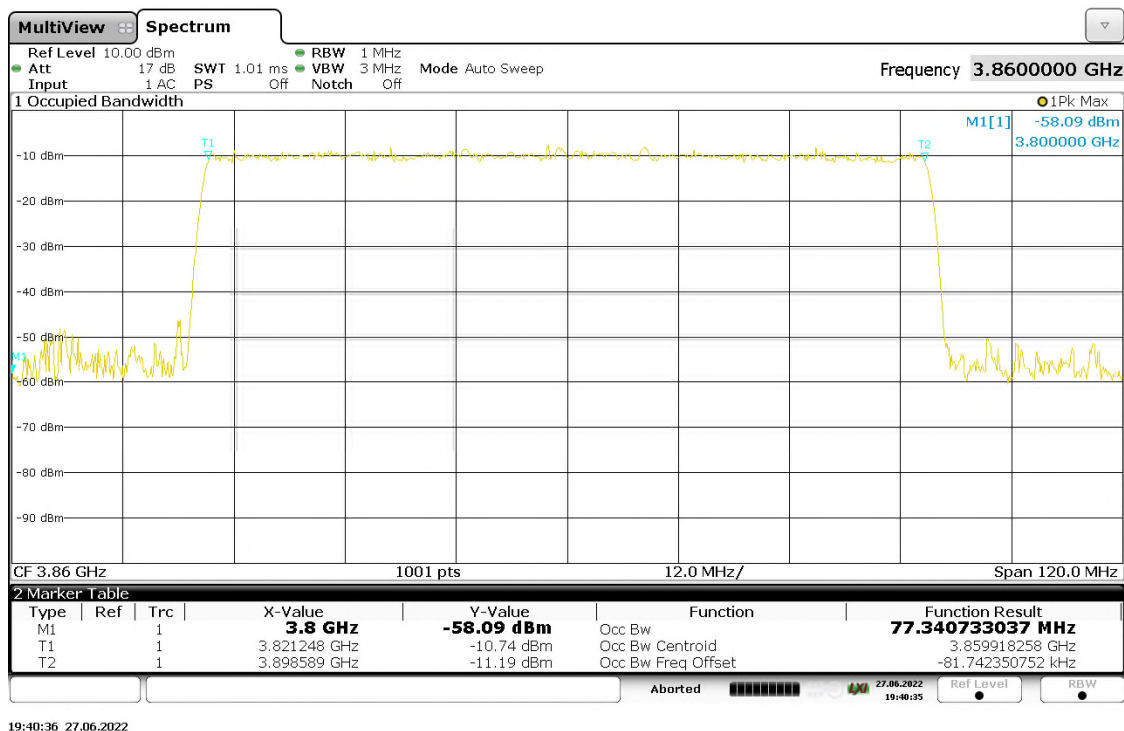


Figure 121: 99% Occupied Bandwidth, 1cc-3 (OBW=80MHz), 256QAM_TM3.1a, High Channel, TX Port 25



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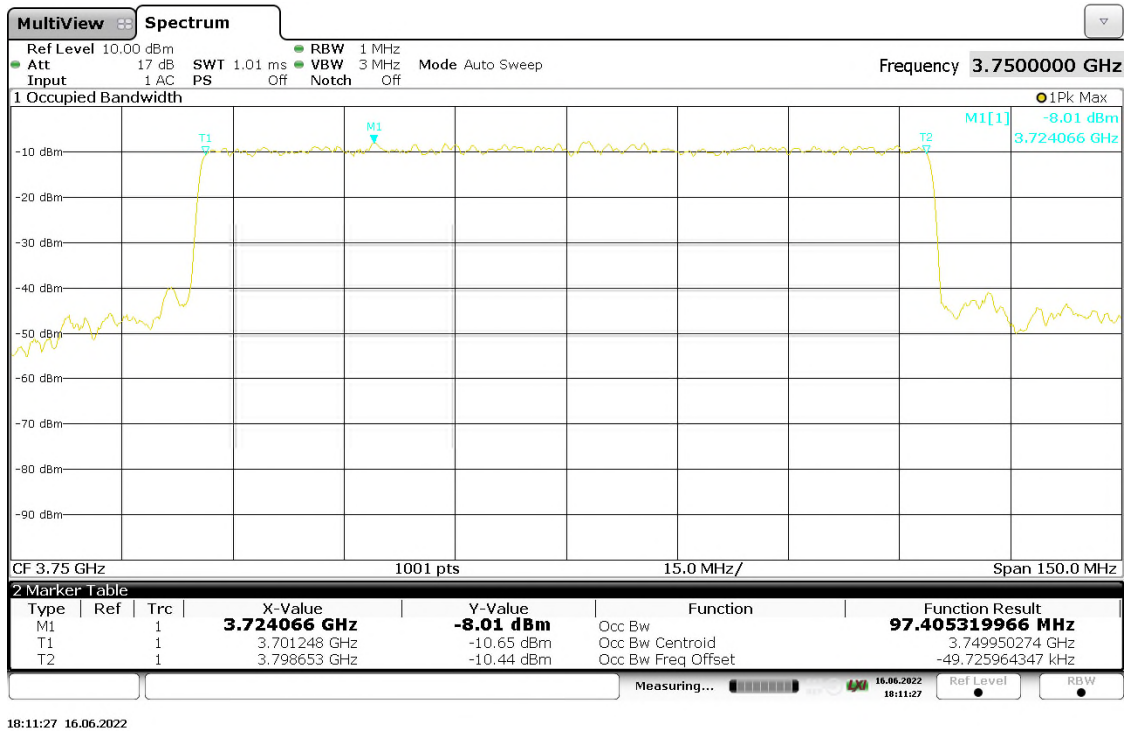
Table 85: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), Low Channel

TX Port	99% Occupied Bandwidth [MHz]			
	QPSK_TM1.1	16QAM_TM3.2	64QAM_TM3.1	256QAM_TM3.1a
12	97.405	97.500	97.246	97.317
28	97.378	97.499	97.261	97.297

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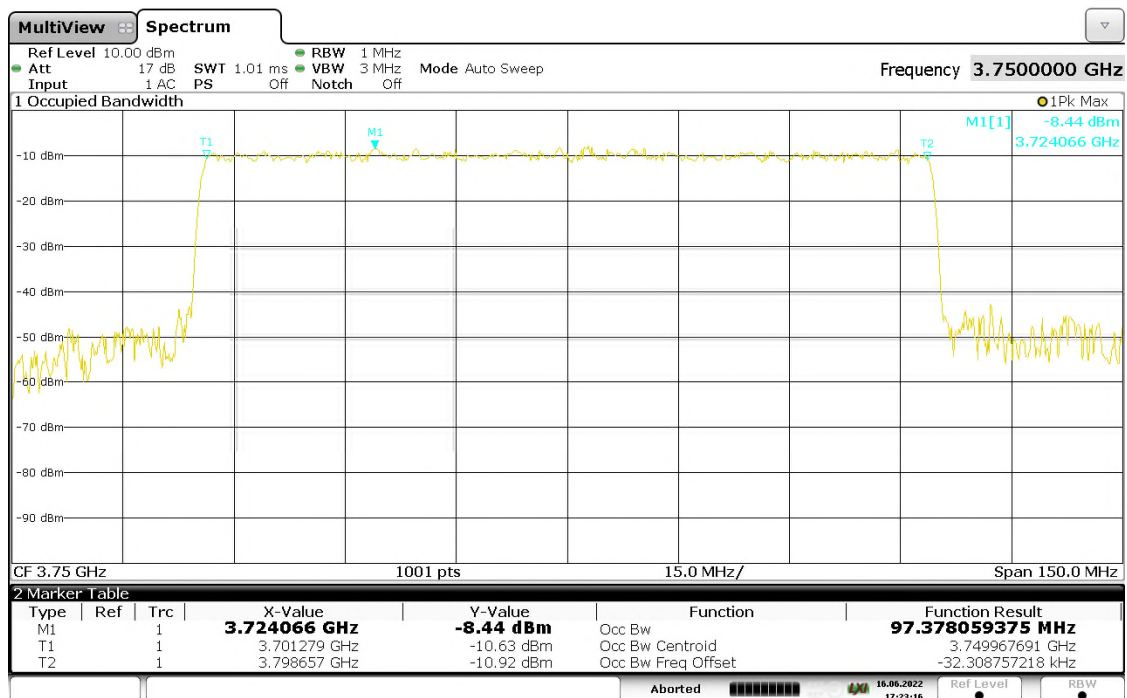
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Figure 122: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), QPSK_TM1.1, Low Channel, TX Port 12



18:11:27 16.06.2022

Figure 123: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), QPSK_TM1.1, Low Channel, TX Port 28

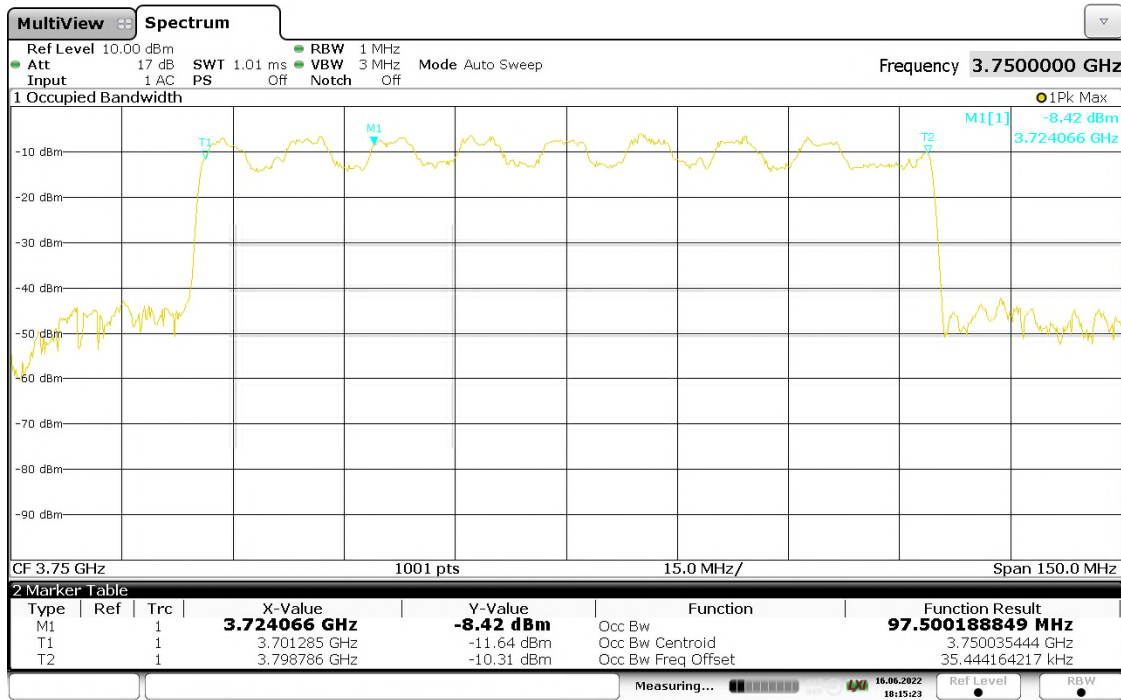


17:23:16 16.06.2022

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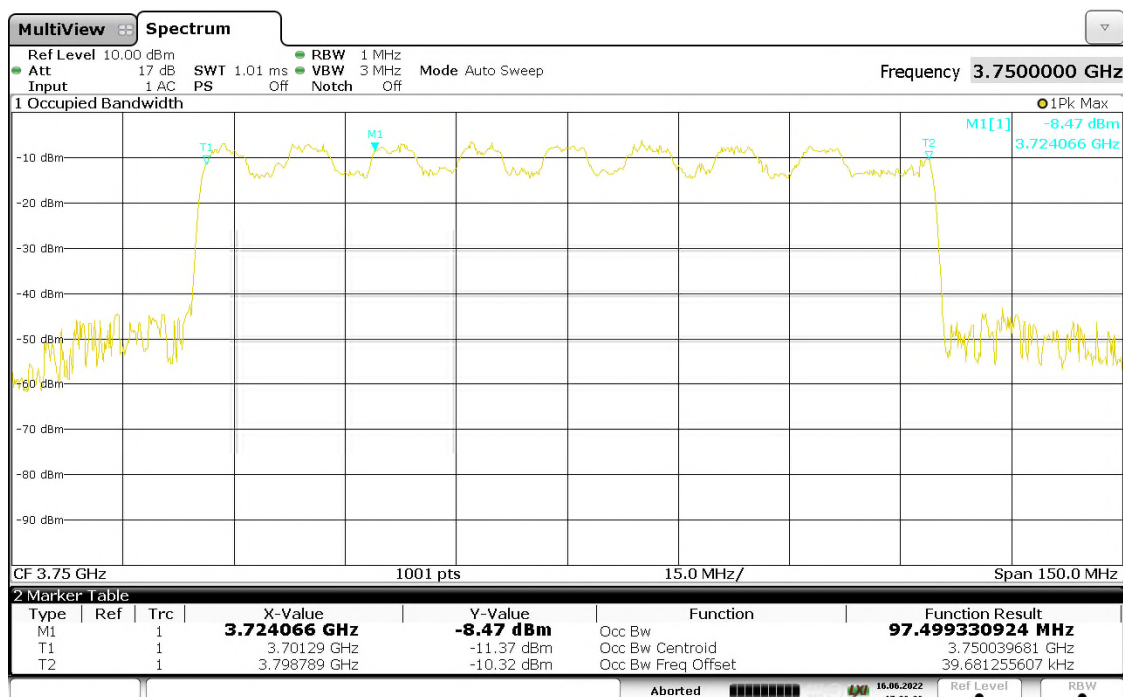
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Figure 124: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 16QAM_TM3.2, Low Channel, TX Port 12



18:15:24 16.06.2022

Figure 125: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 16QAM_TM3.2, Low Channel, TX Port 28

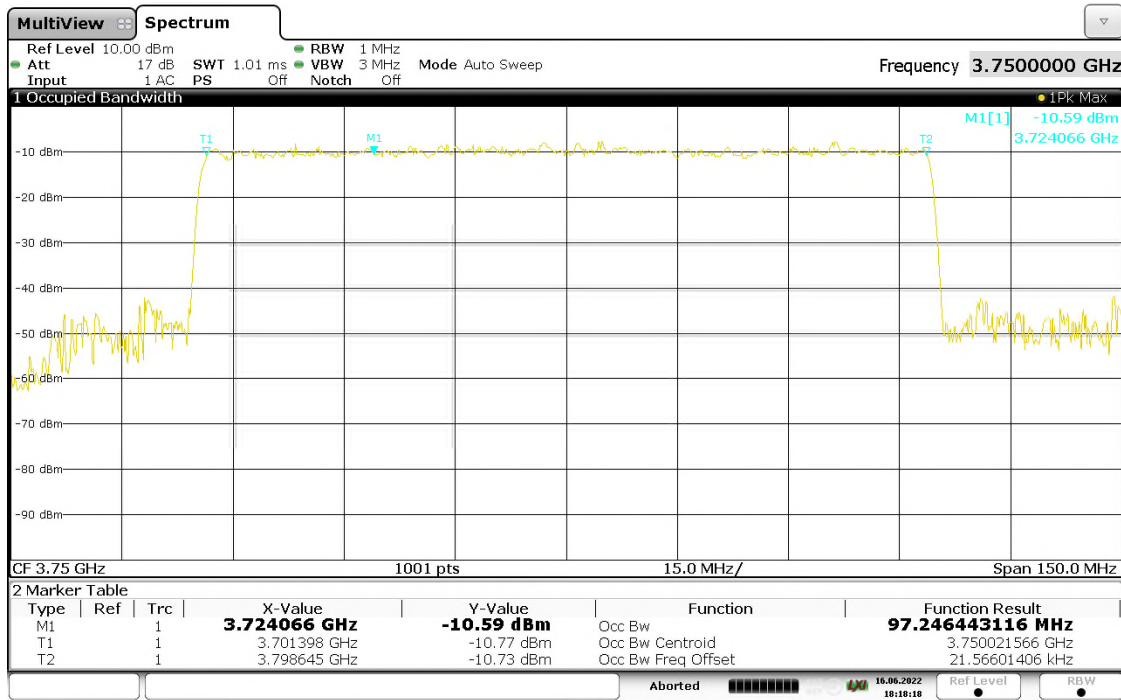


17:26:26 16.06.2022

Prüfbericht-Nr.: **JP223Y4L 001**
Test Report No.:

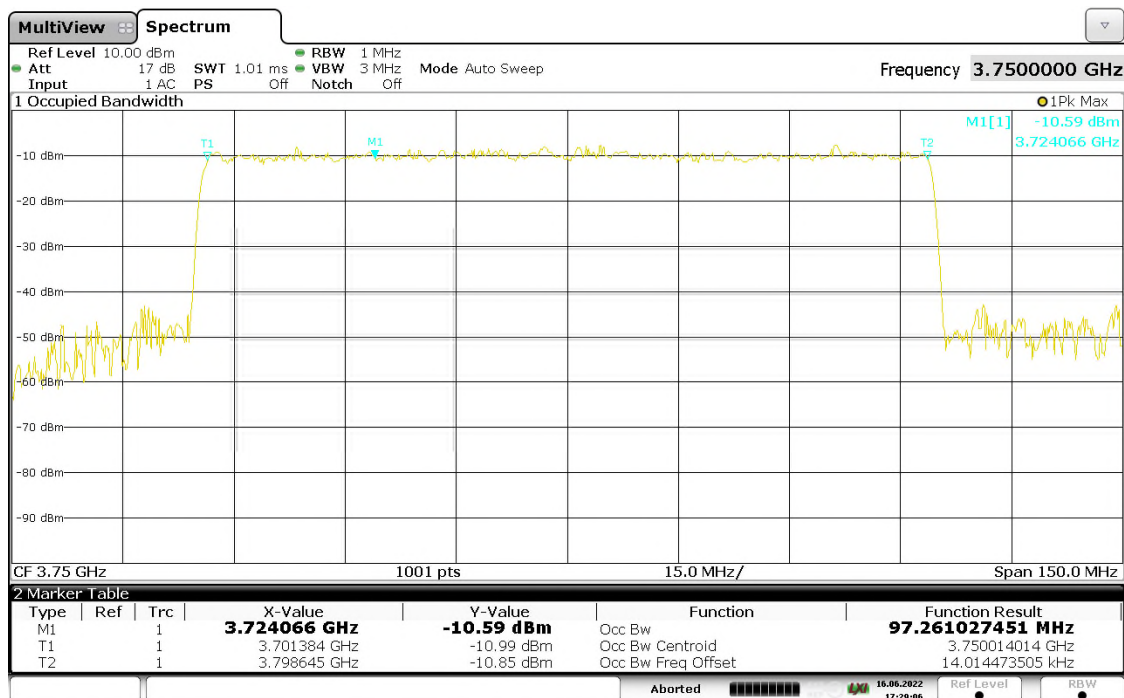
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Figure 126: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 64QAM_TM3.1, Low Channel, TX Port 12



18:18:18 16.06.2022

Figure 127: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 64QAM_TM3.1, Low Channel, TX Port 28



17:29:07 16.06.2022

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Figure 128: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 256QAM_TM3.1a, Low Channel, TX Port 12

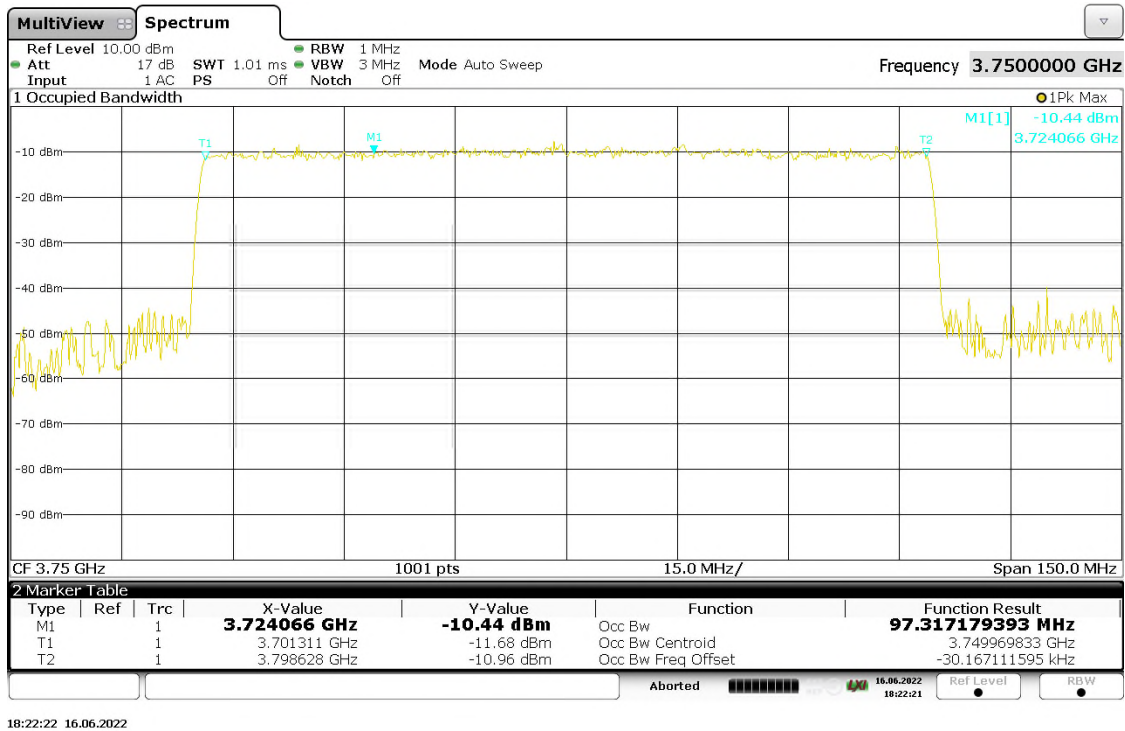
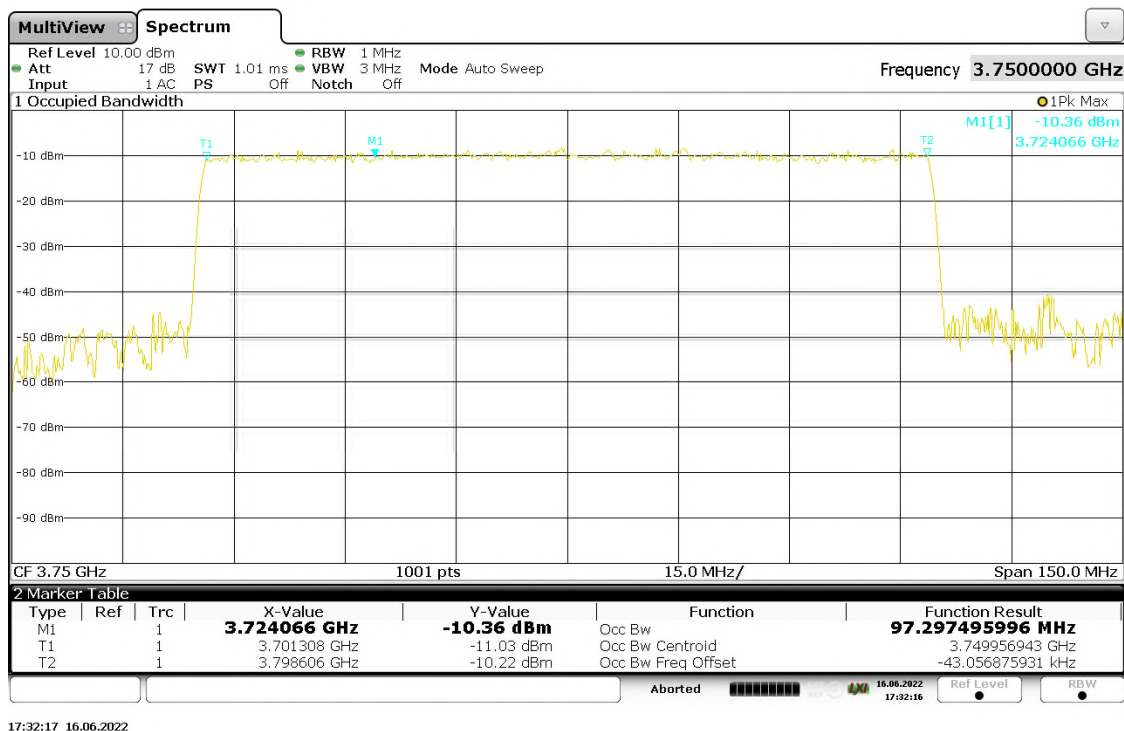


Figure 129: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 256QAM_TM3.1a, Low Channel, TX Port 28



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Table 86: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), Middle Channel

TX Port	99% Occupied Bandwidth [MHz]			
	QPSK_TM1.1	16QAM_TM3.2	64QAM_TM3.1	256QAM_TM3.1a
23	97.432	97.584	97.331	97.310
31	97.434	97.621	97.371	97.356

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Figure 130: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), QPSK_TM1.1, Middle Channel, TX Port 23

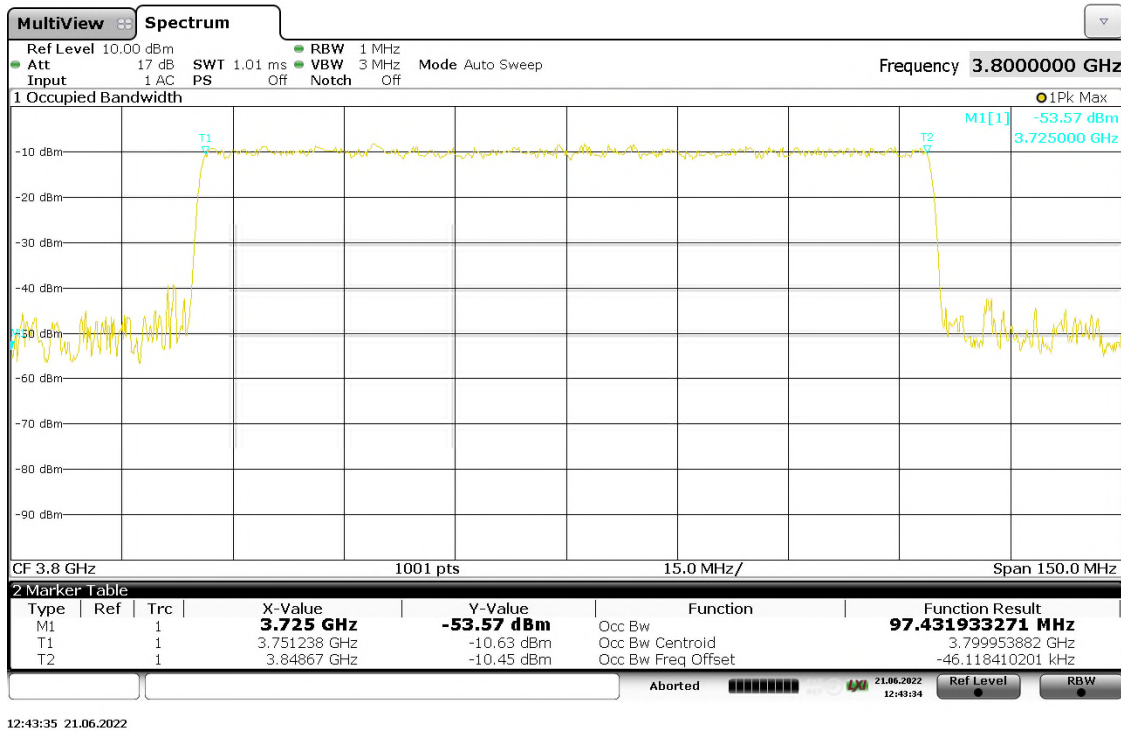
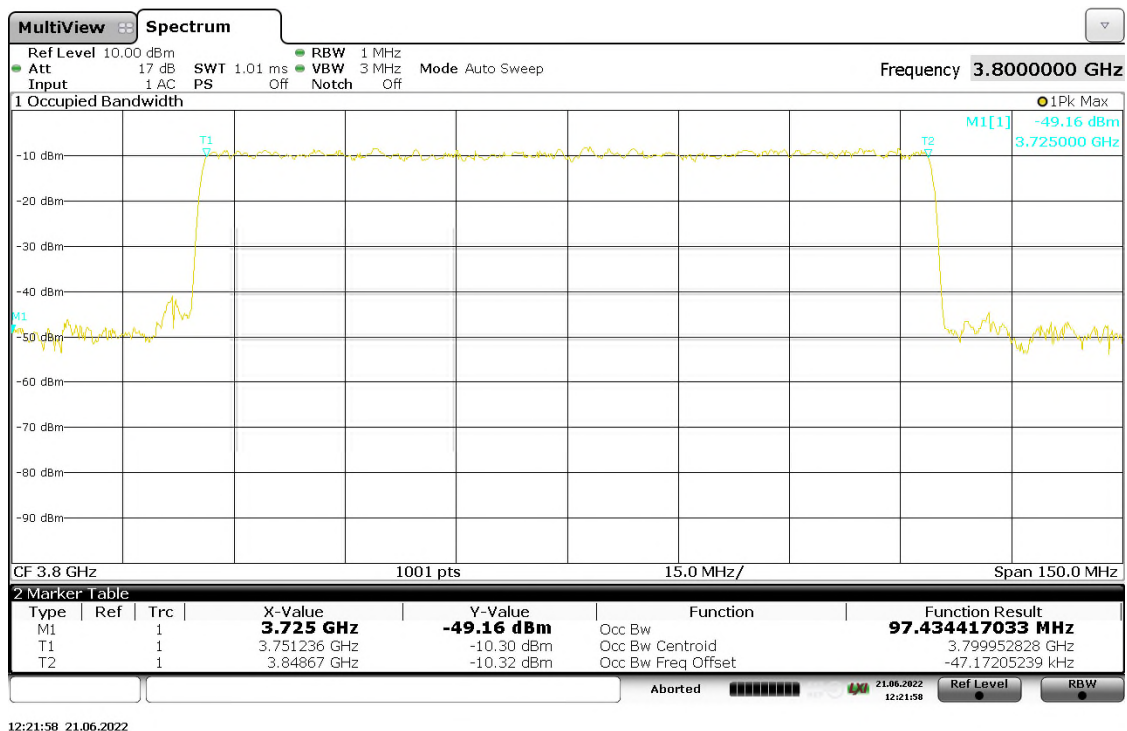


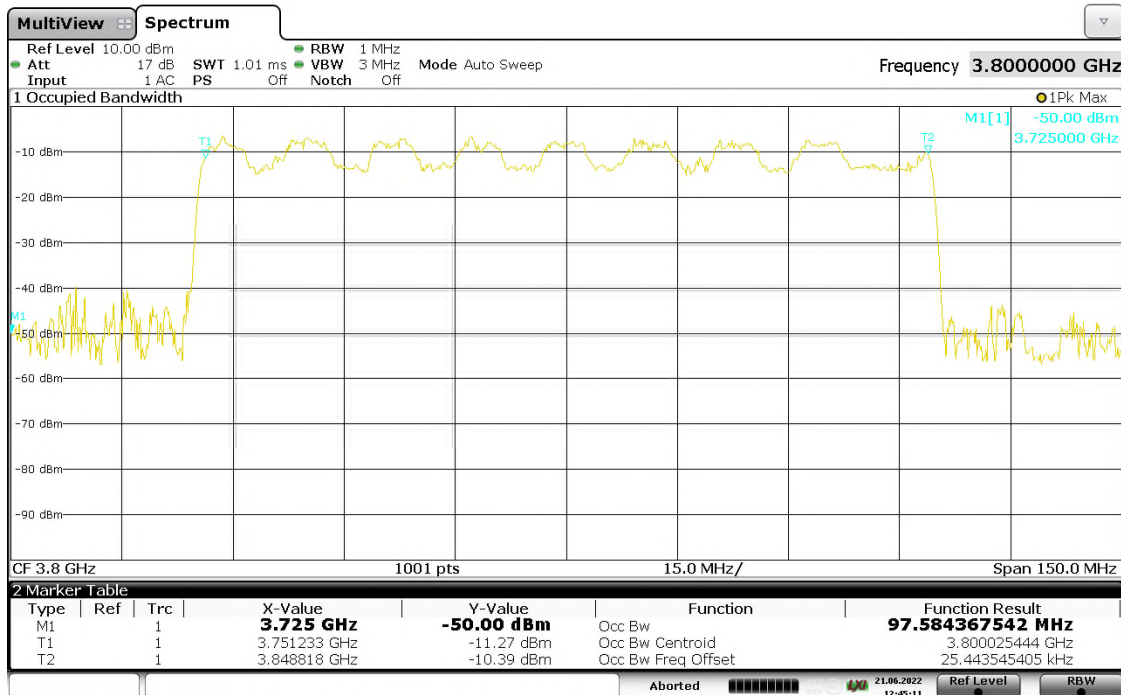
Figure 131: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), QPSK_TM1.1, Middle Channel, TX Port 31



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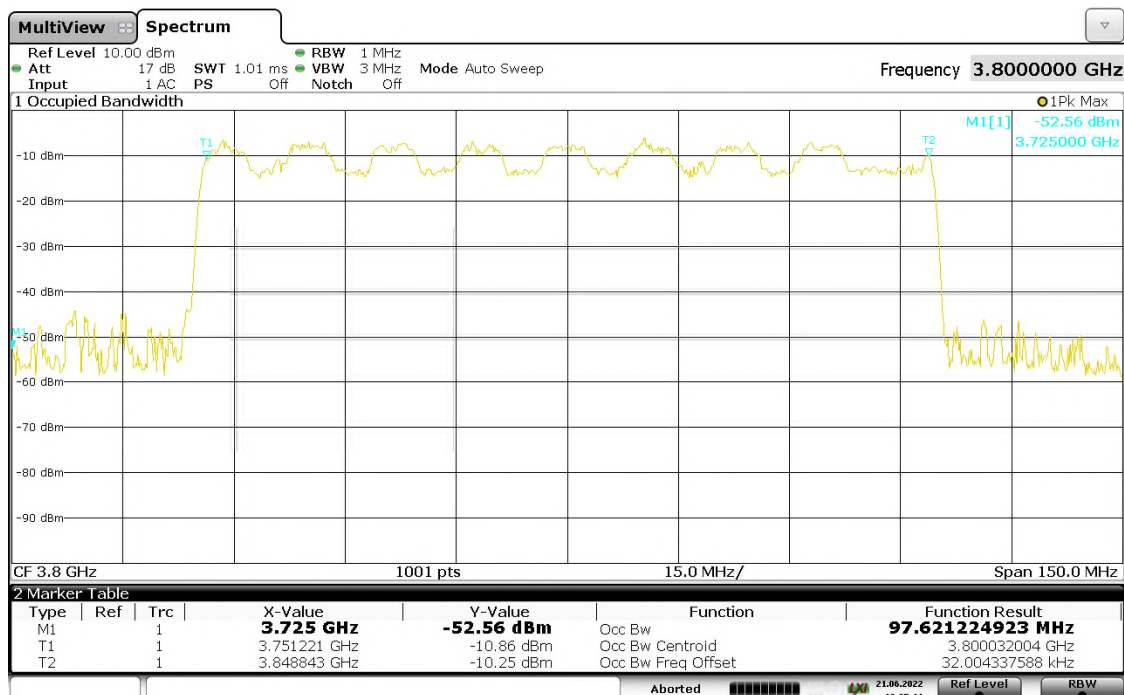
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Figure 132: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 16QAM_TM3.2, Middle Channel, TX Port 23



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Figure 133: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 16QAM_TM3.2, Middle Channel, TX Port 31



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Figure 134: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 64QAM_TM3.1, Middle Channel, TX Port 23

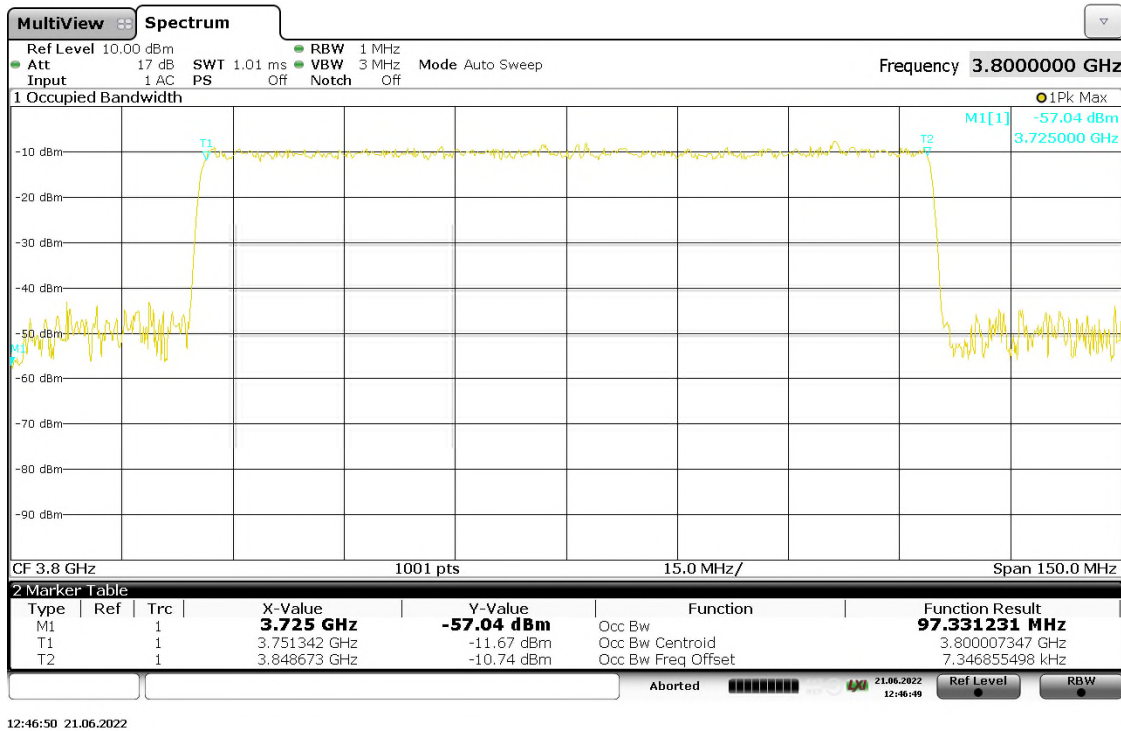
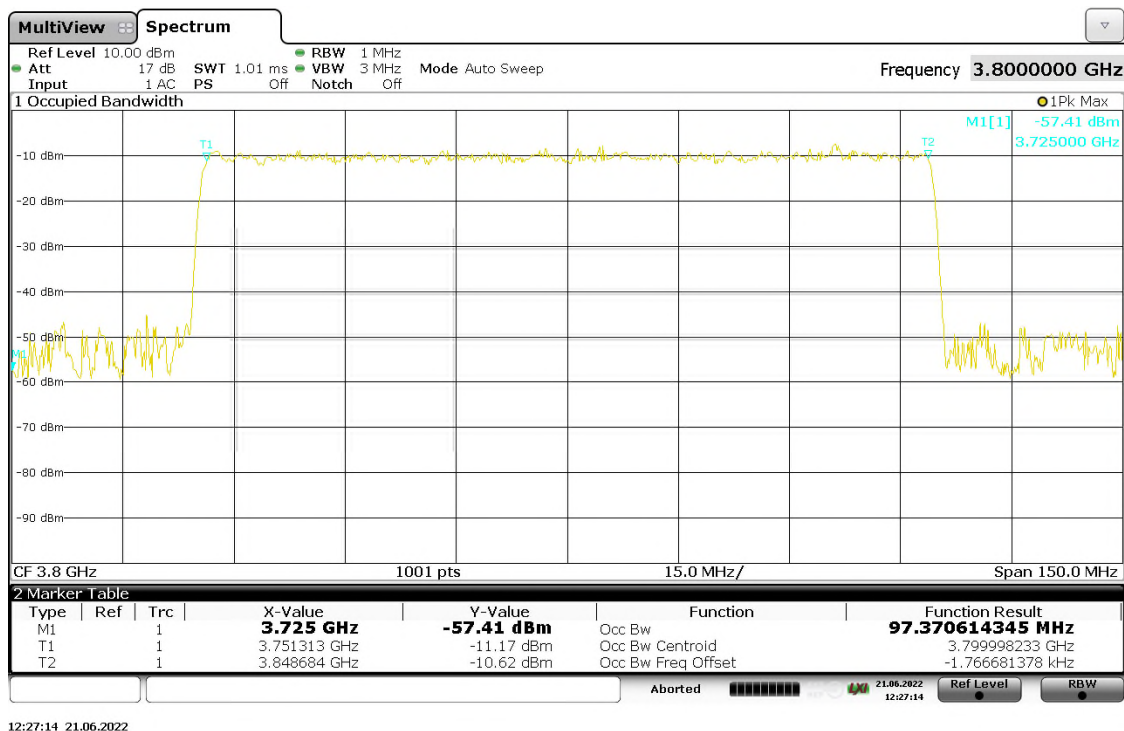


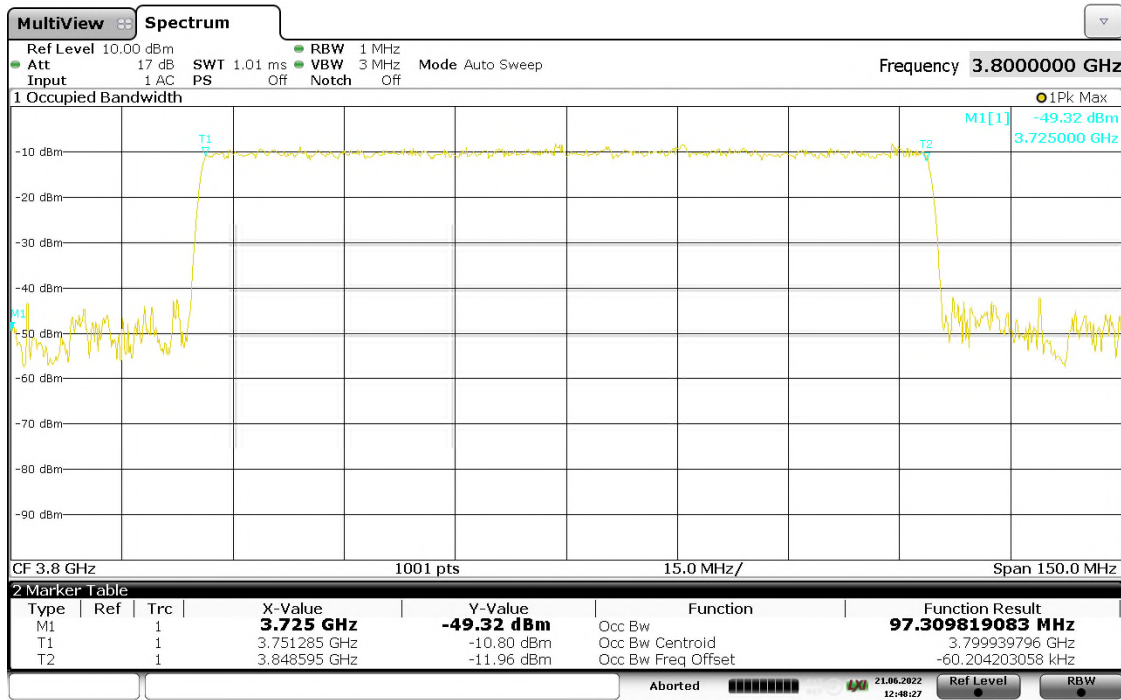
Figure 135: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 64QAM_TM3.1, Middle Channel, TX Port 31



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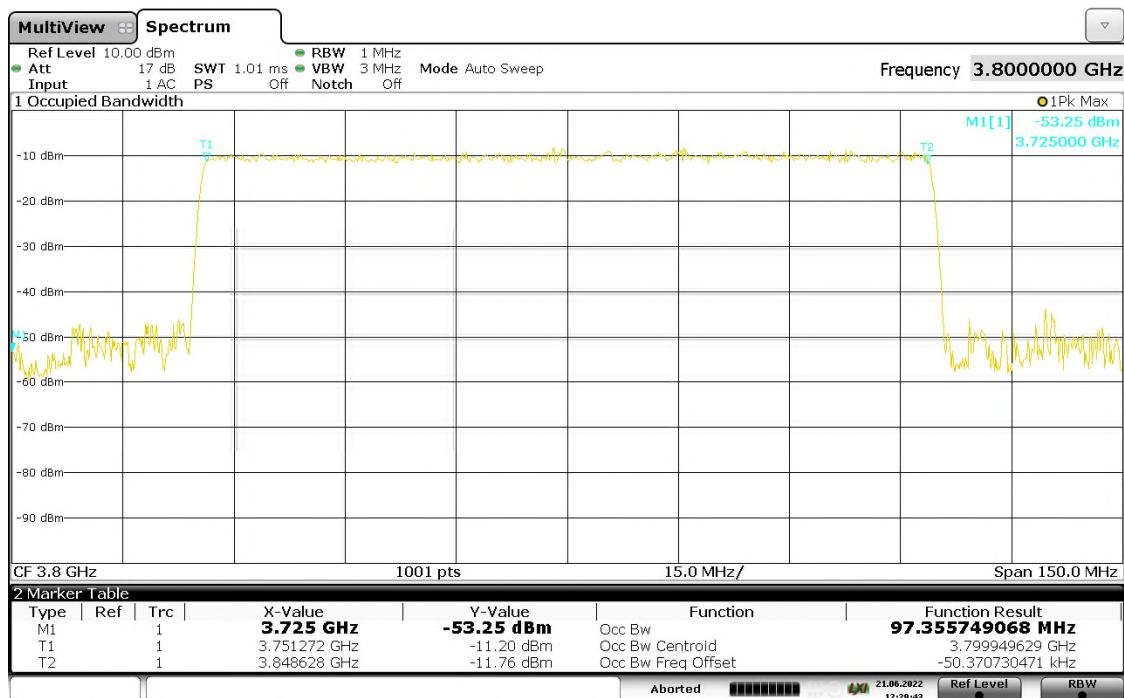
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Figure 136: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 256QAM_TM3.1a, Middle Channel, TX Port 23



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Figure 137: 99% Occupied Bandwidth, 1cc-4 (OBW=100MHz), 256QAM_TM3.1a, Middle Channel, TX Port 31



12:29:44 21.06.2022