

BASE-2A

Rev C

Schematic

SHEET Description

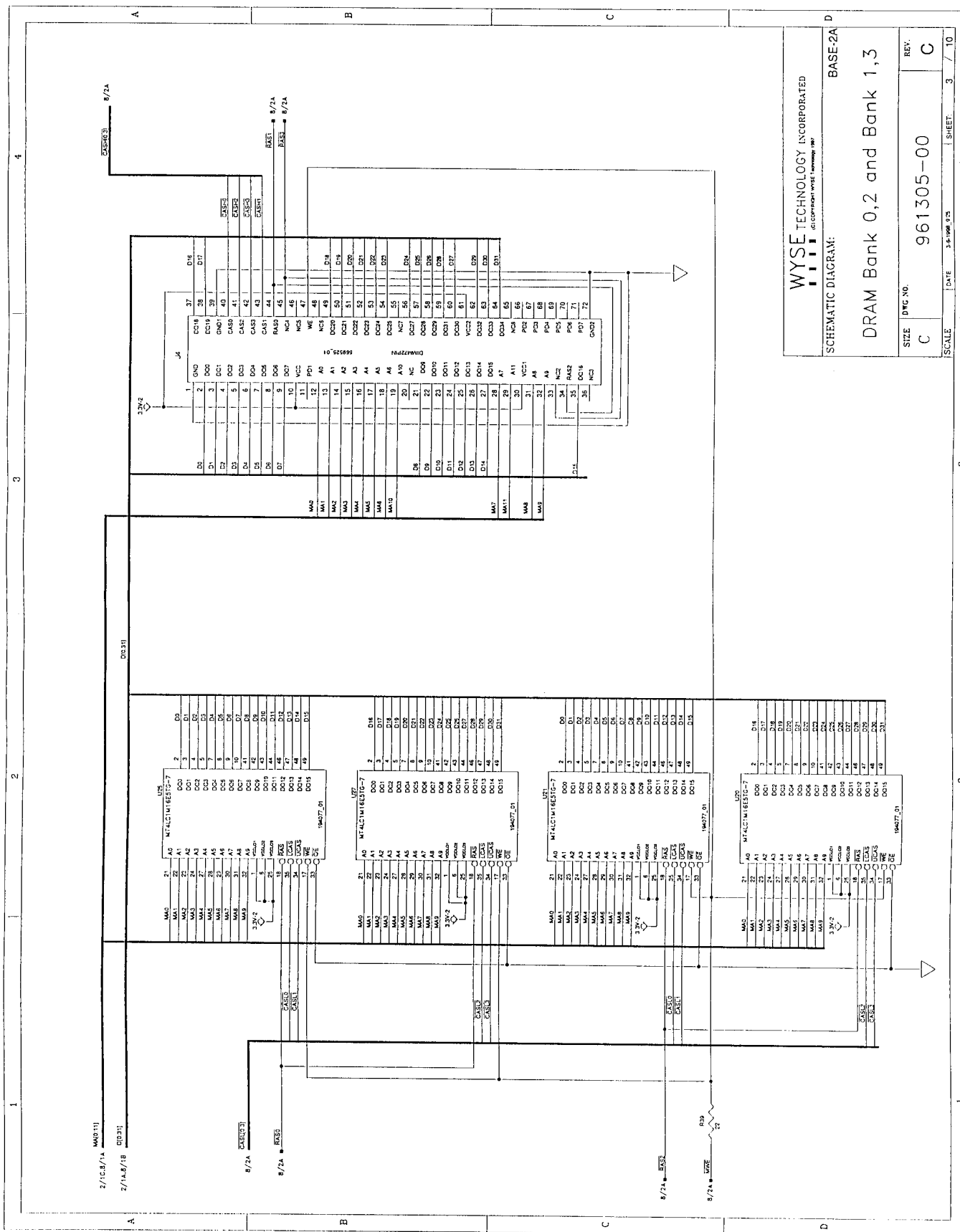
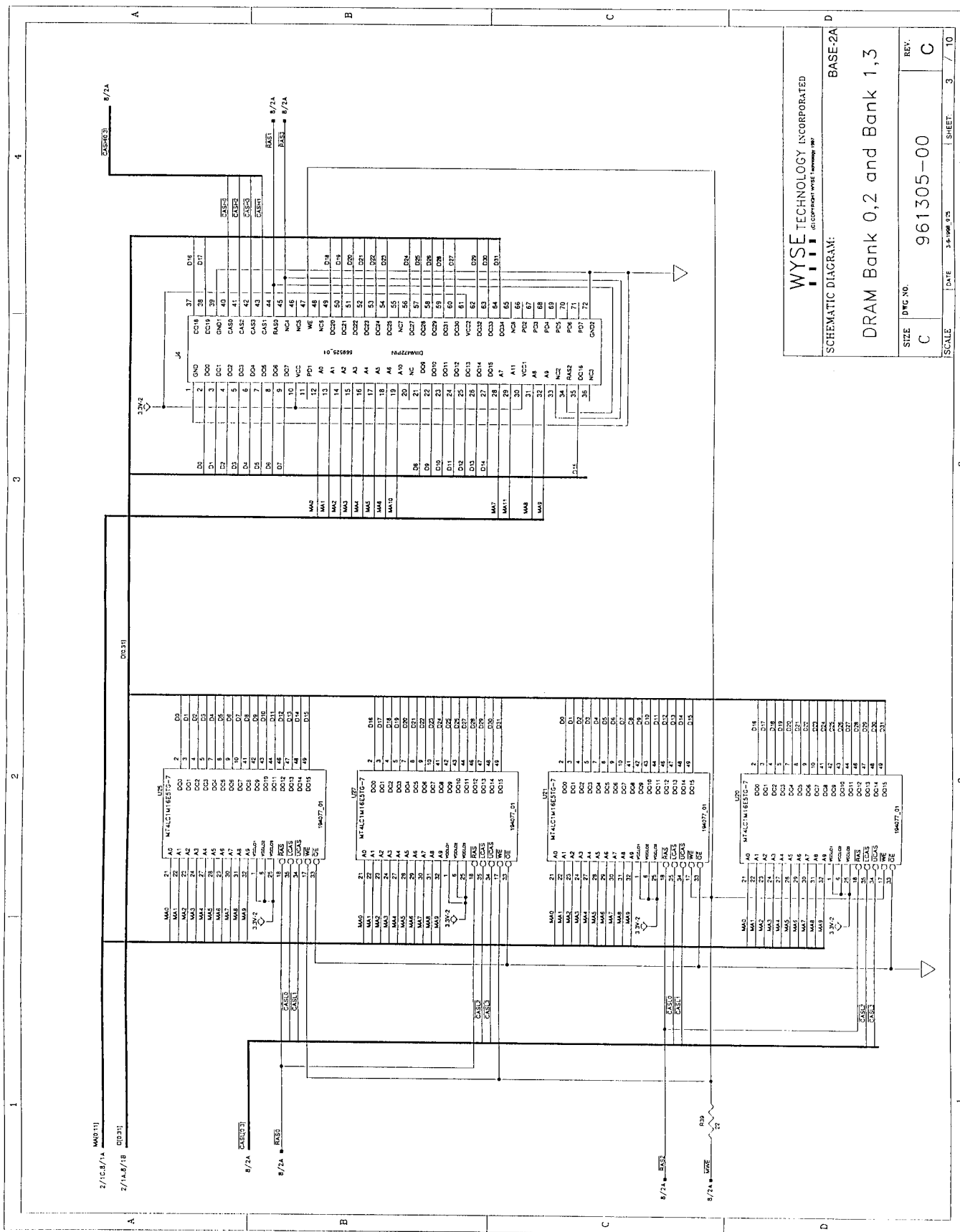
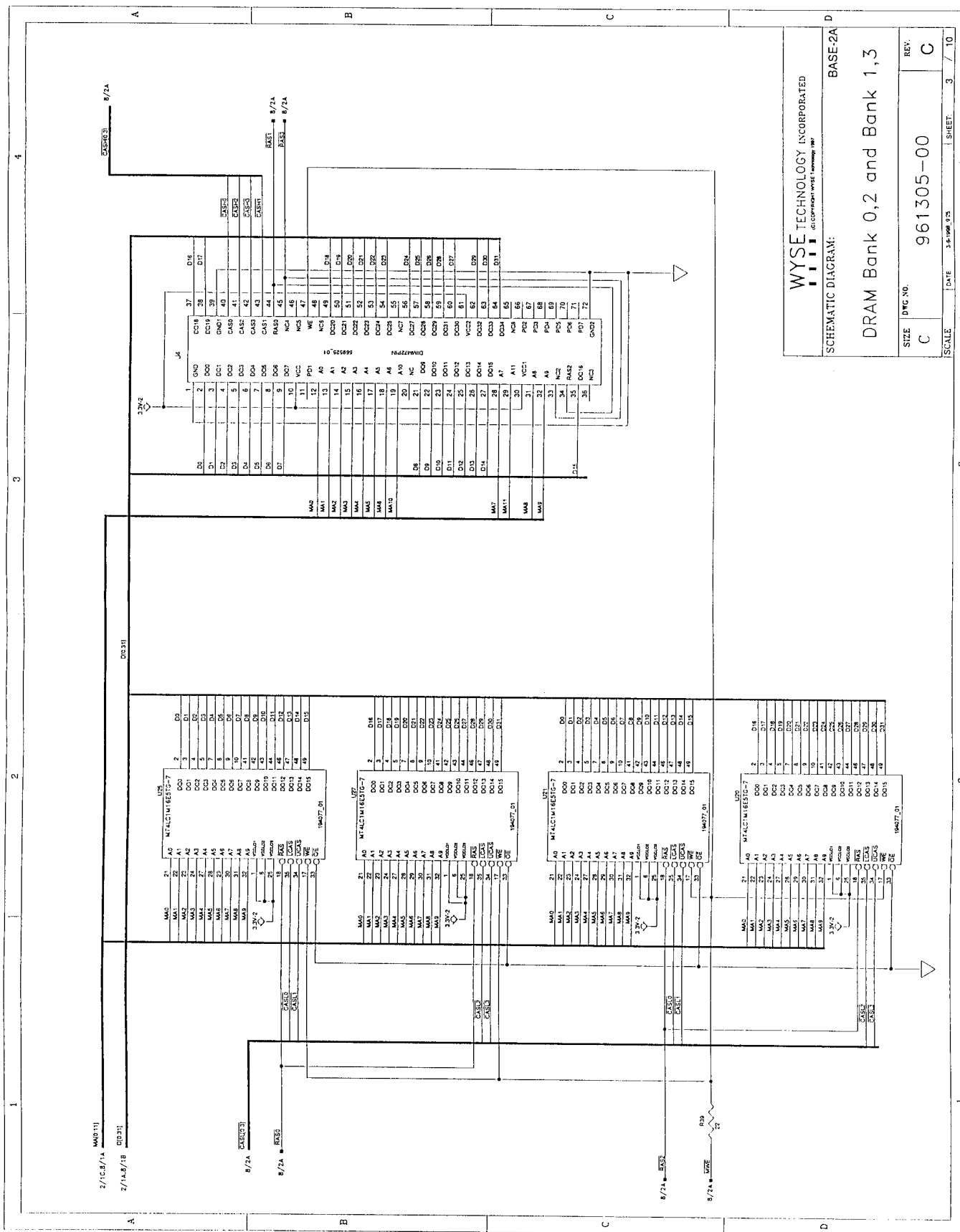
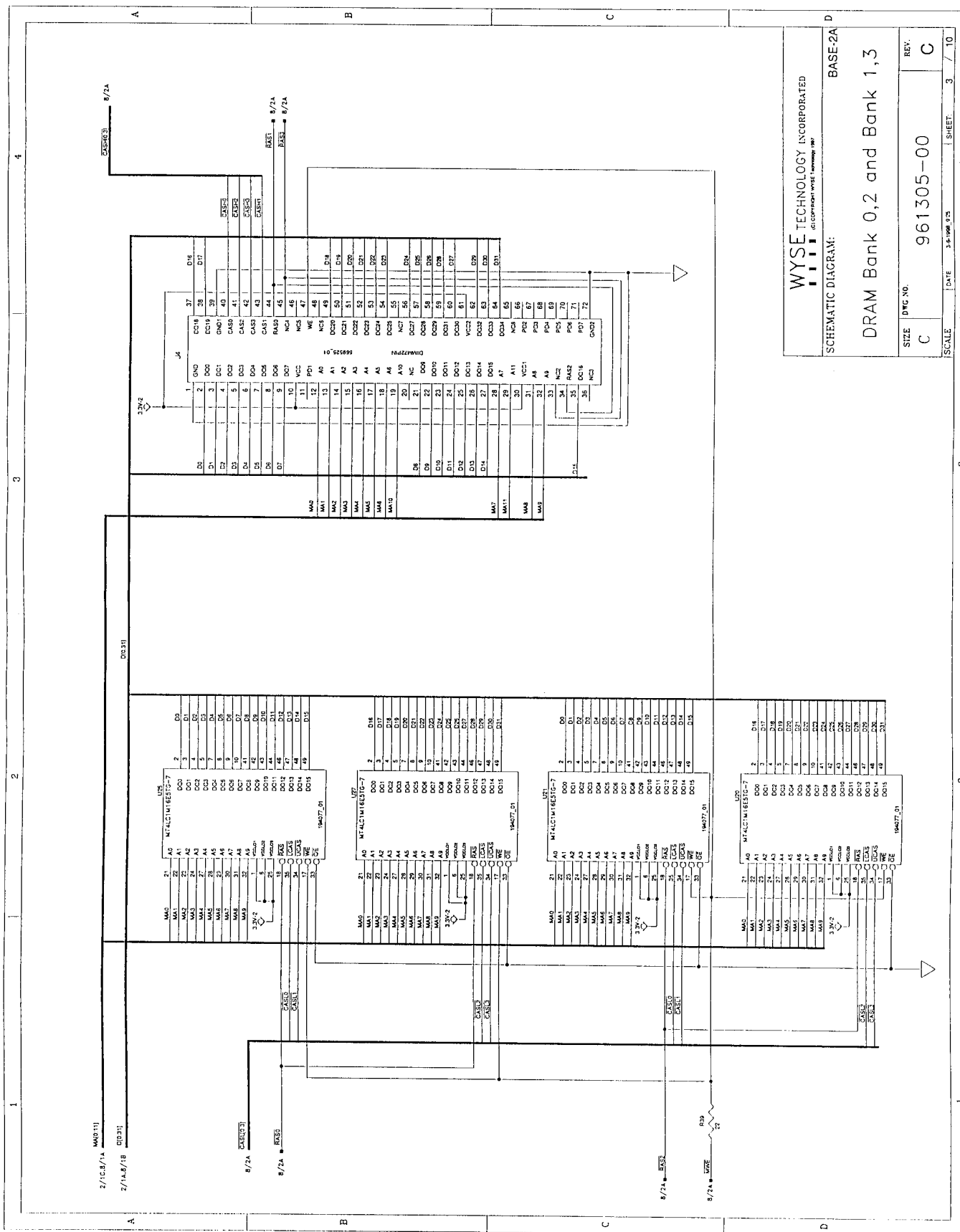
- 2 Data Bus Buffer
- 3 DRAM Bank 0,2 and Bank 1,3
- 4 FLASH Memory
- 5 Audio and Network
- 6 Graphics Controller
- 7 Video, Power Connectors and Bypass Cap.
- 8 System Controller
- 9 Serial and Parallel Ports
- 10 PC-Card Socket, ISA Connector and Keyboard Controller

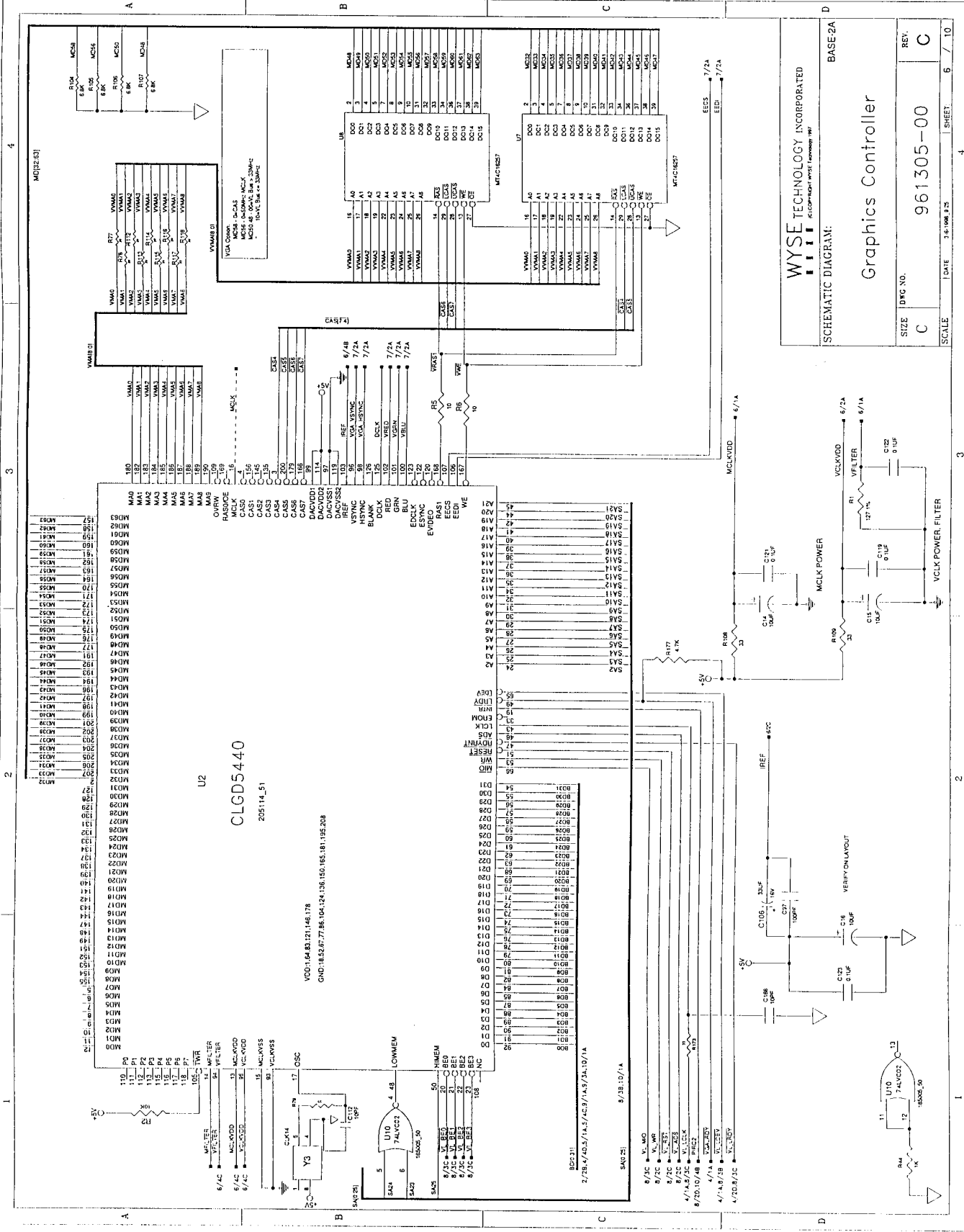
MARS

PCB Layout Copy

CONTRACT NO.		WYSE TECHNOLOGY INCORPORATED	
APPROVAL		SCHEMATIC DIAGRAM:	
DRAWN	LOUISA VU	BASE-2A	
CHK	RONIT LEL	Winterm BASE-2A	
ENG	T. WEY	Logic PCBA	
ISSUED		SIZE	DWG NO.
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DATE		SCALE	REV.
3-11-1998		1:1	C
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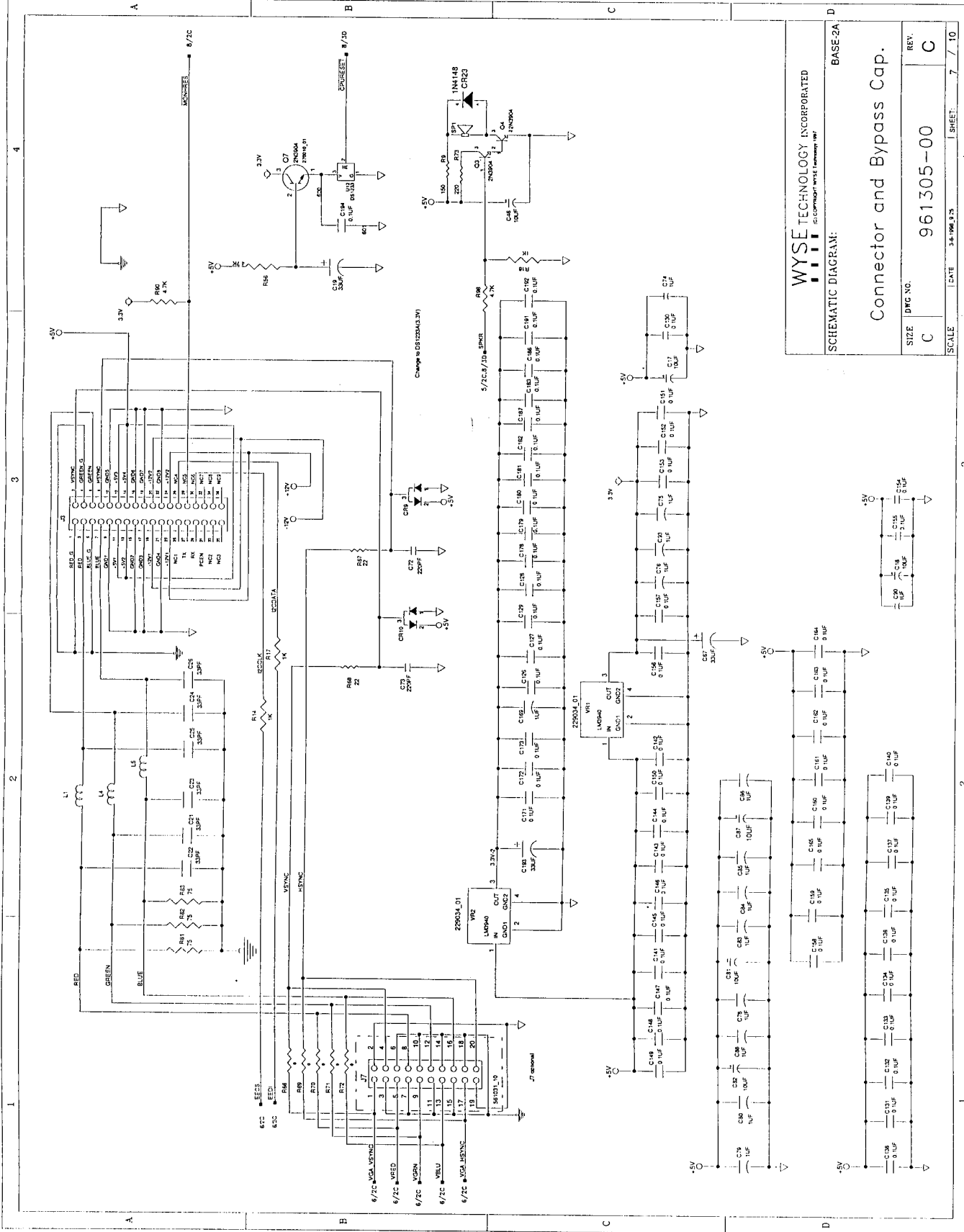




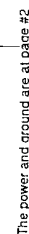
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Graphics Controller

REV. C
SIZE DWG NO. 961305-00
SCALE DATE 3-6-88 9-5 SHEET 6 / 10



WYSE TECHNOLOGY INCORPORATED		BASE-2A	
SCHEMATIC DIAGRAM:		Connector and Bypass Cap.	
SIZE	DWG NO.	REV.	
C	961305-00	C	
SCALE	DATE	SHEET	
	1.4.1988 3.75	7	10

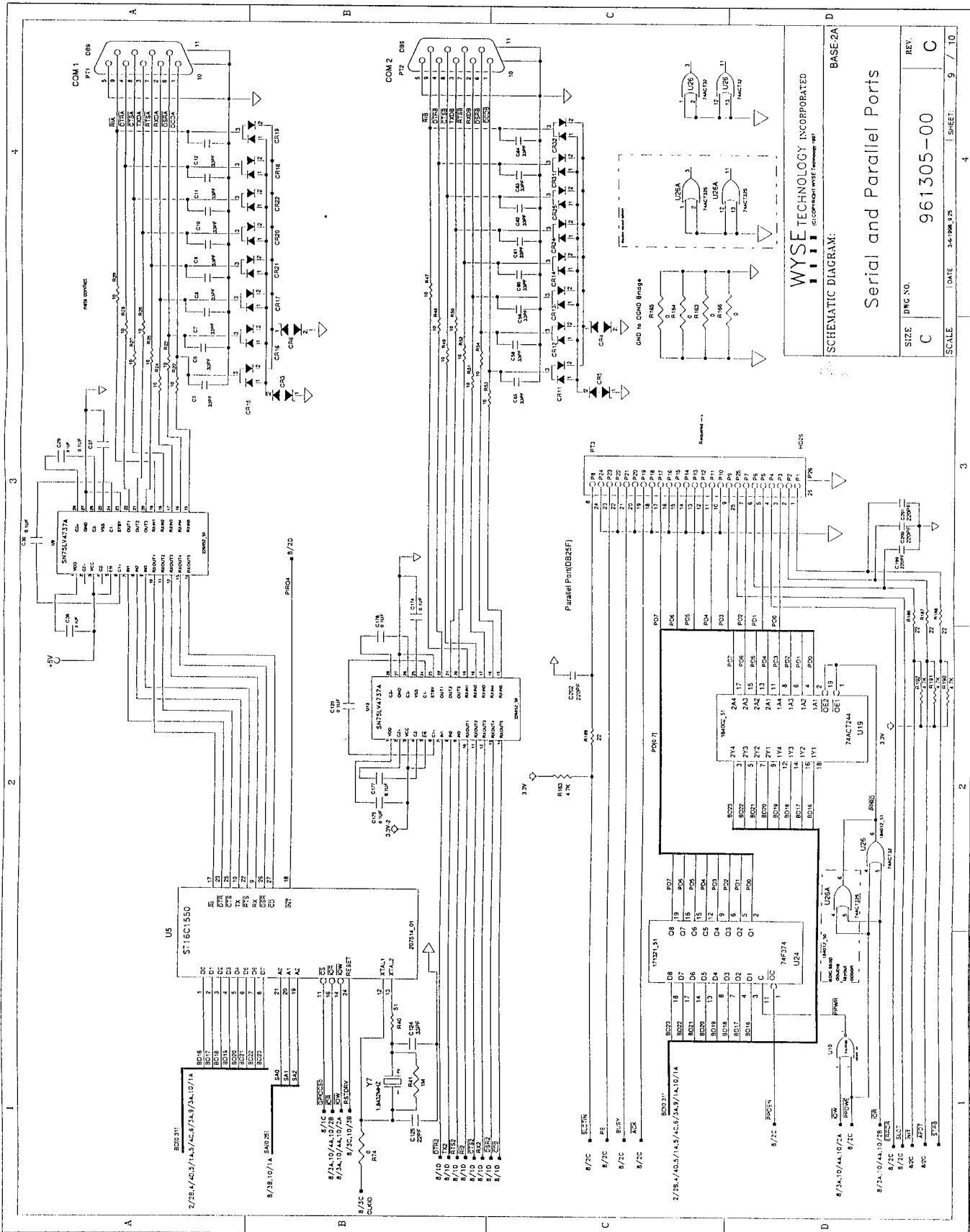


PCMVCCSV and PCMVPSV are analogs to PCMVCC and PCMVPS that have the same structure as Figure 1.

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■■■■
(A) COMPACT WISE Technology 1997

System Controller

SIZE	DWG NO.	DATE	SHEET	REV.
C	961305-00	3-5-1998 9:21	8	C



WYSE TECHNOLOGY INCORPORATED
SCHEMATIC DIAGRAM

Serial and Parallel Ports

SIZE	DWG NO.	REV.
C	961305-00	C
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3/4\"	9/10	4

