

BASE-2A

Rev C

Schematic

SHEET Description

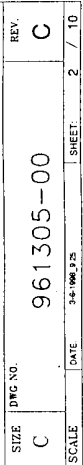
- 2 Data Bus Buffer
- 3 DRAM Bank 0,2 and Bank 1,3
- 4 FLASH Memory
- 5 Audio and Network
- 6 Graphics Controller
- 7 Video, Power Connectors and Bypass Cap.
- 8 System Controller
- 9 Serial and Parallel Ports
- 10 PC-Card Socket, ISA Connector and Keyboard Controller

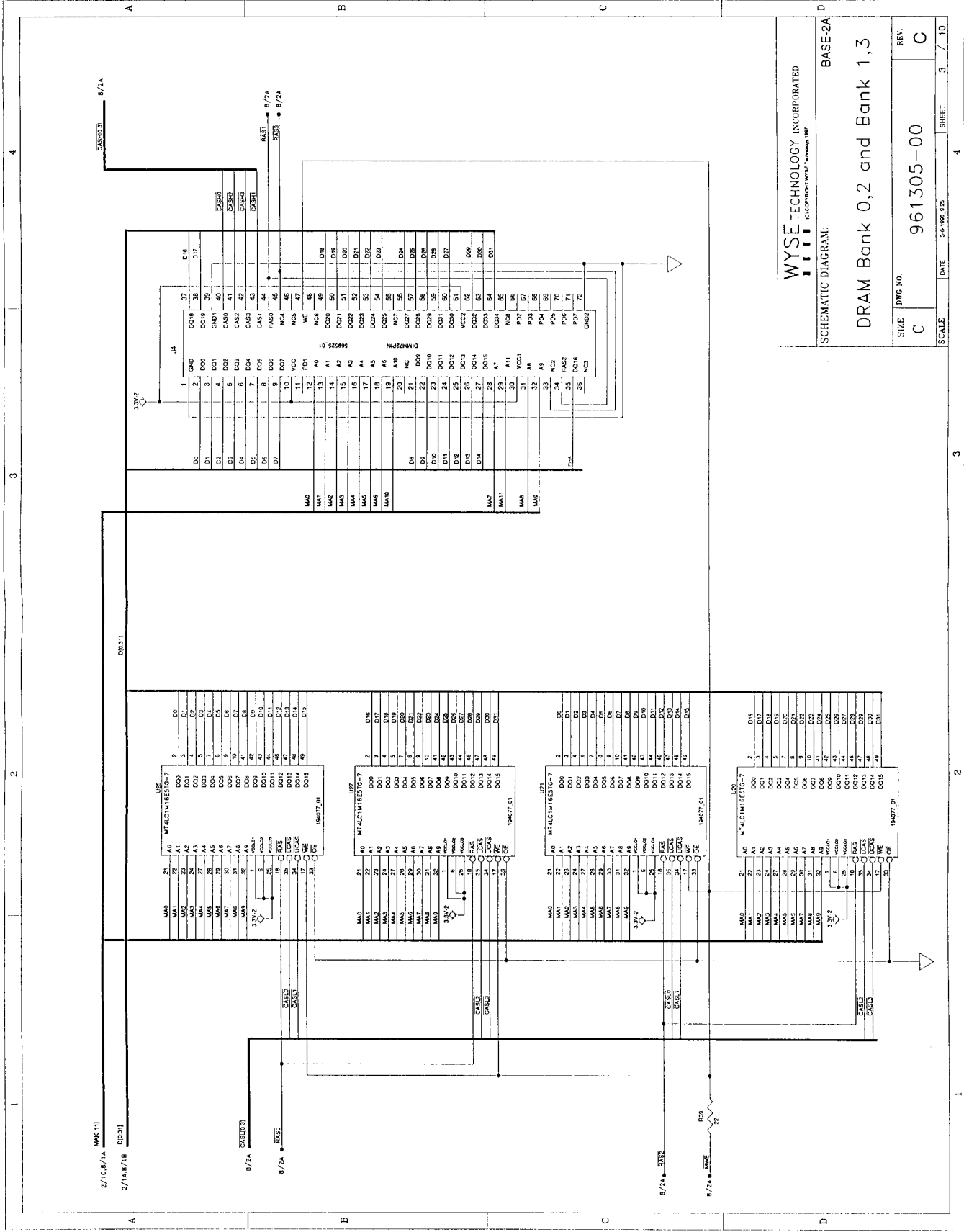
MQIR
PCB Layout Copy

REVISION				DATE	APPROVED
DRAWING	EN	REV	DESCRIPTION	DATE	APPROVED

CONTRACT NO.		WYSE TECHNOLOGY INCORPORATED	
APPROVAL		DATE	
DRAWN	J.QUINN WU	DATE	
CHK	ROHIT TELE	DATE	
ENG	T. MEY	DATE	
ISSUED		DATE	
NOTED: This drawing is for information only. It is not to be used for manufacturing without the approval of the design engineer.		DATE	
WYSE TECHNOLOGY INC.		DATE	
SCALE		DATE	
SIZE		DATE	
C		DATE	
DWC NO.		DATE	
961305-00		DATE	
REV.		DATE	
C		DATE	

3-11-1998_10:22





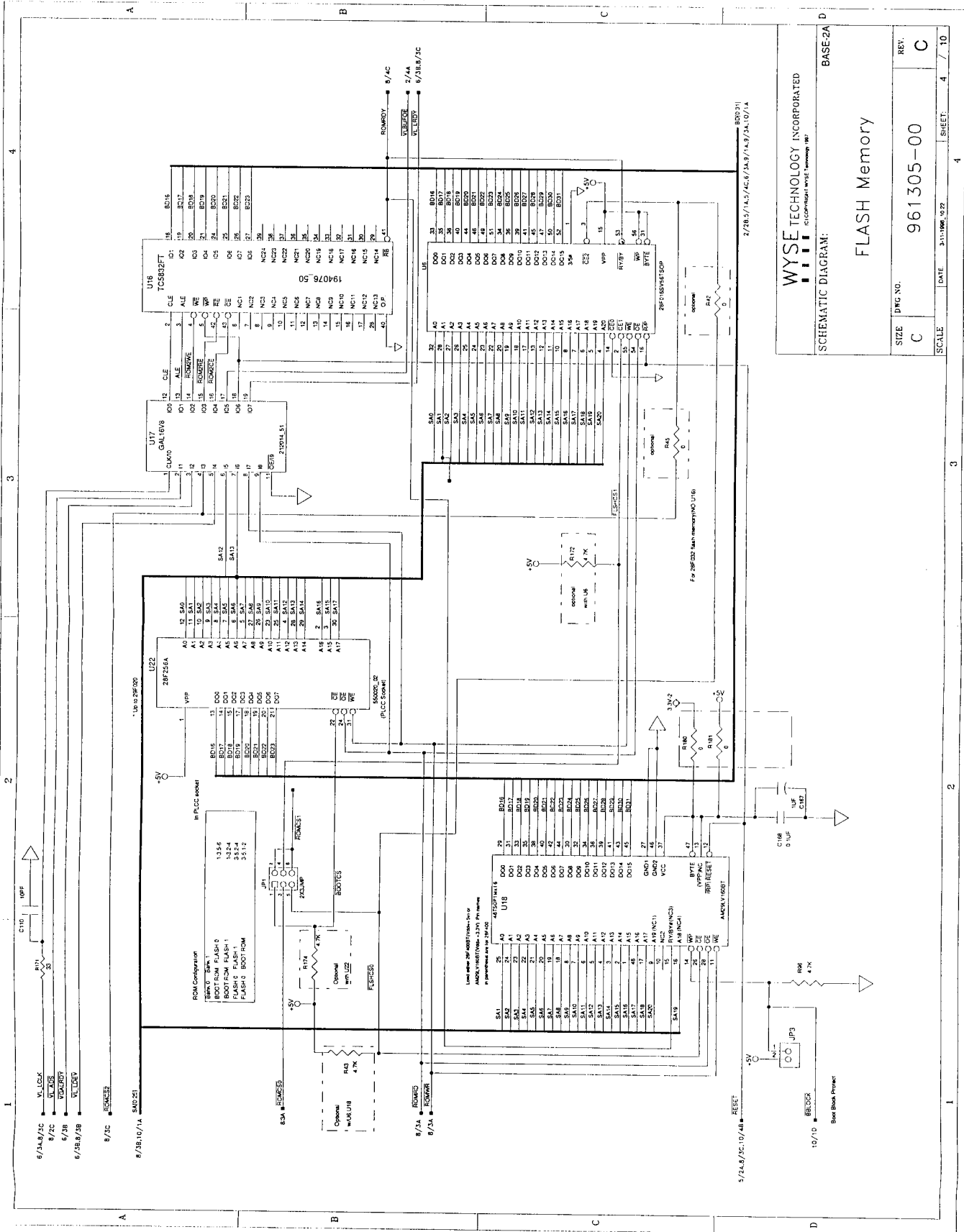
WYSE TECHNOLOGY INCORPORATED
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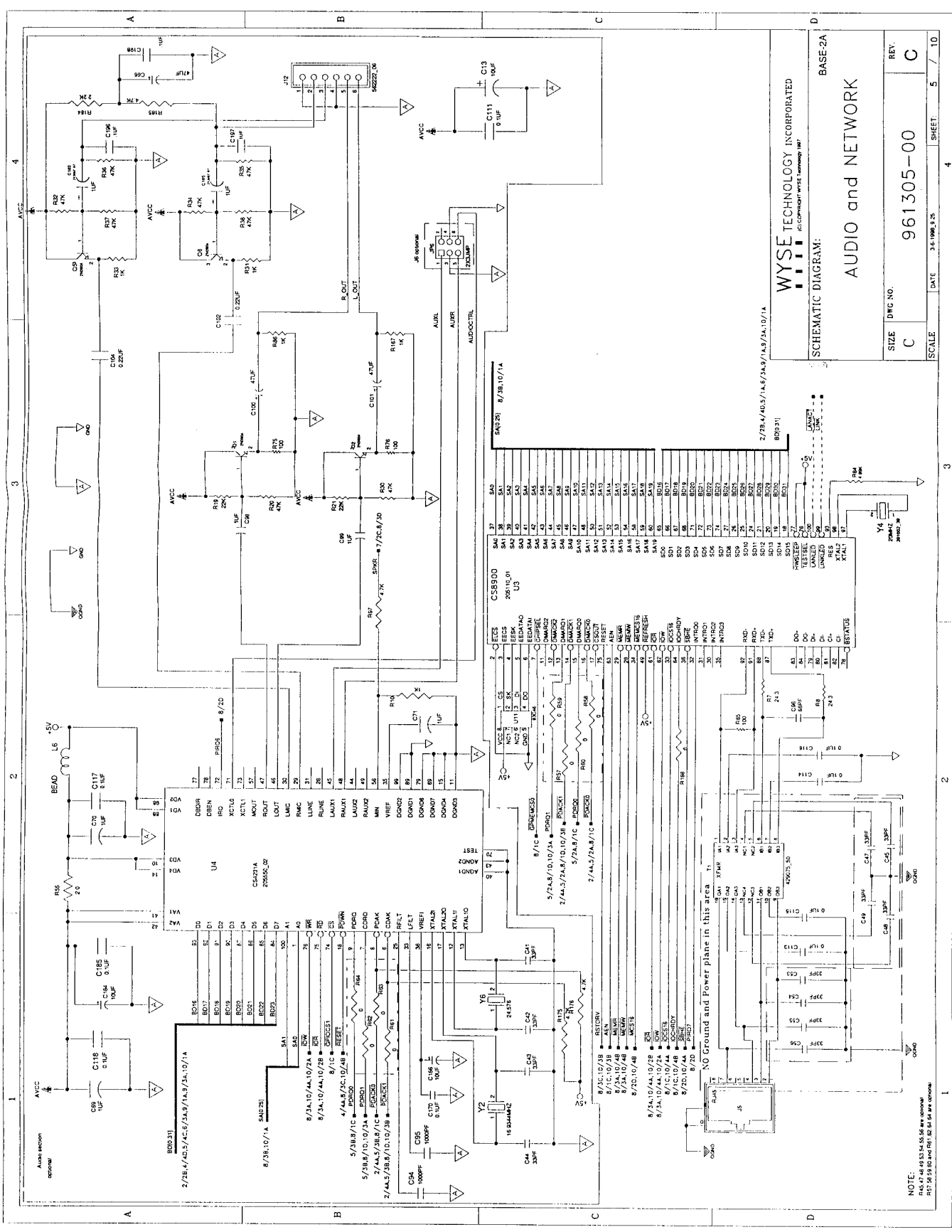
SCHEMATIC DIAGRAM: BASE-2A

DRAM Bank 0,2 and Bank 1,3

SIZE	DWG NO.	REV.
C	961305-00	C

SCALE	DATE	SHEET
3/4"=1"	3-4-1998	3 / 10





WYSE TECHNOLOGY INCORPORATED
101 COWBOY WYDE TOWNSHIP, IN 46782

BASE 2A

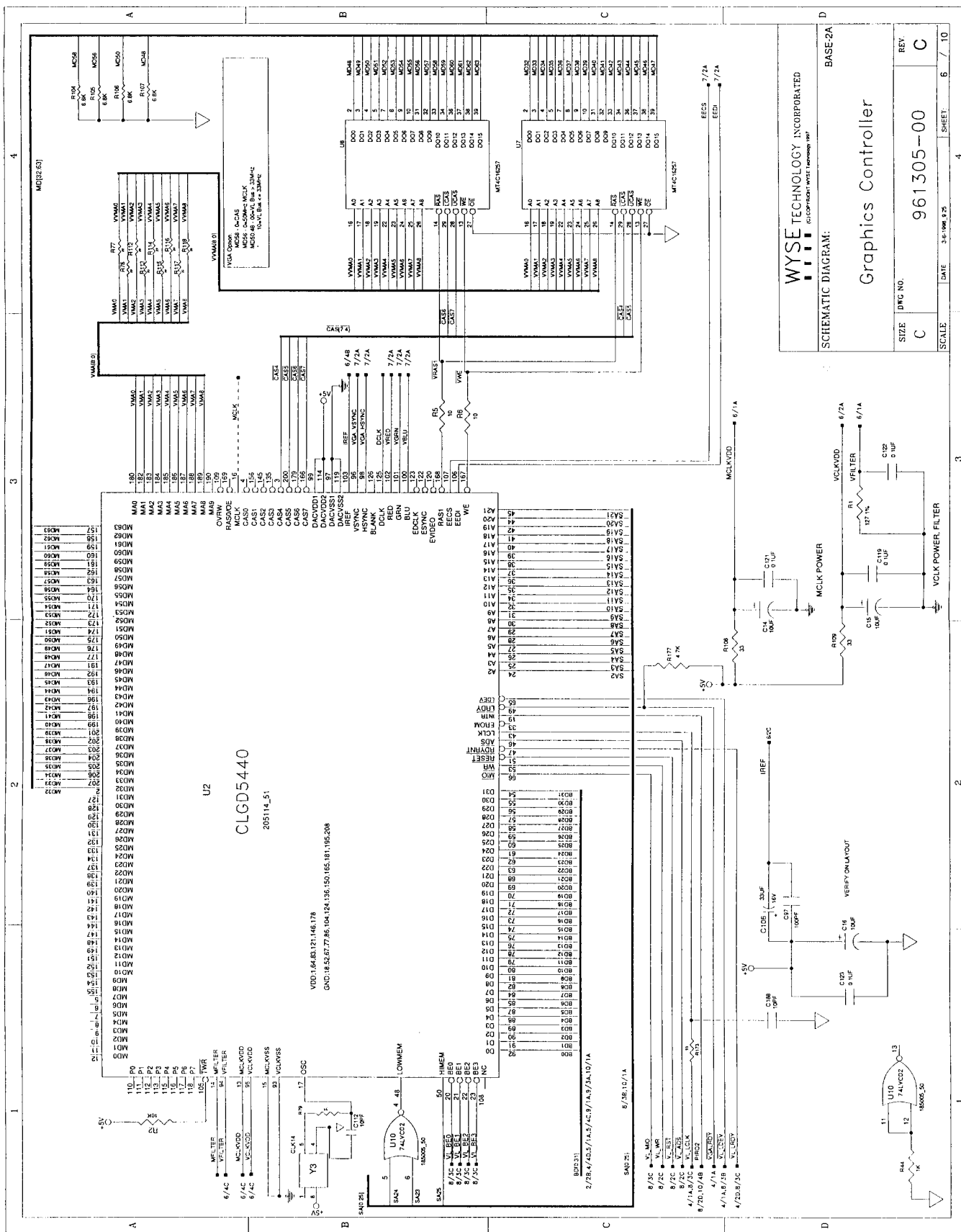
AUDIO and NETWORK

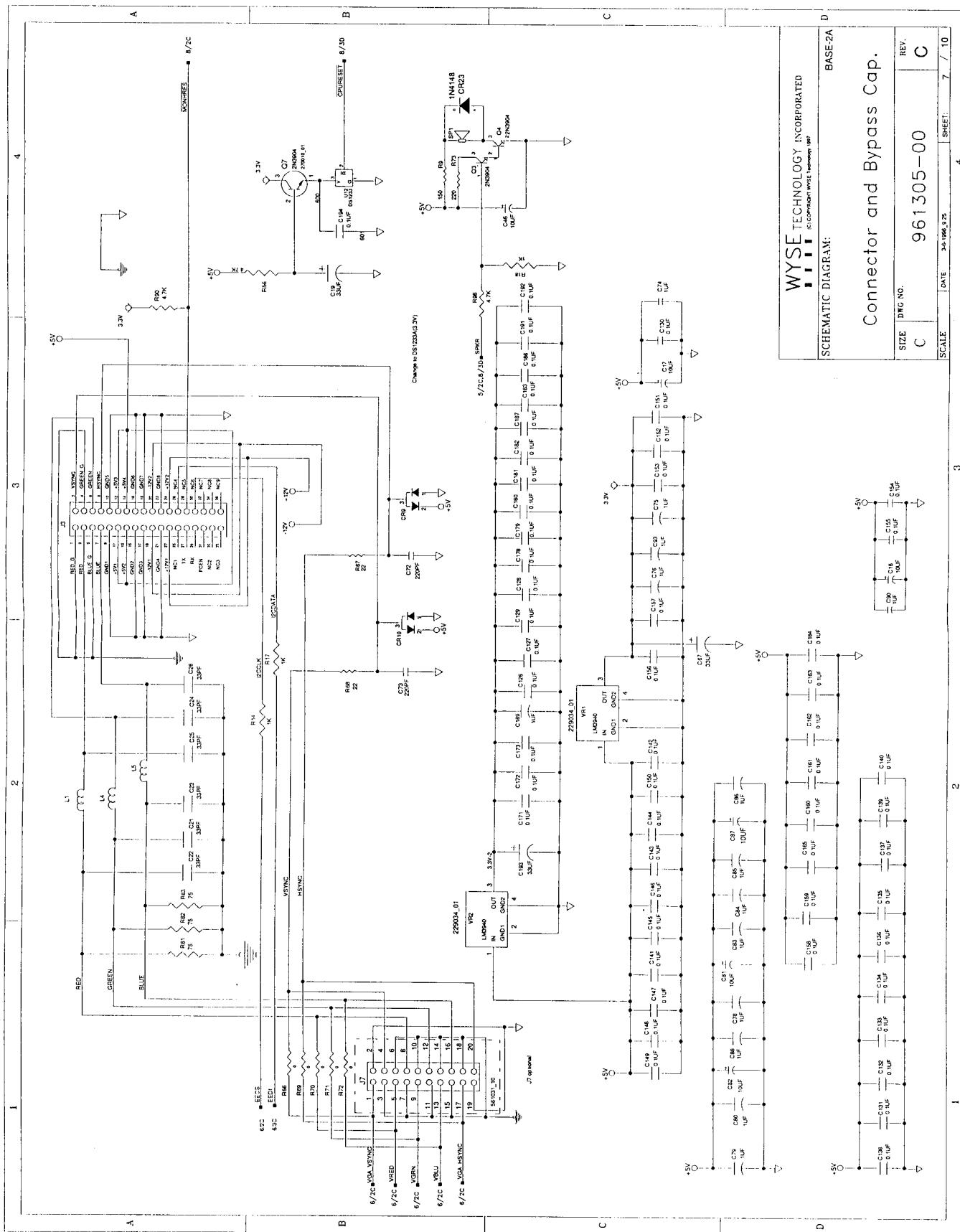
REV. C

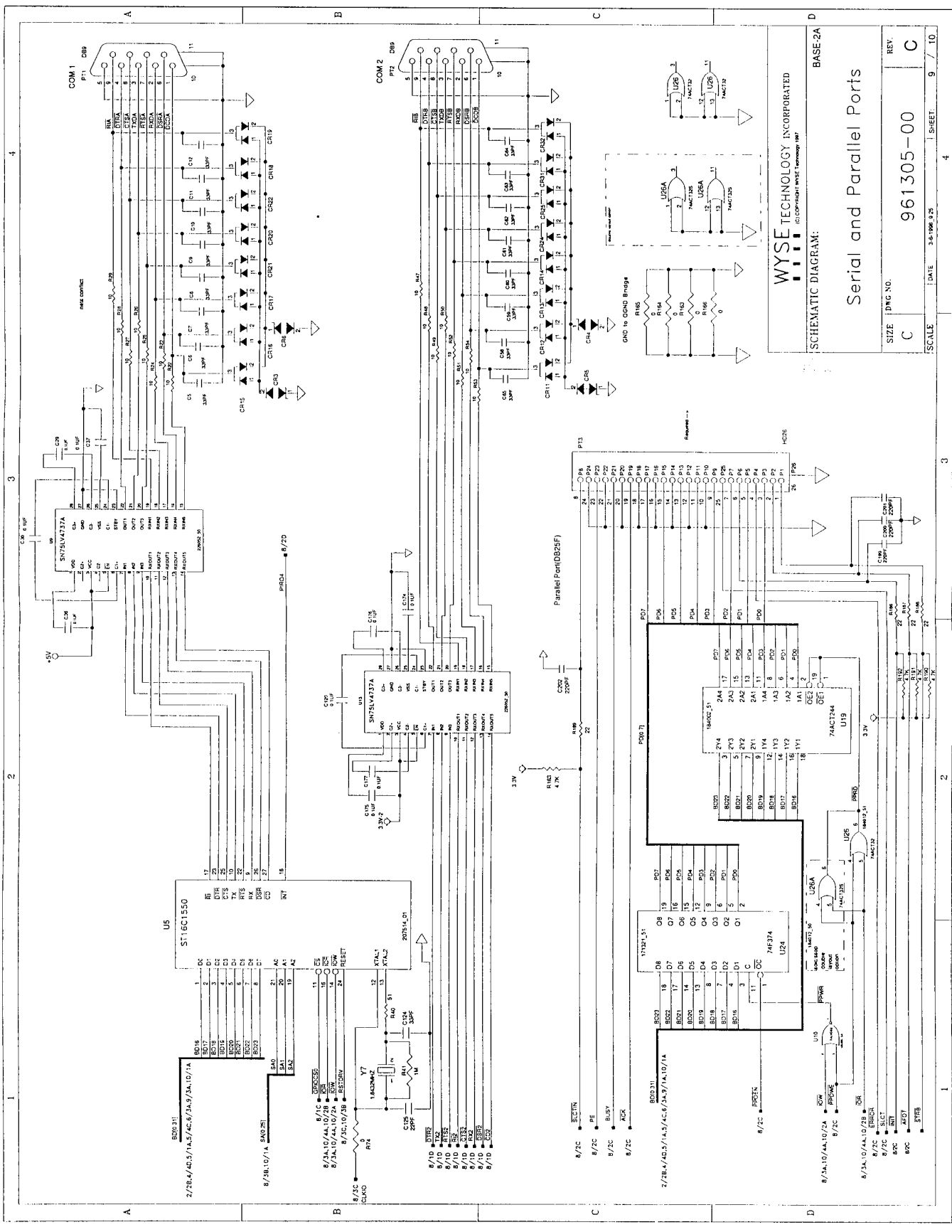
SIZE DFC NO. 961305-00

SCALE DATE 3-6-1983 SHEET 5 / 10

NOTE:
1. ALL DIMENSIONS ARE IN INCHES.
2. DIMENSIONS ARE GIVEN IN PARENTHESES.
3. DIMENSIONS ARE GIVEN IN PARENTHESES.







WYSE TECHNOLOGY INCORPORATED
SCHEMATIC DIAGRAM

SIZE	ENC NO.	REV.
C	961305-00	C
SCALE	DATE	1 SHEET
	3-6-1988 JJS	9 / 10

Serial and Parallel Ports

BASE-2A

