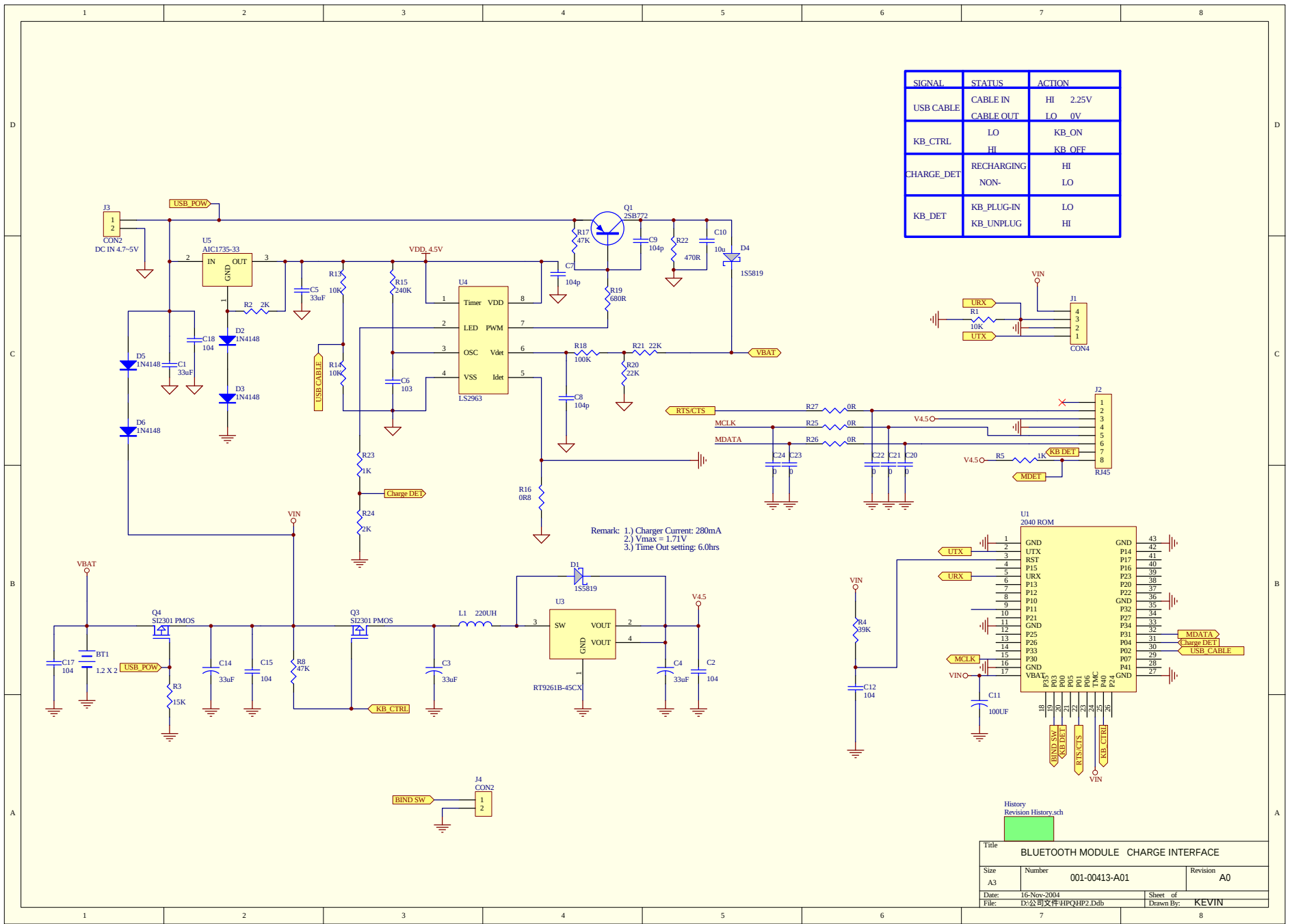
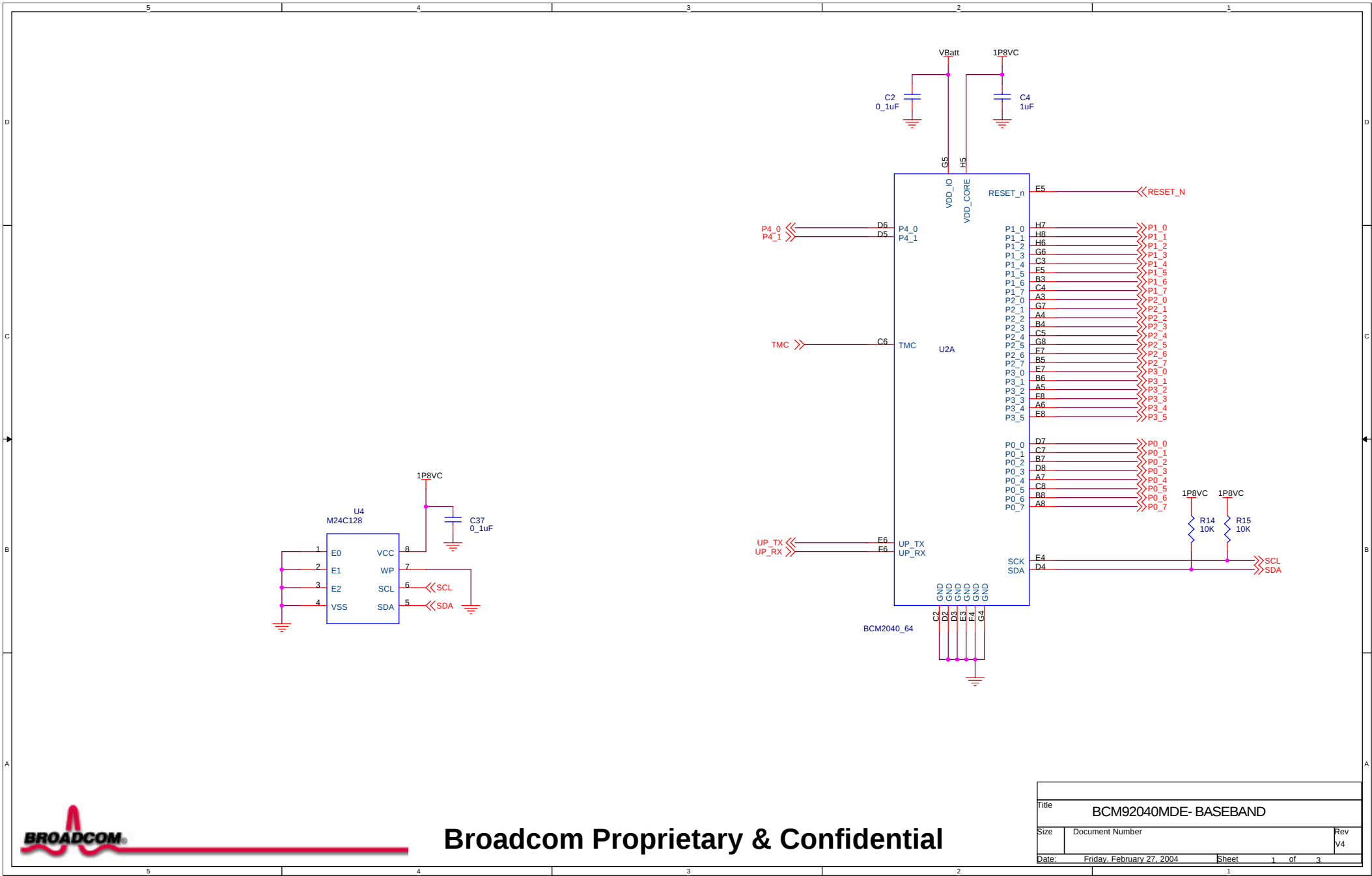
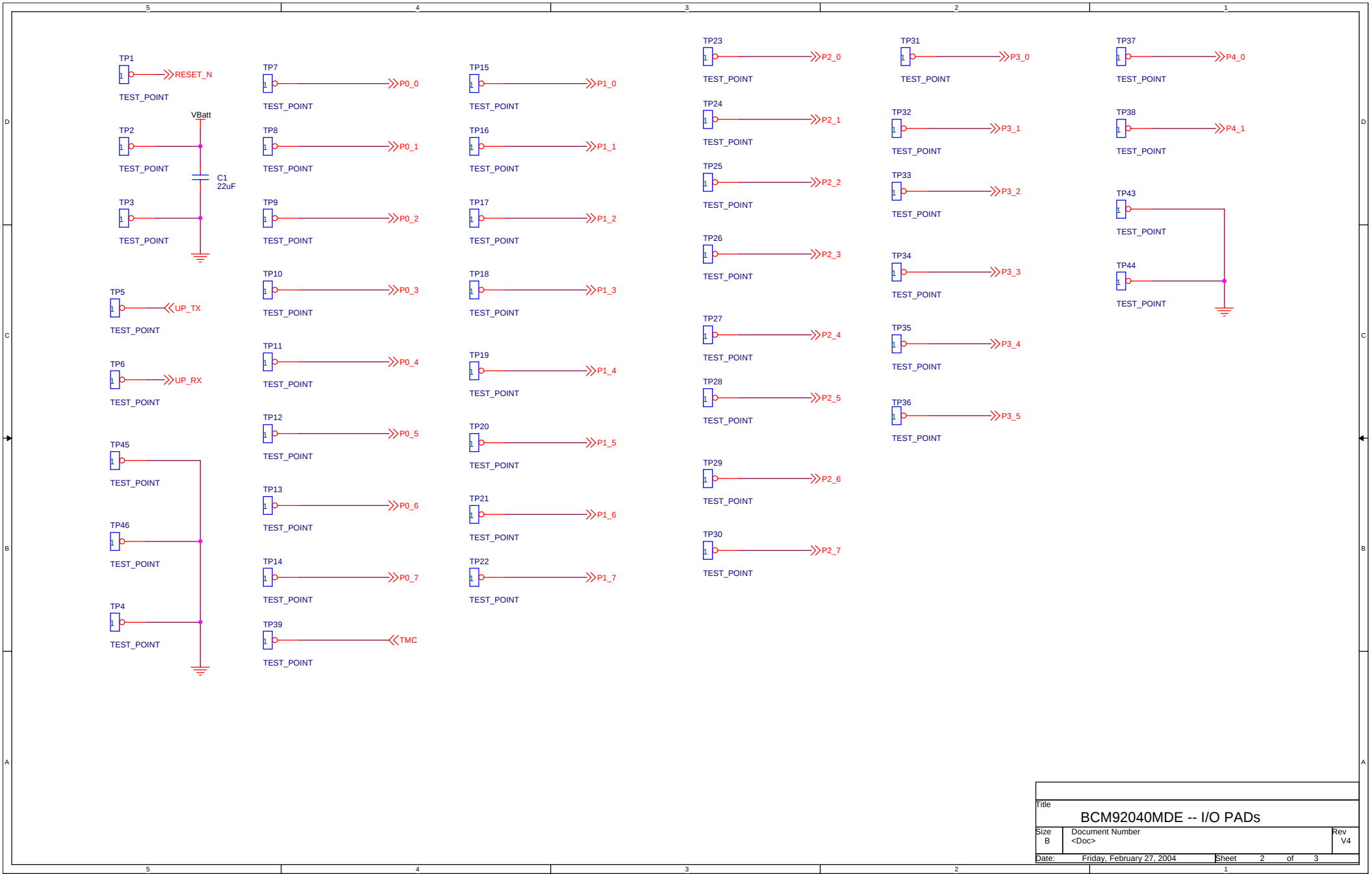






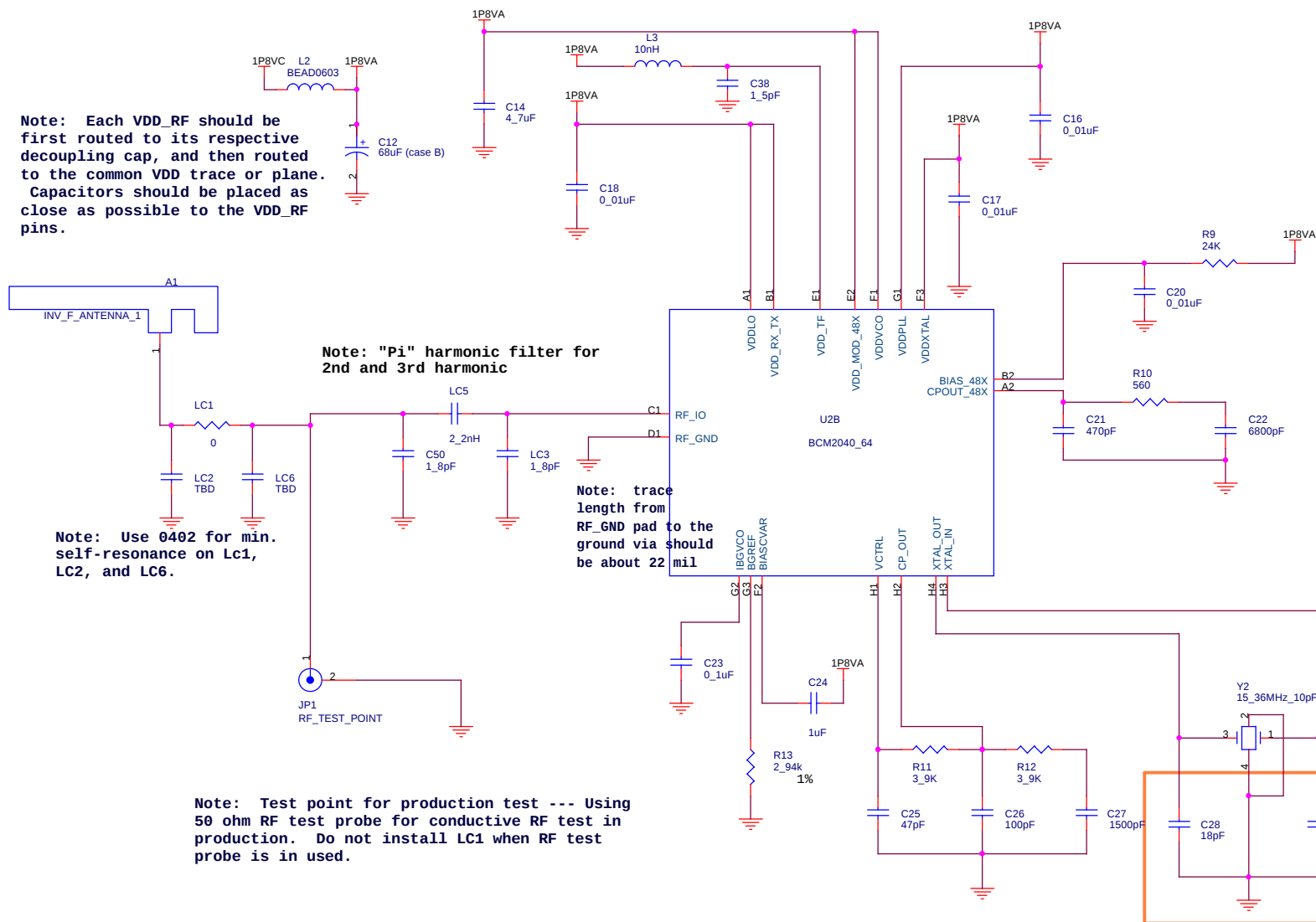
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Note: Each VDD_RF should be first routed to its respective decoupling cap, and then routed to the common VDD trace or plane. Capacitors should be placed as close as possible to the VDD_RF pins.



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