

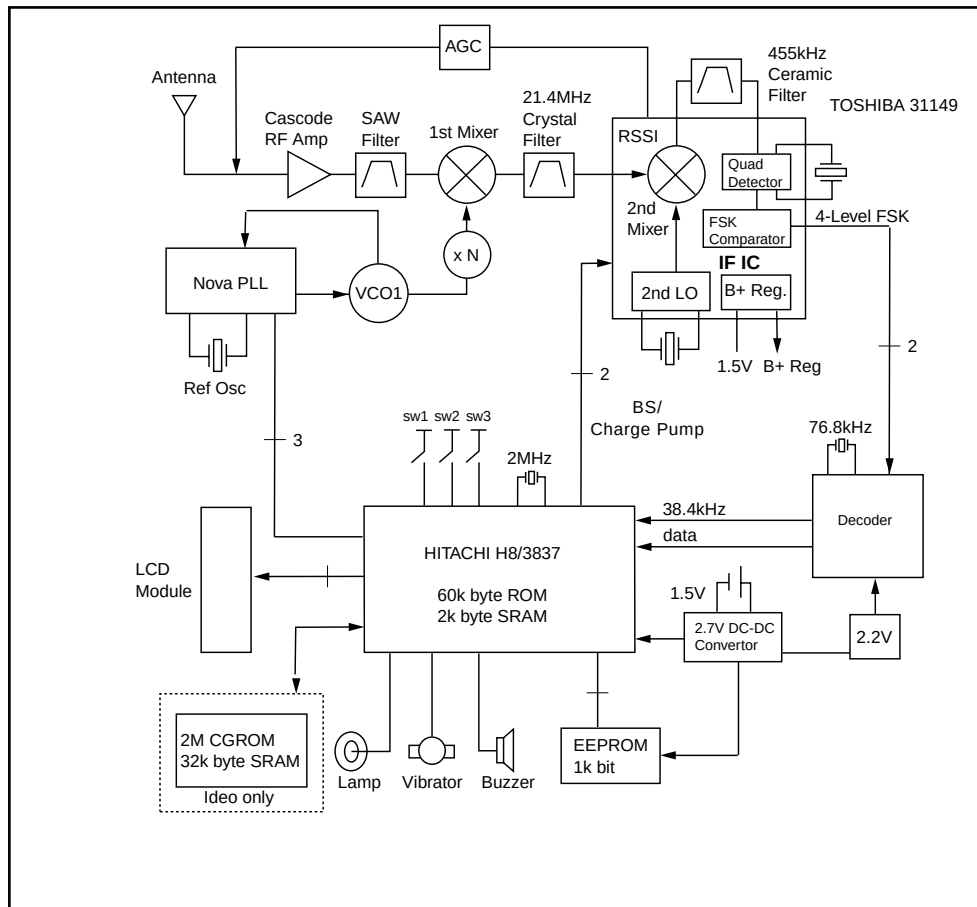


Figure 1-2. Pager Functional Block Diagram

Decoder

The microcomputer controls the receiver circuits, powering them up and down at the proper time intervals, decodes the binary information received, stores the received message in external RAM and generates the alert tones. It also processes user inputs from the keypad and generates the display screen. The linear support module provides boosted dc voltage to all decoder circuits, monitors battery conditions and drives the lamp, speaker, and vibrator motor.