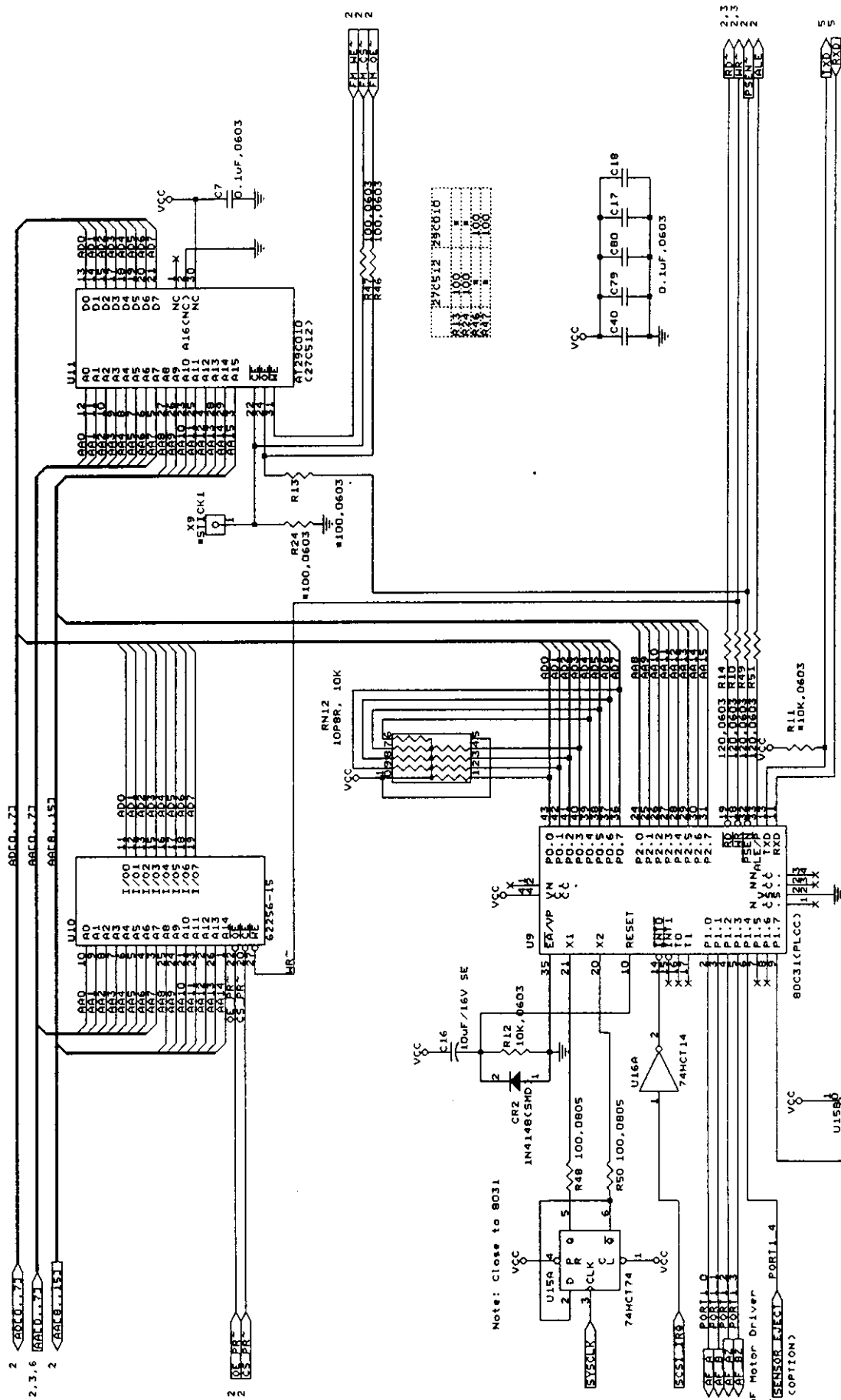




PROJECT : APS 4000 OPI Flamingo
 FUNCTION : CPU, PRIVATE RAM, ROM, FLASH ROM
 DESIGNER : C.P. HUANG
 Size Document Number
 A APS1.SCH (1)
 Date: May 8, 1998 Sheet 1 of 10

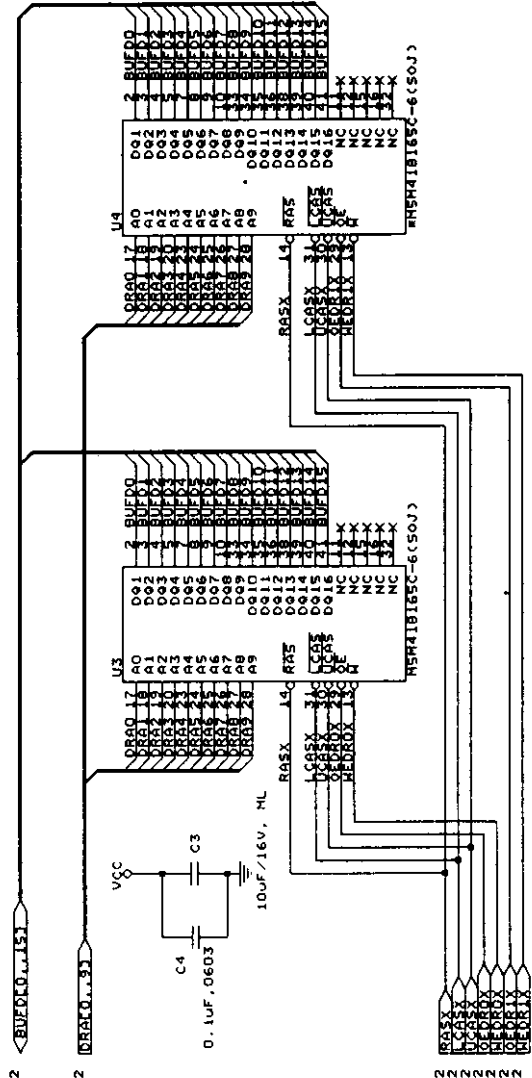
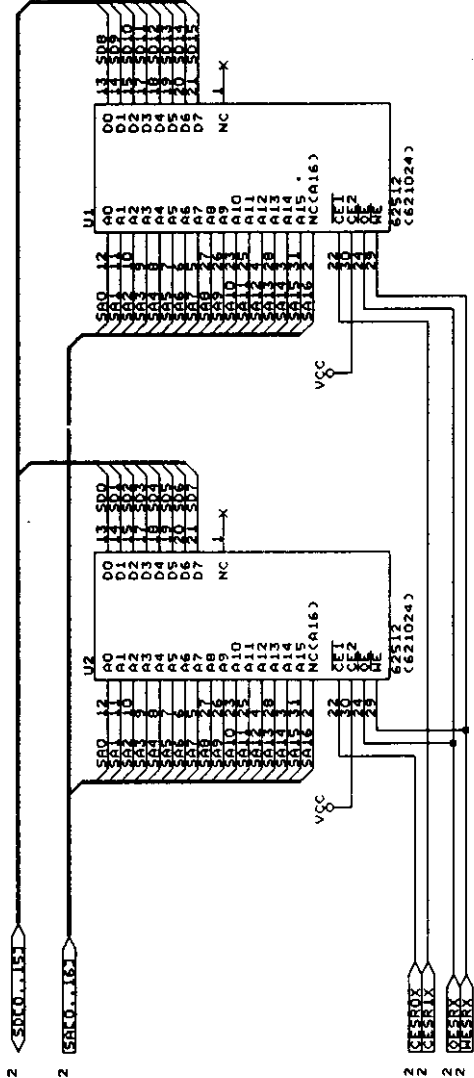


IMAGE BUFFER
2MB (+ 2MB OPTION)

1M X 16

1M X 16 (OPTION FORT 4MB BUFFER)



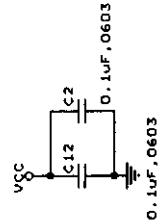
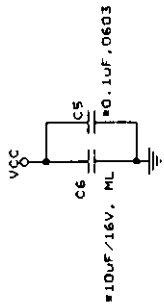
SYSTEM RAM (LOOK-UP TABLE)

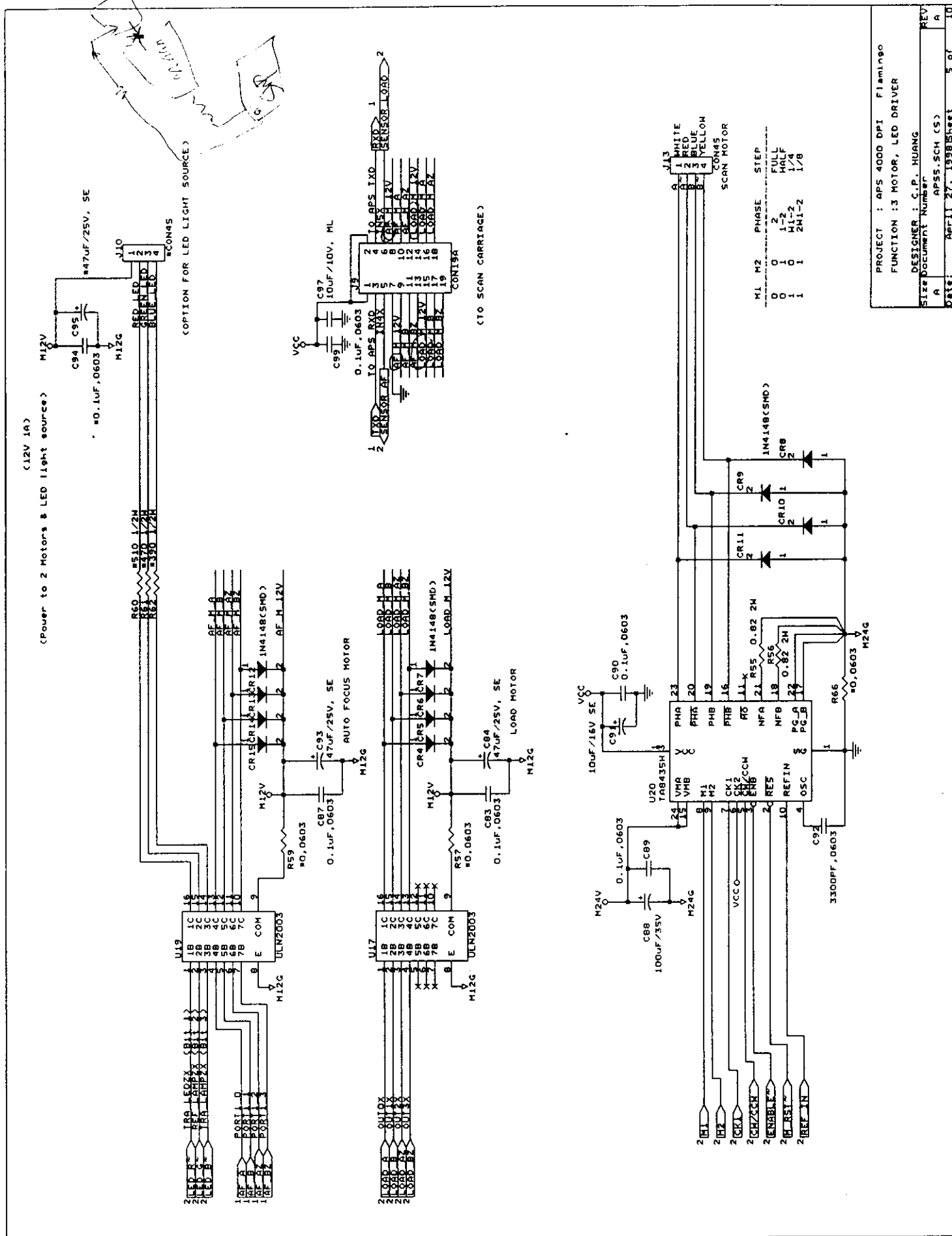
64KB X 2 -- 62512 X2

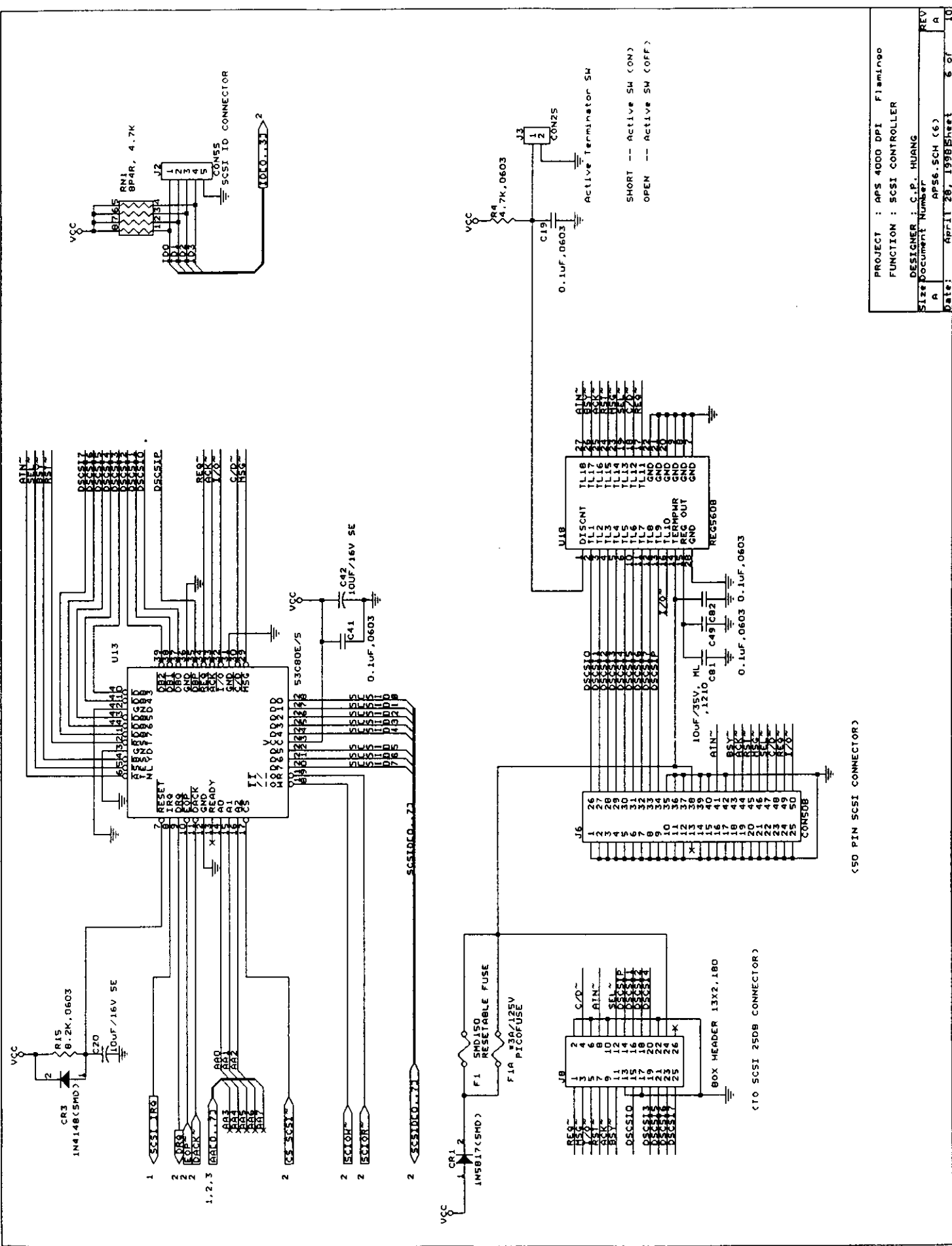
OPTION FOR 128KB X 2 -- 621024 X 2

OPTION FOR 128K BYTE X 2 = 128K WORD (H1 + L0 BYTE)

SRAM: 64K BYTE X 2 = 64K WORD (H1 + L0 BYTE)

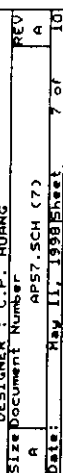


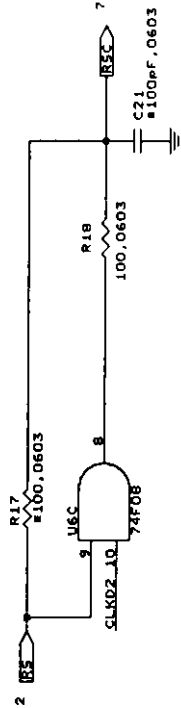
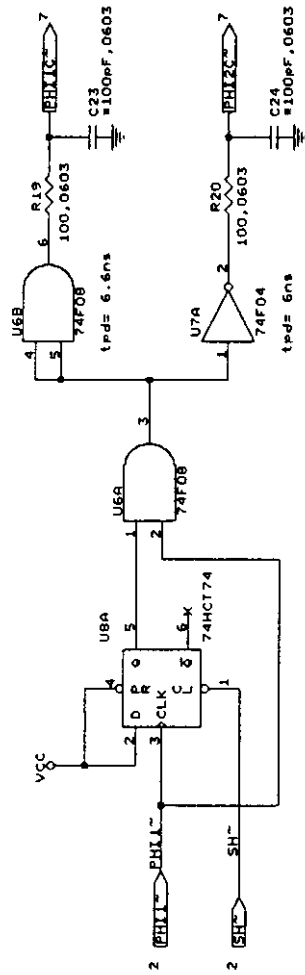




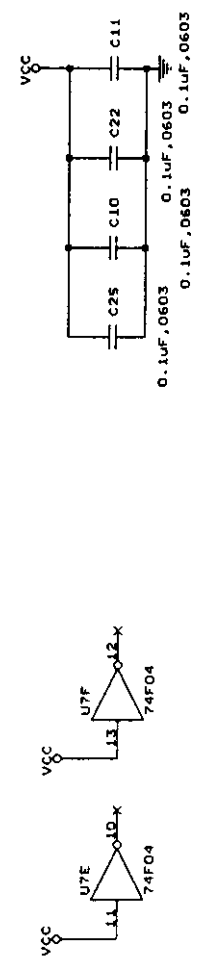
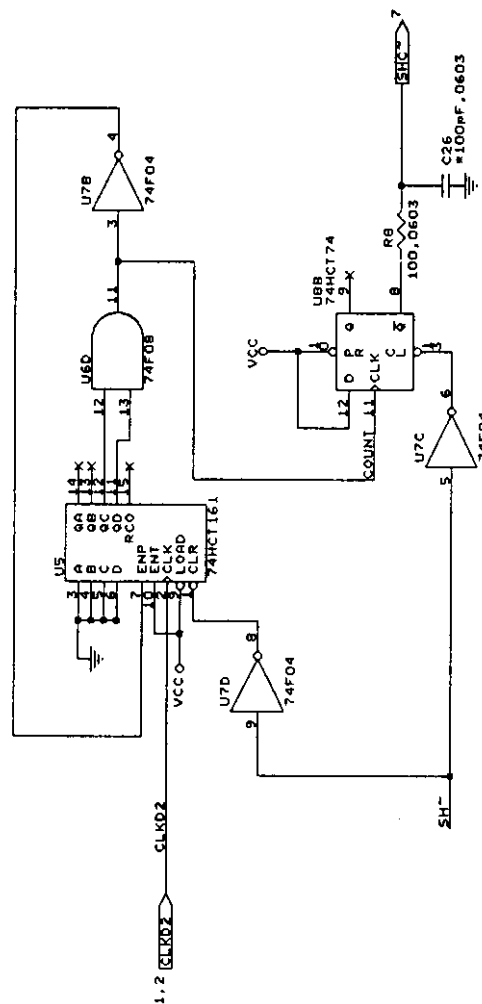
<50 PIN SCSI CONNECTOR>

<10 SCSI 250B CONNECTOR>

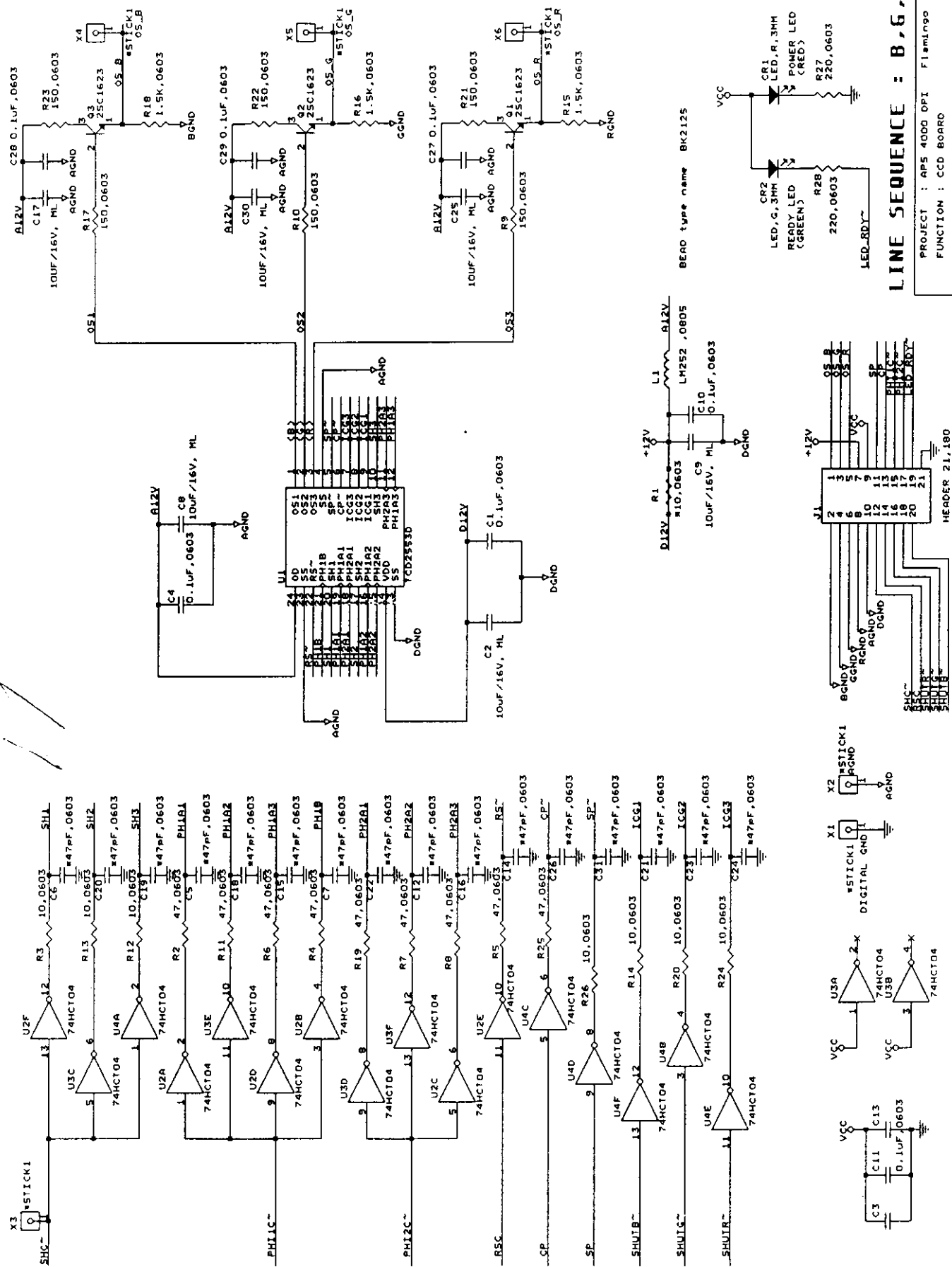




R21	100	OPEN	100
R22	100	OPEN	100



PROJECT : APS 4000 DPI Flamingo
 FUNCTION : CCD TIMING
 DESIGNER : C.P. HUANG
 Size Document Number
 A APS8.SCH (8)
 Date: April 9, 1998 Sheet 8 of 10



LINE SEQUENCE = B, G, R

PROJECT : APS 4000 DPI FlamePro
 FUNCTION : CCD BOARD
 DESIGNER : C.P. HUANG
 Size Document Number
 A APS2553.SCH
 Date : May 8, 1998 Sheet 1 of 1