

The diagram illustrates the GEMPLUS 680 computer system, divided into two main sections: the main board and the interface board.

**Main Board Components:**

- 80C524  $\mu$ P:** The central microprocessor unit.
- ASIC:** Application Specific Integrated Circuit.
- 13.56MHz:** A crystal oscillator.
- 14.74MHz:** Another crystal oscillator.
- GEMPLUS 680:** The main board identifier.
- Connectors:** Various connectors are shown, including a large multi-pin connector labeled +V, 0v, and Screen, and a smaller connector labeled Rx and Tx.

**Interface Board Components:**

- BEWATOR COTAG:** A component labeled vertically on the left side of the interface board.
- 8 MHz:** A clock signal source.
- 2/68 1B or later:** A memory module.
- Interface Pins:** A row of pins labeled +V, 0V, hold, Amber, Red, Green, D0, D1, and DA.

The diagram uses various symbols and labels to represent different components and their connections, providing a comprehensive overview of the system's hardware architecture.