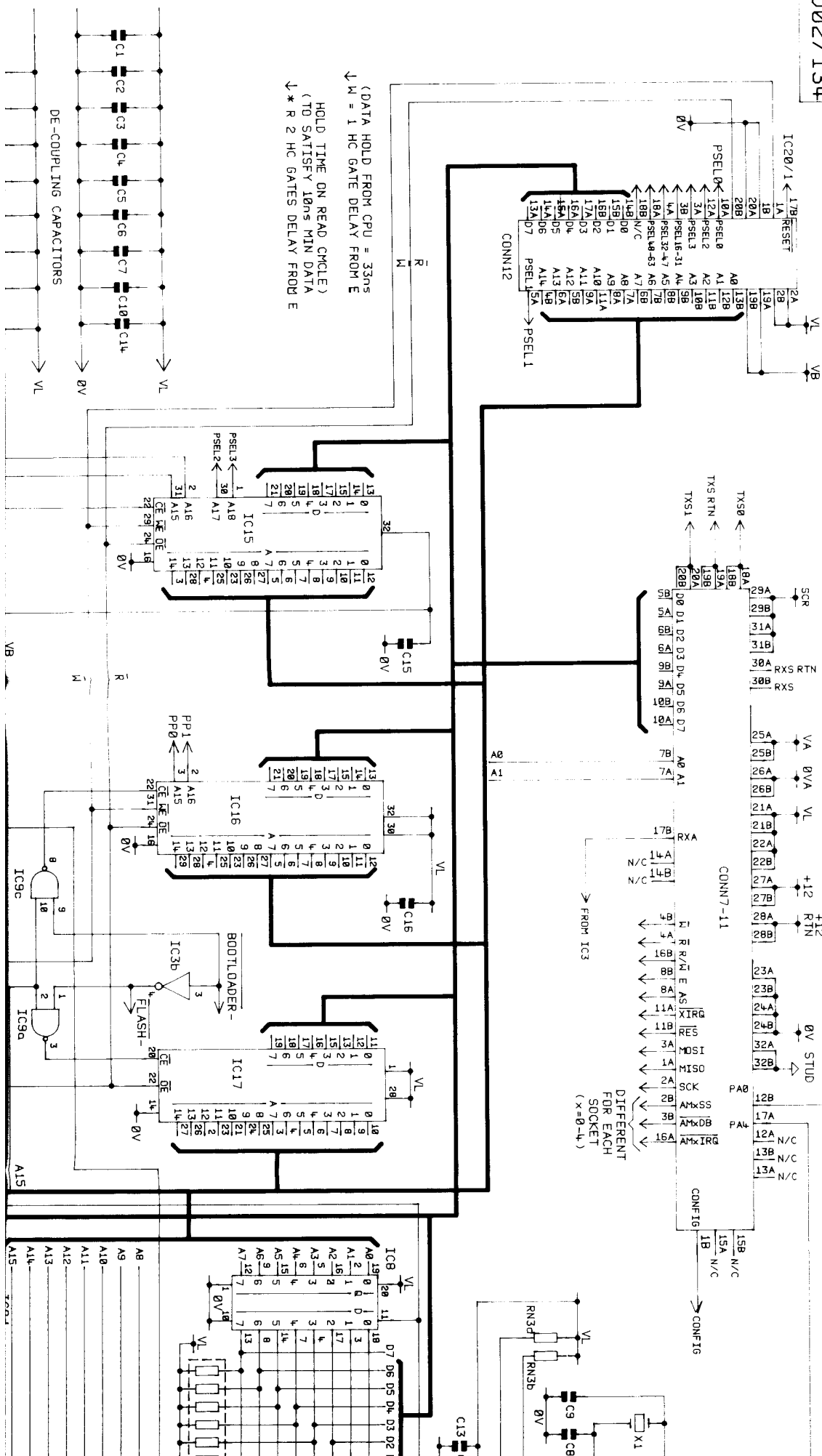
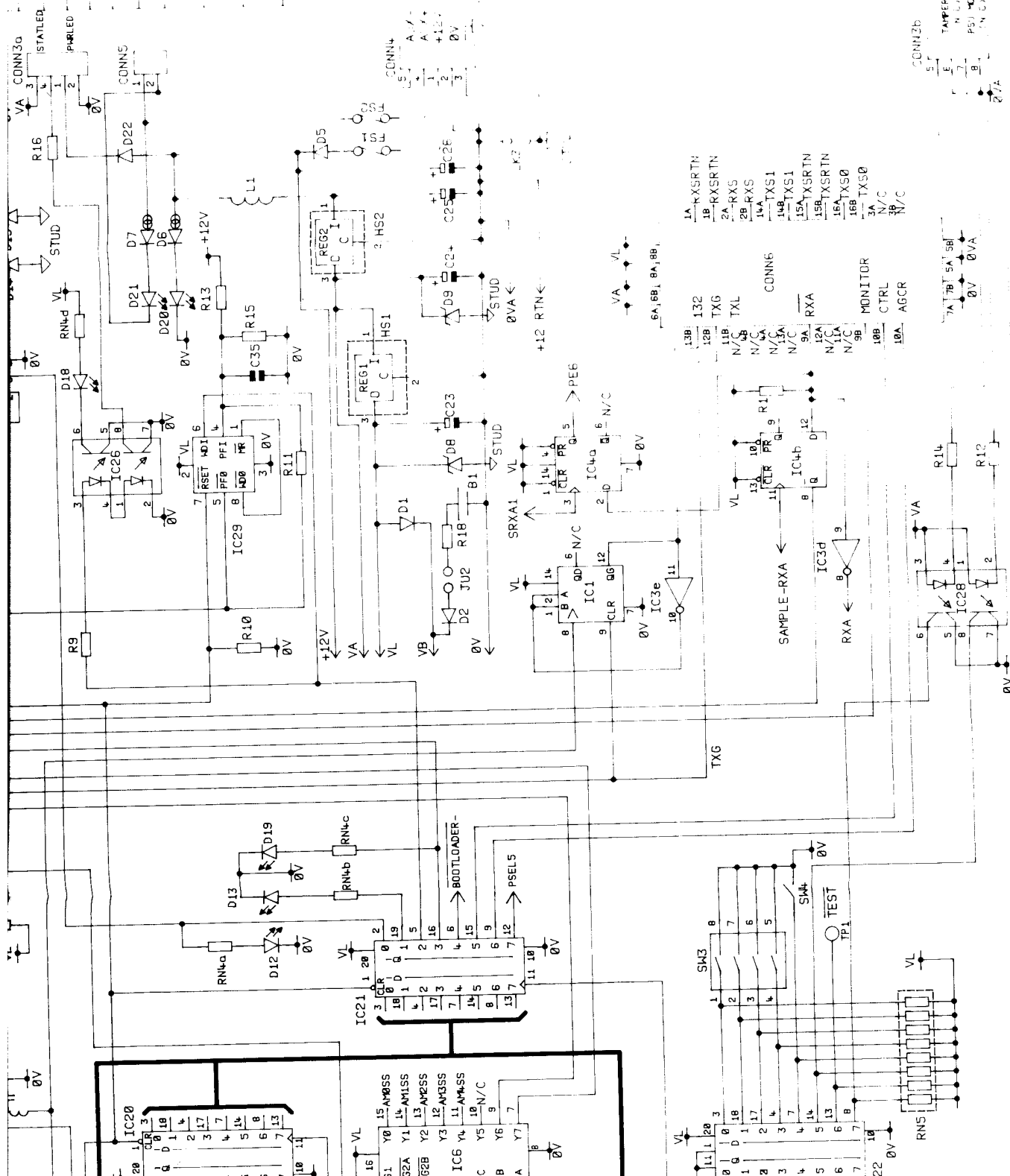


CD002/134

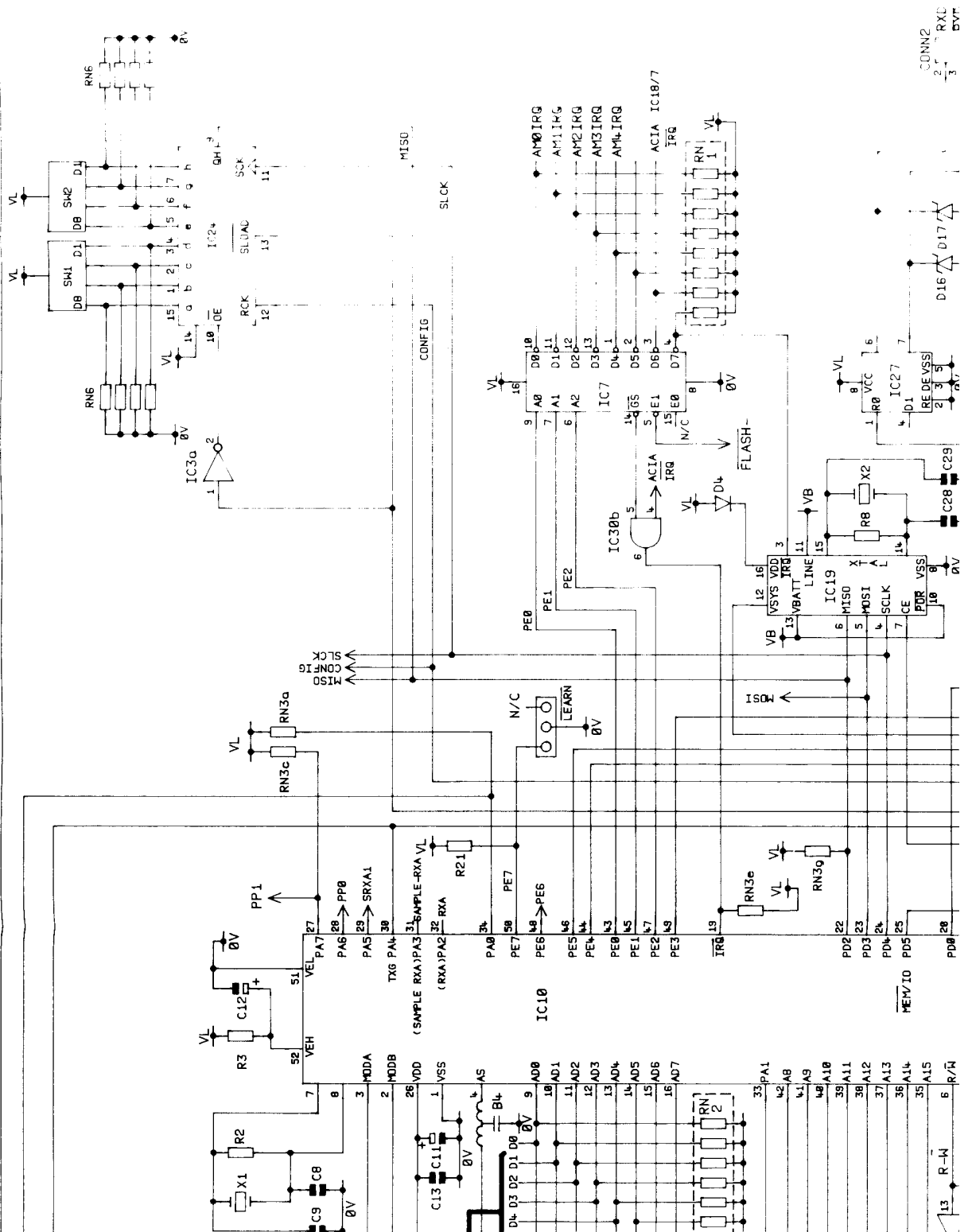


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CD02/134



Notes



2	26-06-98	A MODEL	DM
1	21-04-98	PROTOTYPE	DM
Issi Date Revision Init			
Drawn	VISORLAKE 21-04-98		
Checked	PCB DESIGNS		
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Used ON
 1X02 423
 1X02 424

Third Angle Projection Scale:



All dimensions in Tolerance:

MILLIMETRES

Unless otherwise stated unless otherwise stated

Material: Finish:

Title:

CIRCUIT DIAGRAM FOR

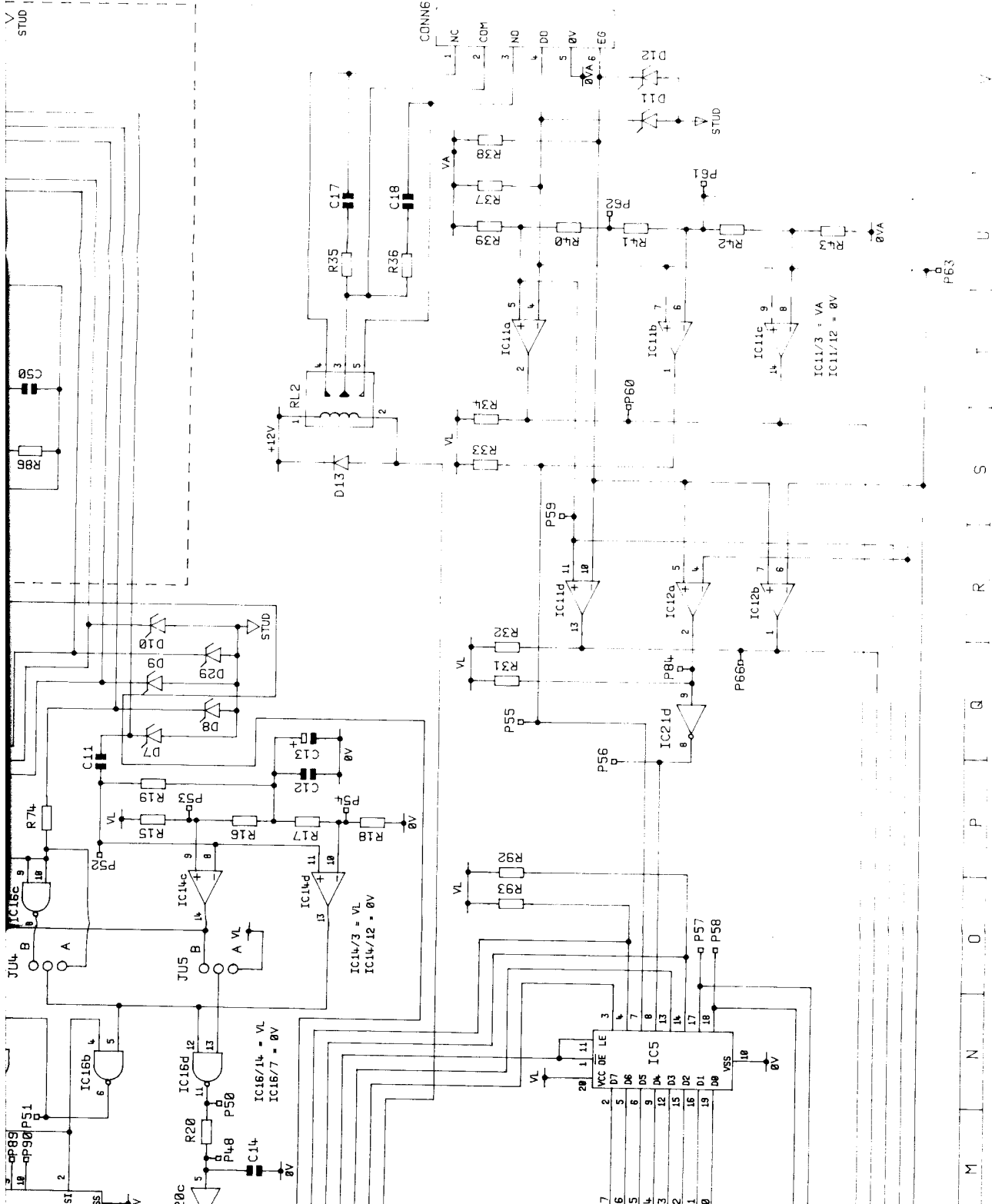
Mk 2 READER MODULE

Drawing Number:

CD02/135

Sht. 1

of 1



DRG. No.

CD02/135

SEE NOTE 2 (R638X90)

CONN7

IC17/14 = VL

IC17/7 = 0V

IC17D

IC19C

P73

IC19D

IC17C

IC17D

IC17E

IC17F

IC17G

IC17H

IC17I

IC17J

IC17K

IC17L

IC17M

IC17N

IC17O

IC17P

IC17Q

IC17R

IC17S

IC17T

IC17U

IC17V

IC17W

IC17X

IC17Y

IC17Z

SEE NOTE 2

IC19A

IC19B

IC19C

IC19D

IC19E

IC19F

IC19G

IC19H

IC19I

IC19J

IC19K

IC19L

IC19M

IC19N

IC19O

IC19P

IC19Q

IC19R

IC19S

IC19T

IC19U

IC19V

IC19W

IC19X

IC19Y

IC19Z

IC19A

IC19B

IC19C

IC19D

+12V

JU3

A

B

C

D

E

F

G

H

I

J

K

L

M

N

O

P

Q

R

S

T

U

V

W

X

Y

Z

IC15A

IC15B

IC15C

IC15D

IC15E

IC15F

IC15G

IC15H

IC15I

IC15J

IC15K

IC15L

IC15M

IC15N

IC15O

IC15P

IC15Q

IC15R

IC15S

IC15T

IC15U

IC15V

IC15W

IC15X

IC15Y

IC15Z

IC15A

IC15B

IC15C

IC15D

IC15E

IC15F

IC15G

IC15H

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IC15P

IC15Q

IC15R

IC15S

IC15T

IC15U

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IC15X

IC15Y

IC15Z

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IC15B

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IC15D

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IC15N

IC15O

IC15P

IC15Q

IC15R

IC15S

IC15T

IC15U

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IC15Q

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IC15S

IC15T

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IC15V

IC15W

IC15X

IC15Y

IC15Z

IC15A

IC15B

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IC15V

IC15W

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IC15H

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IC15J

IC15K

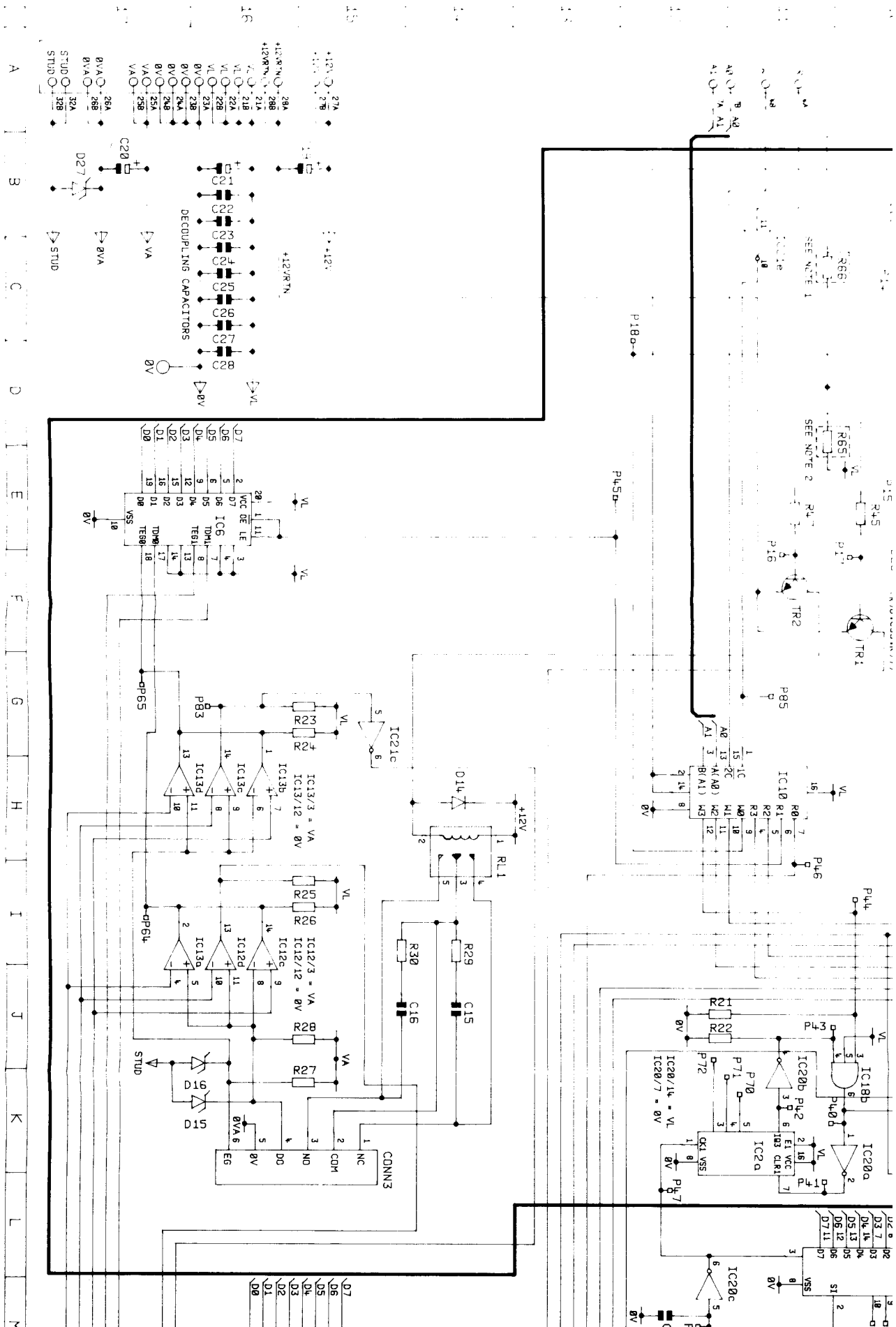
IC15L

IC15M

IC15N

IC15O

IC15P

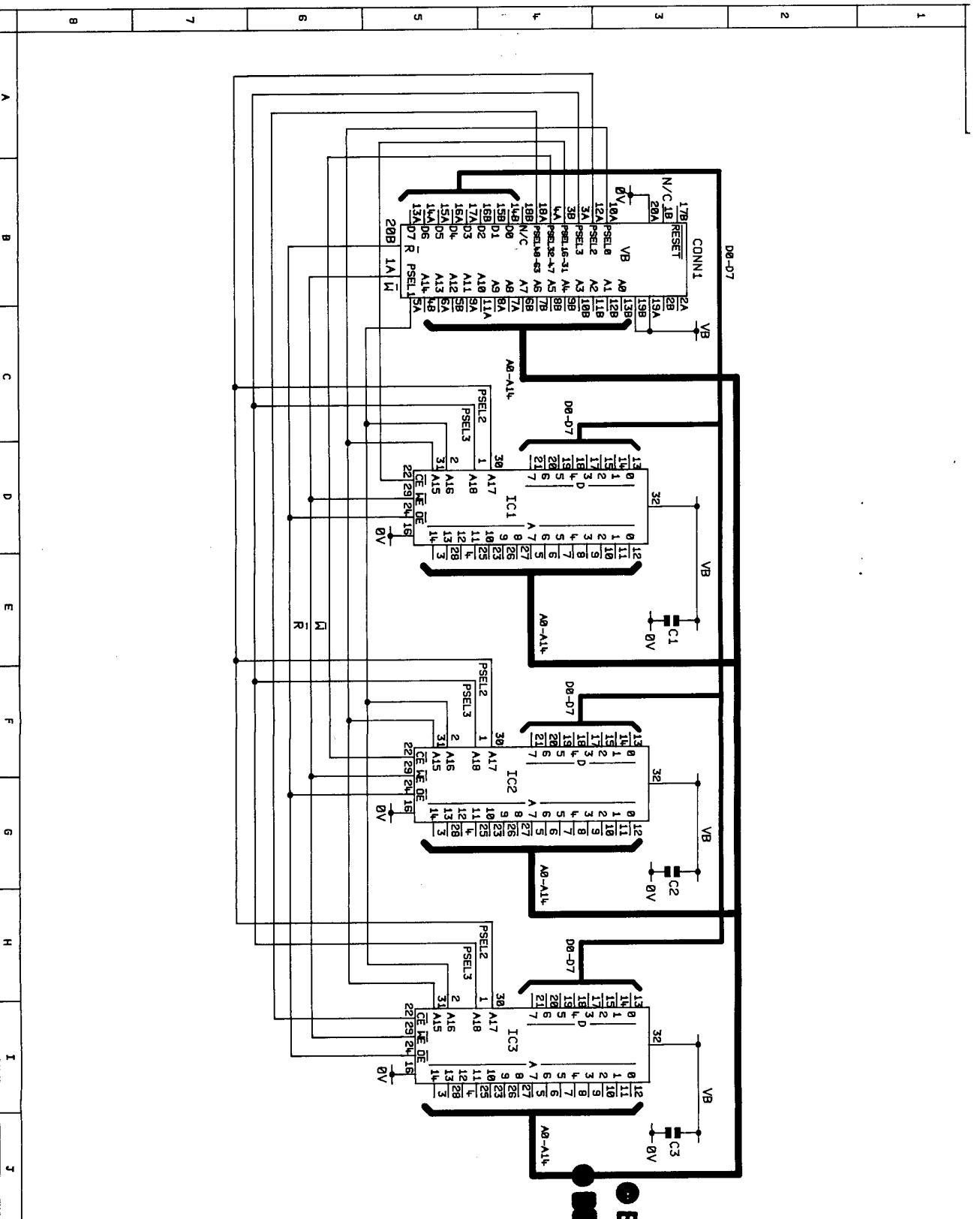


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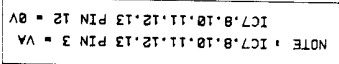
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LEGEND A/M FOR FLASH CONTROLLER MOTHERBOARD									
TITLE		DATE		USED ON		ISSUE			
DRN	Visor/lock PCB Desigins	04-03-98	DL-02-292			2			
	01702 580446								
CMCD.		DRG No.		A02/442		SH3		OF5	



BE MEM EXPANSION LTD		TAG LTD	
Iss Date:	Revision:	Init:	
1 28-02-98		VL	
Drawn:	WISORAKE	10-SON	
Checked:			
Authorised:			
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Used	Box 422		
on			
Third Angle Projection	Scale:		
All dimensions in MILLIMETRES	Tolerances:		
Unless otherwise stated	Unless otherwise stated		
Materials	Finish:		
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Drawing Number:		Sht. 1 of 1	
CD02/133			

DRG. No.

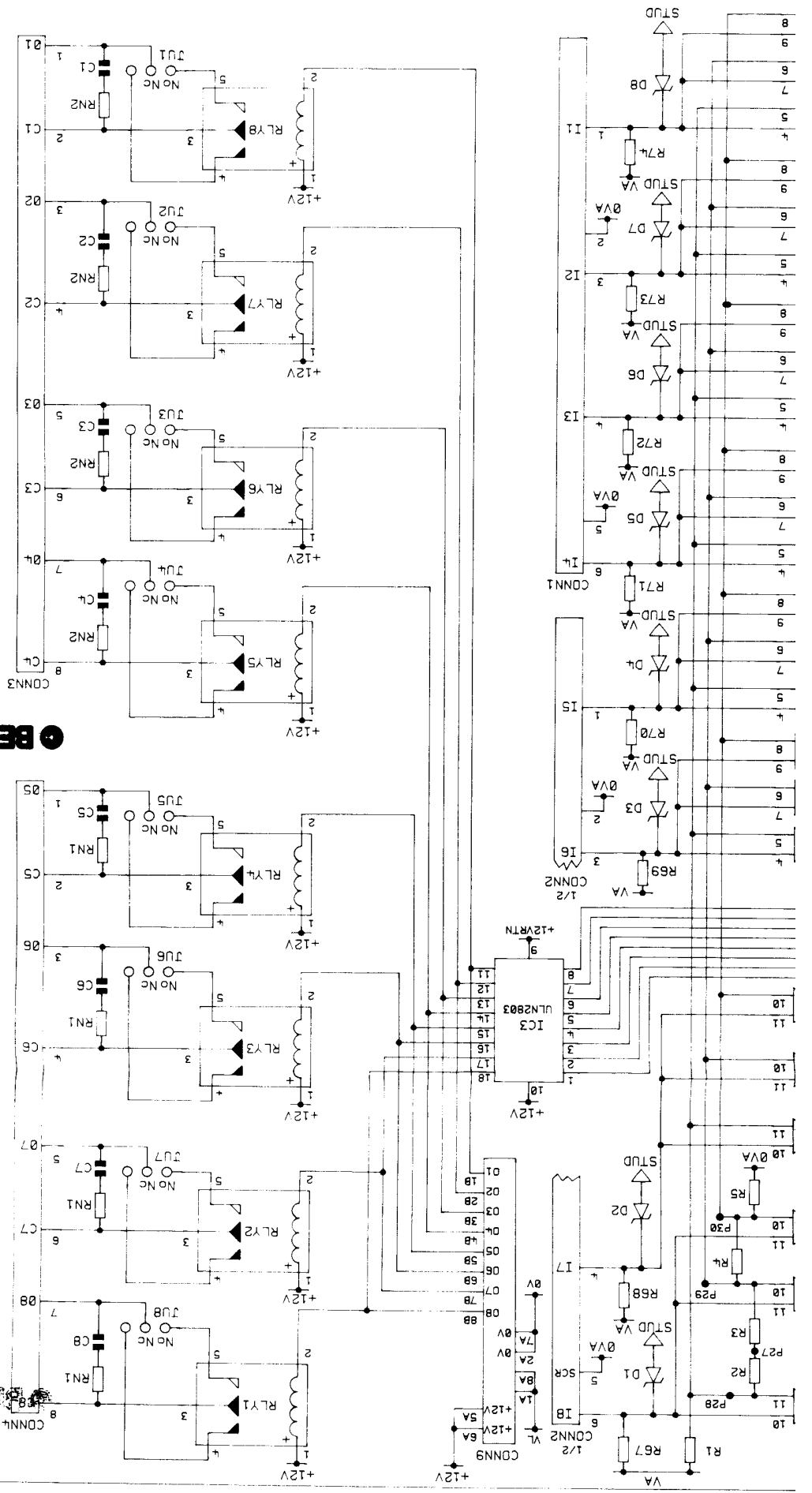


CIRCUIT DIAGRAM
INPUT/OUTPUT MODULE-16/8

Title	
Material:	Finish:
All dimensions in mm unless otherwise stated	
Tolerances:	
Third Angle Projection	
Scale:	
Used:	
On:	
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ISS	
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23-07-98	
VL	

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Notes:



Notes:

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Used 10.4
 On

Third Angle Project Scale:

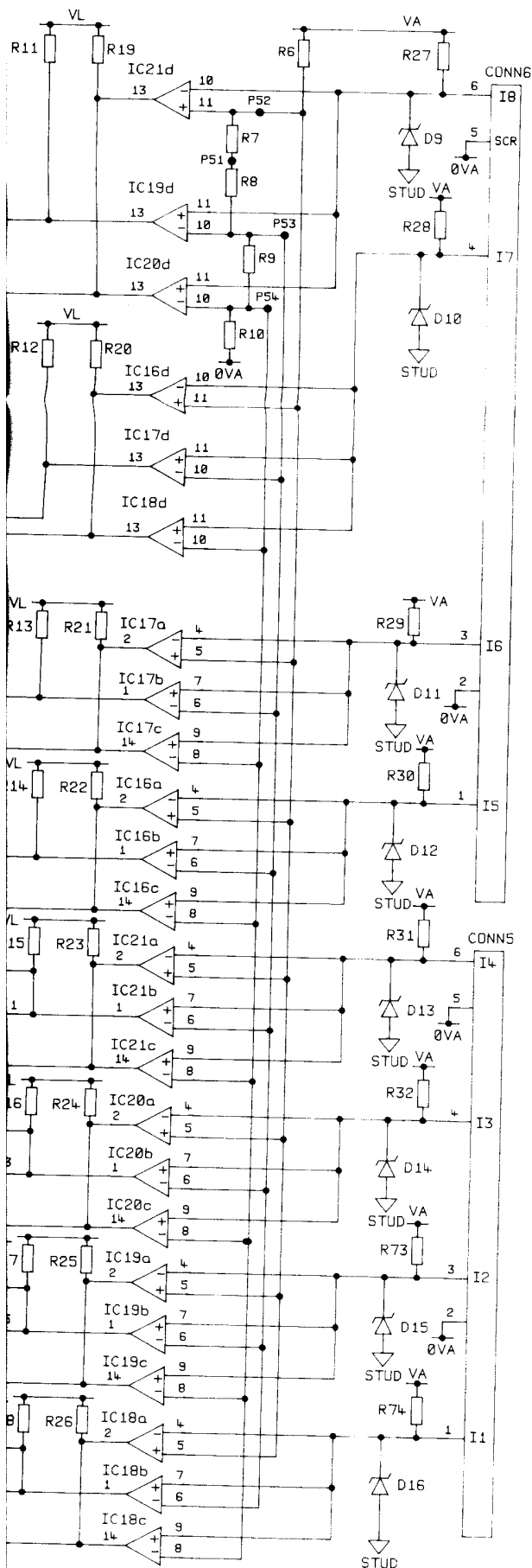
All dimensions in
 MILLIMETRES
 Unless otherwise
 stated

Materials: Finish:

Title
 CIRCUIT DIAGRAM
 INPUT/OUTPUT MODULE-16/8

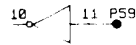
Drawing No sht. 2

CD02/136 of 2

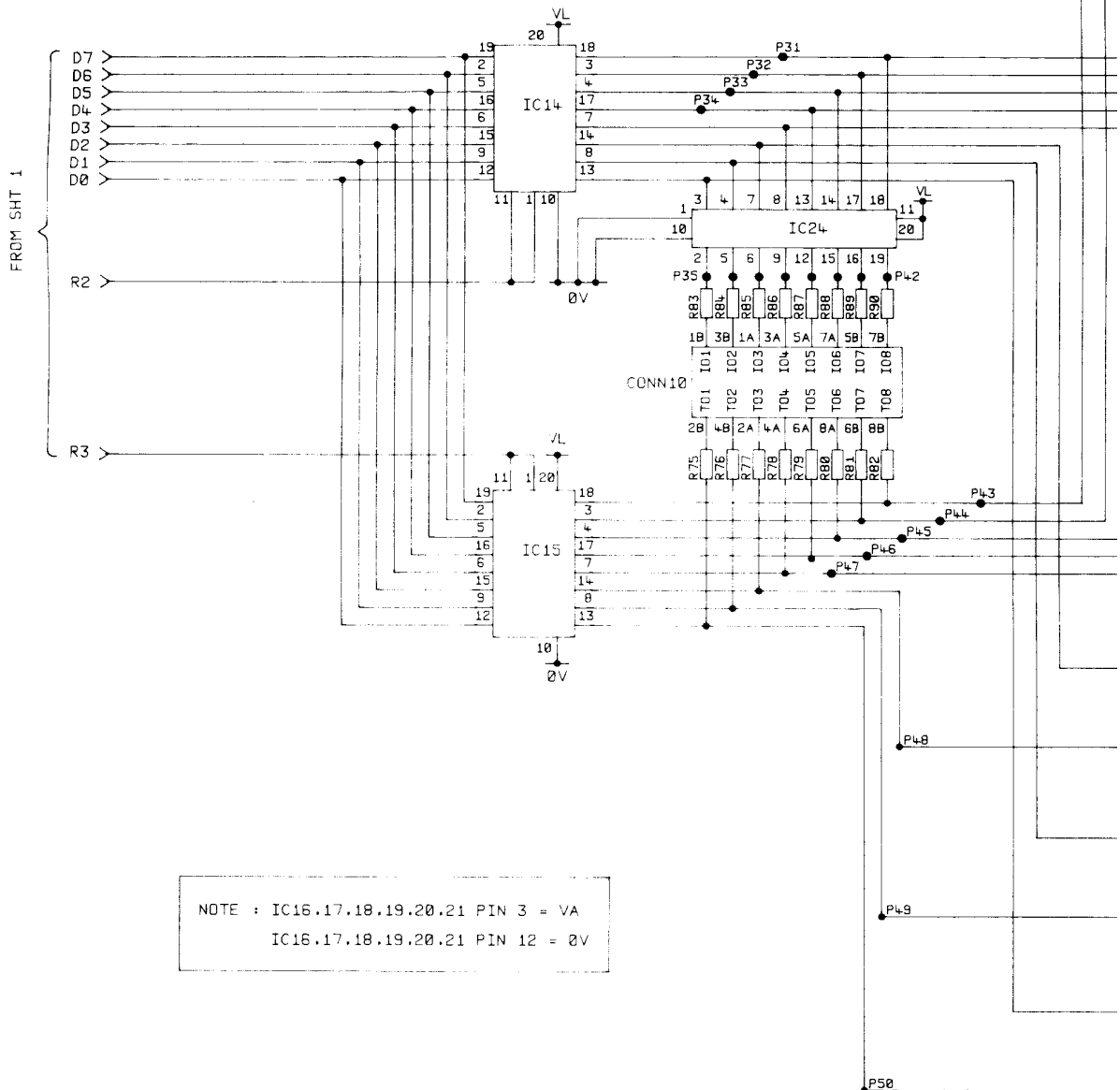
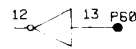


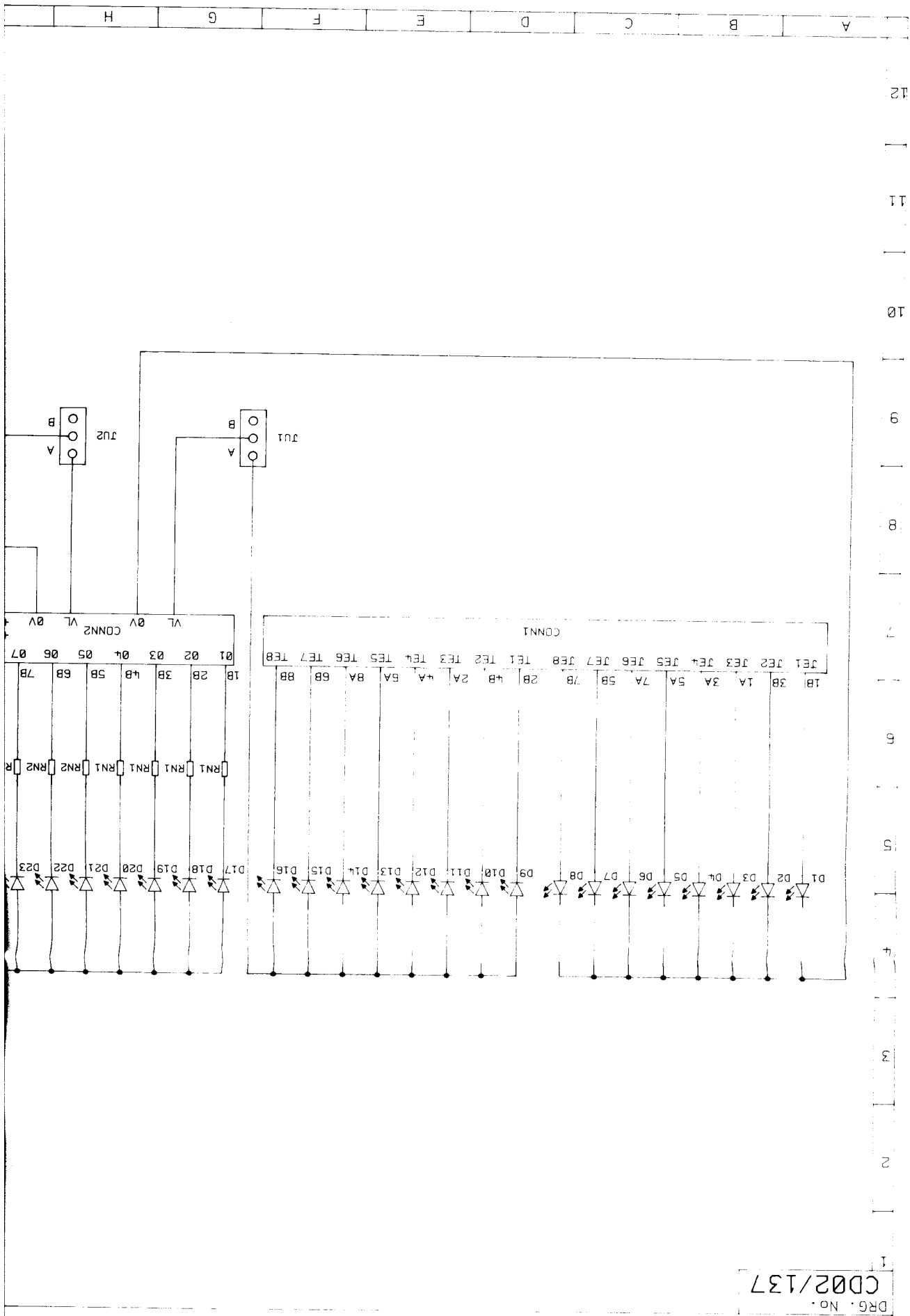
SPARE GATES

IC22e



IC22f





2	26-06-98	A MODEL	DM
1	21-04-98	PROTOTYPE	DM
Iss Date		Revision	Init
Drawn		VISORLACE	21-04-98
Checked		PCB DESIGNS	
Authorised			

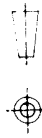
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Used **100% 423**
 ON **100% 424**

Third Angle Projection Scale:



All dimensions in Tolerance:

MILLIMETRES

Unless otherwise stated/unless otherwise stated

Material: Finish:

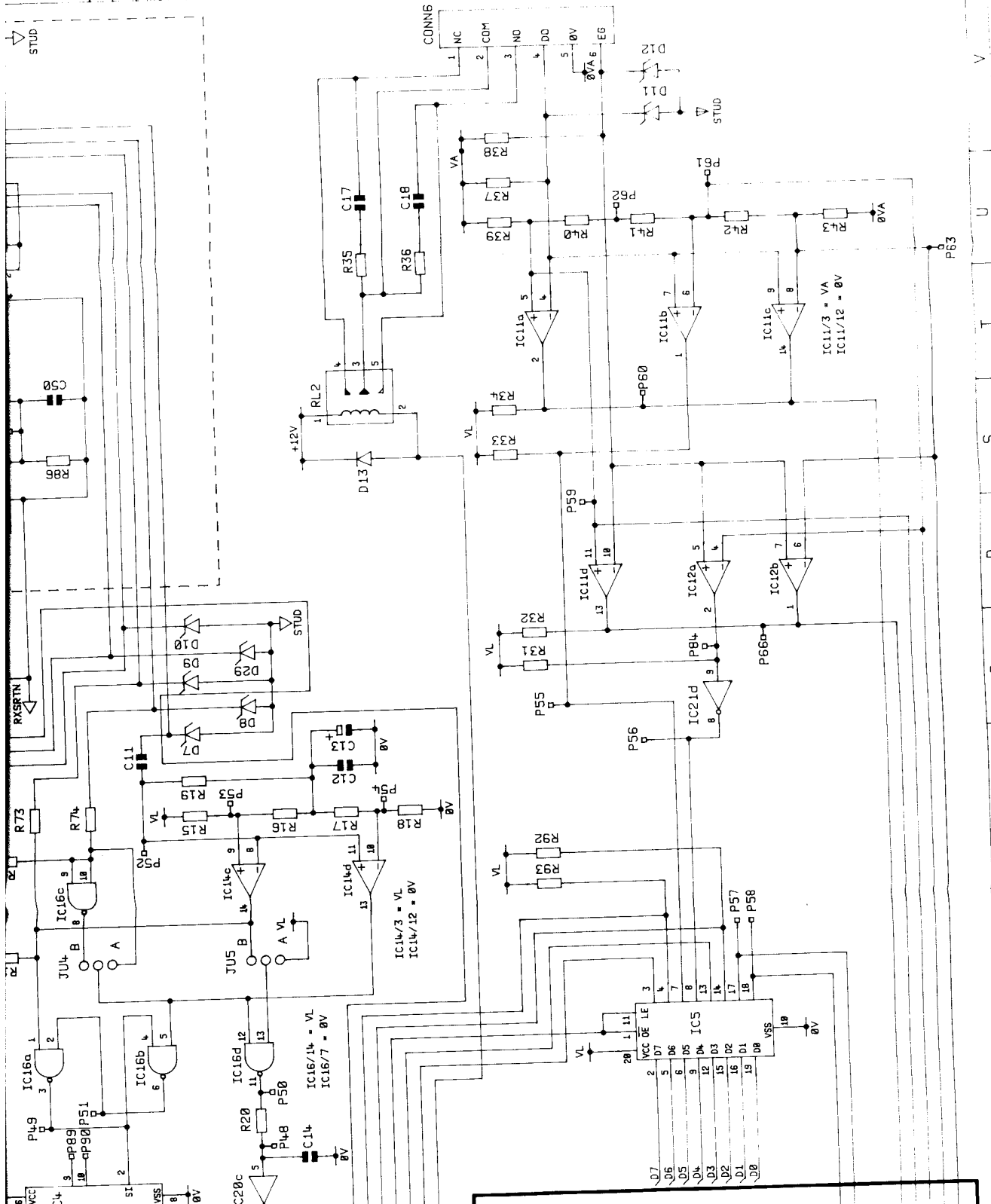
Title: **CIRCUIT DIAGRAM FOR**

Mk 2 READER MODULE

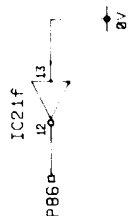
Drawing Number:

CD02/135

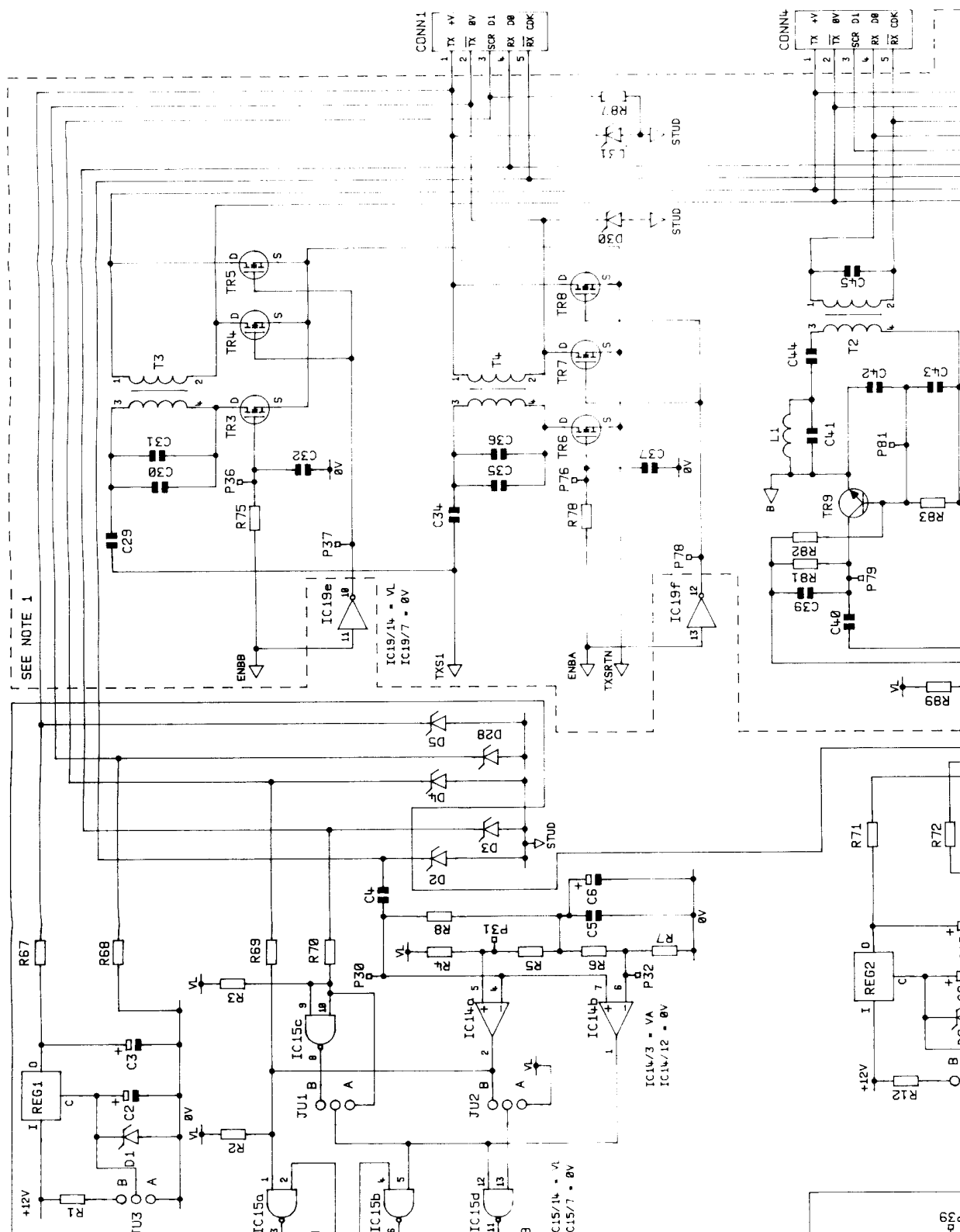
Sht. 1 of: 1



1. Components only fitted to Catag (mfl version of PCA).
2. Components only fitted to swipe reader version of PCA.



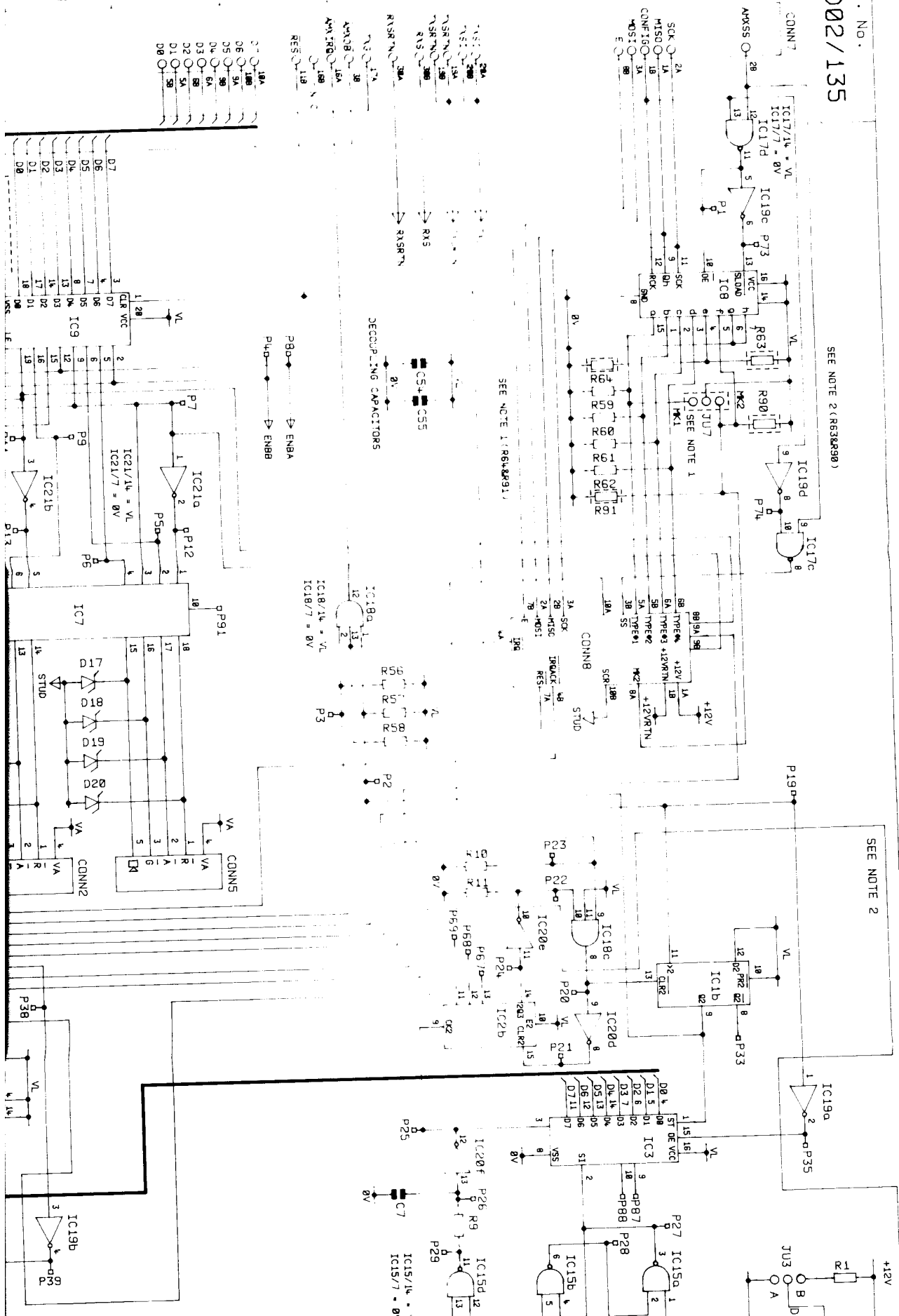
SPARE GATE

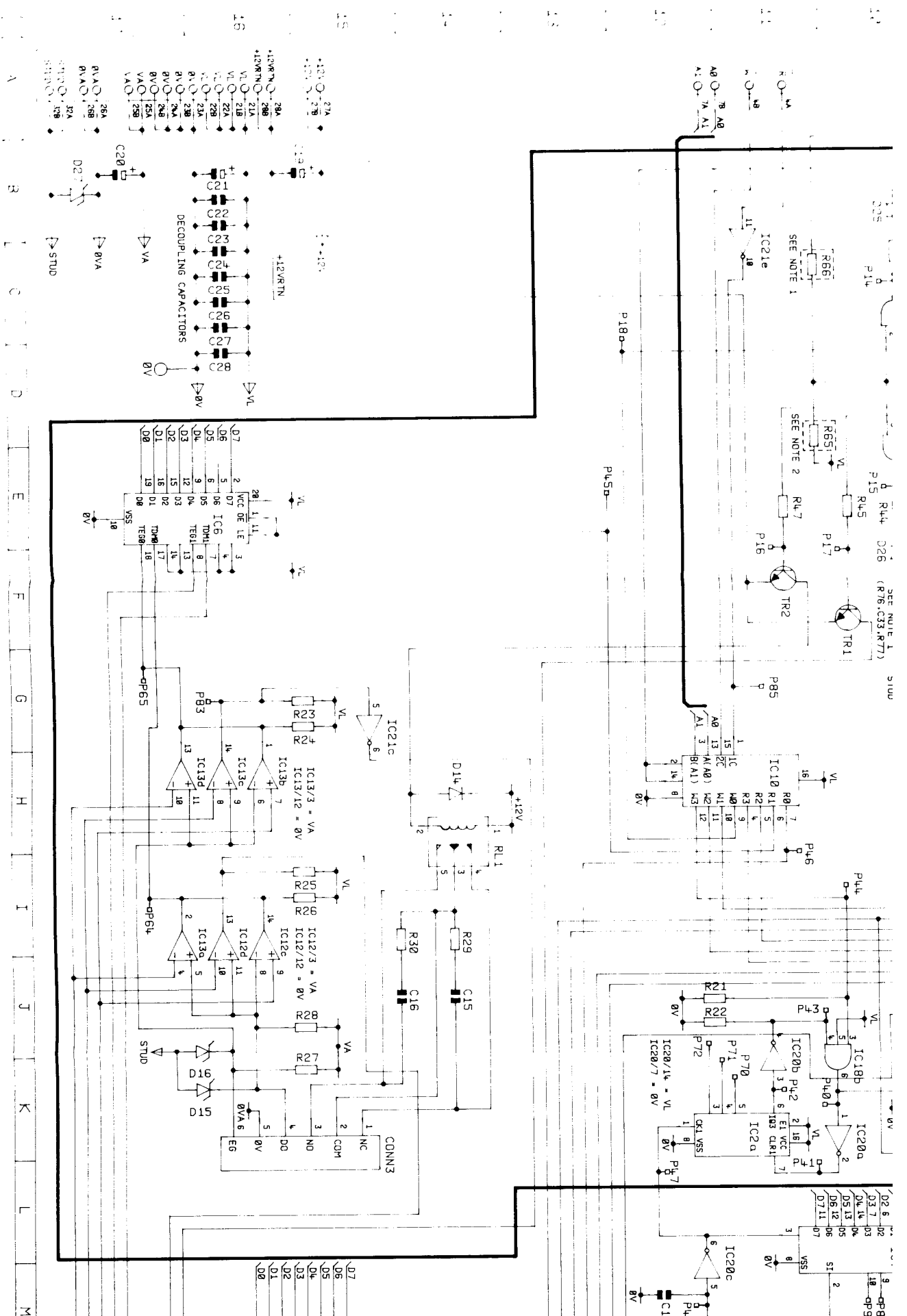


CD02/135

SEE NOTE 2 (R63&R90)

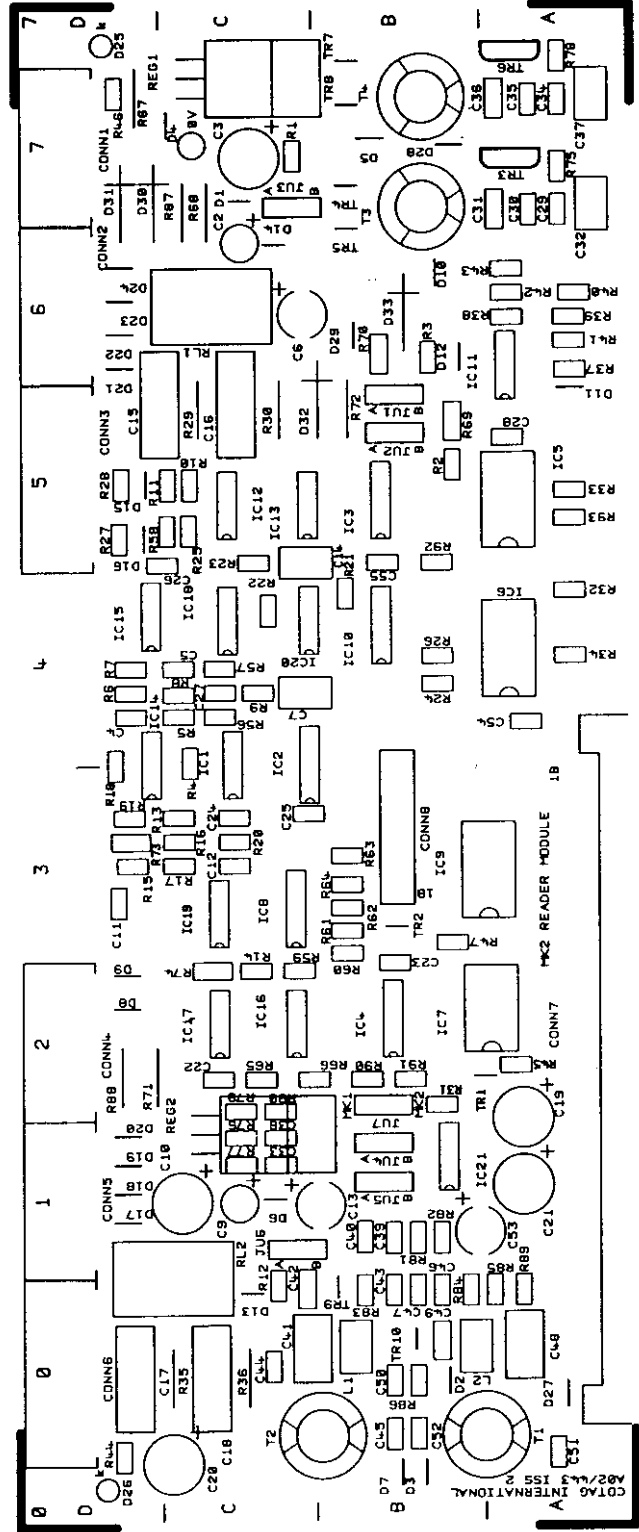
SEE NOTE 2





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ISSUE RECORD	
SHEET	ISSUE
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3	2
4	2
5	2
6	2
7	2

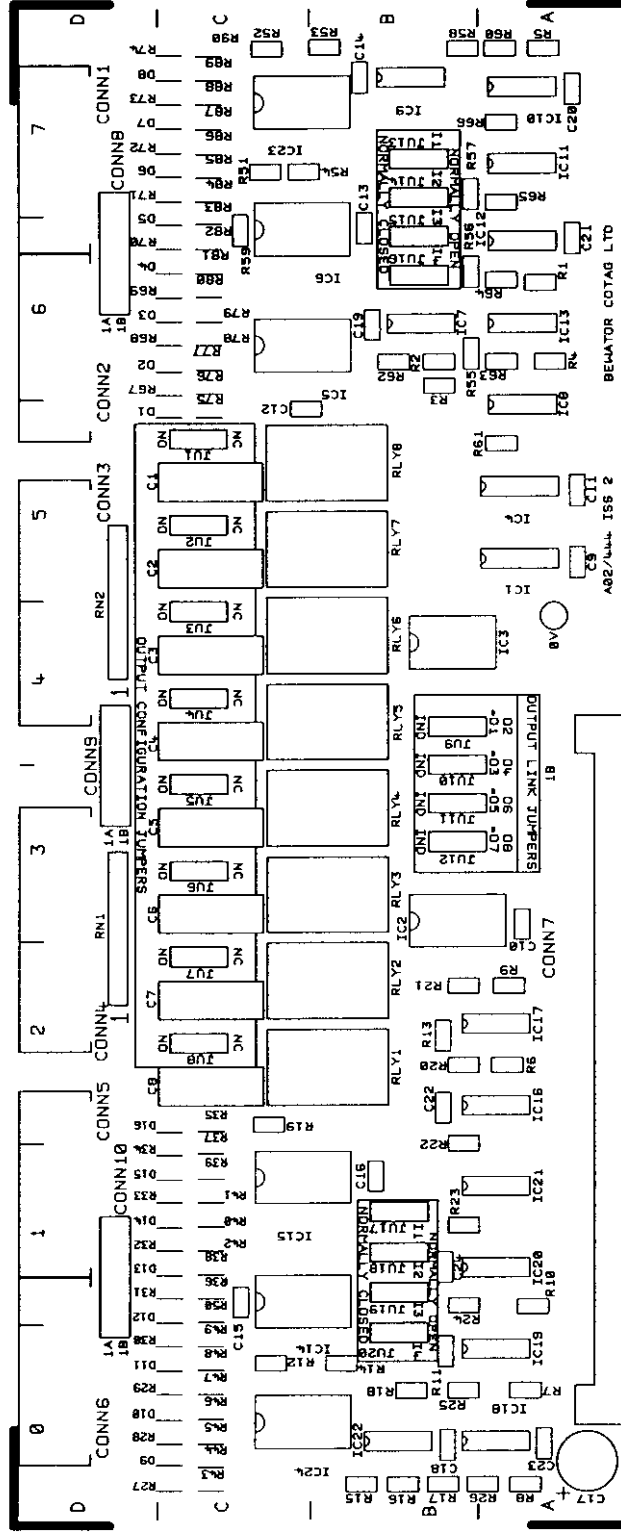


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ISSUE RECORD		
SHEET	ISSUE	
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3	2	
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6	2	
7	2	

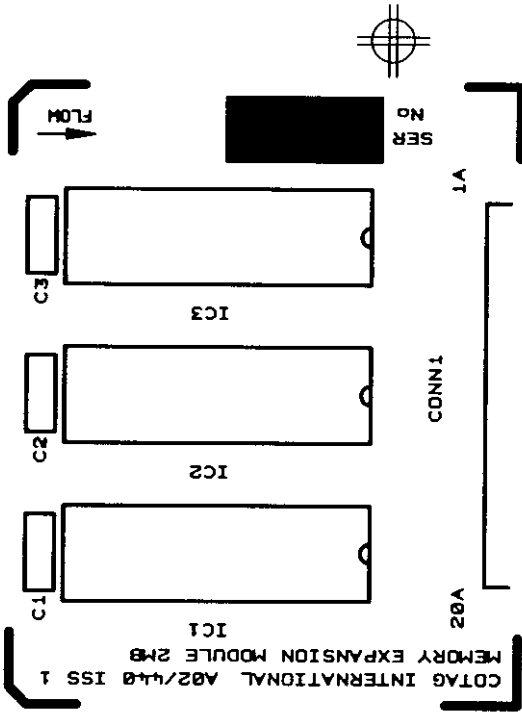


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ISSUE RECORD	
SHEET	ISSUE
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2	1
3	1
4	1
5	1

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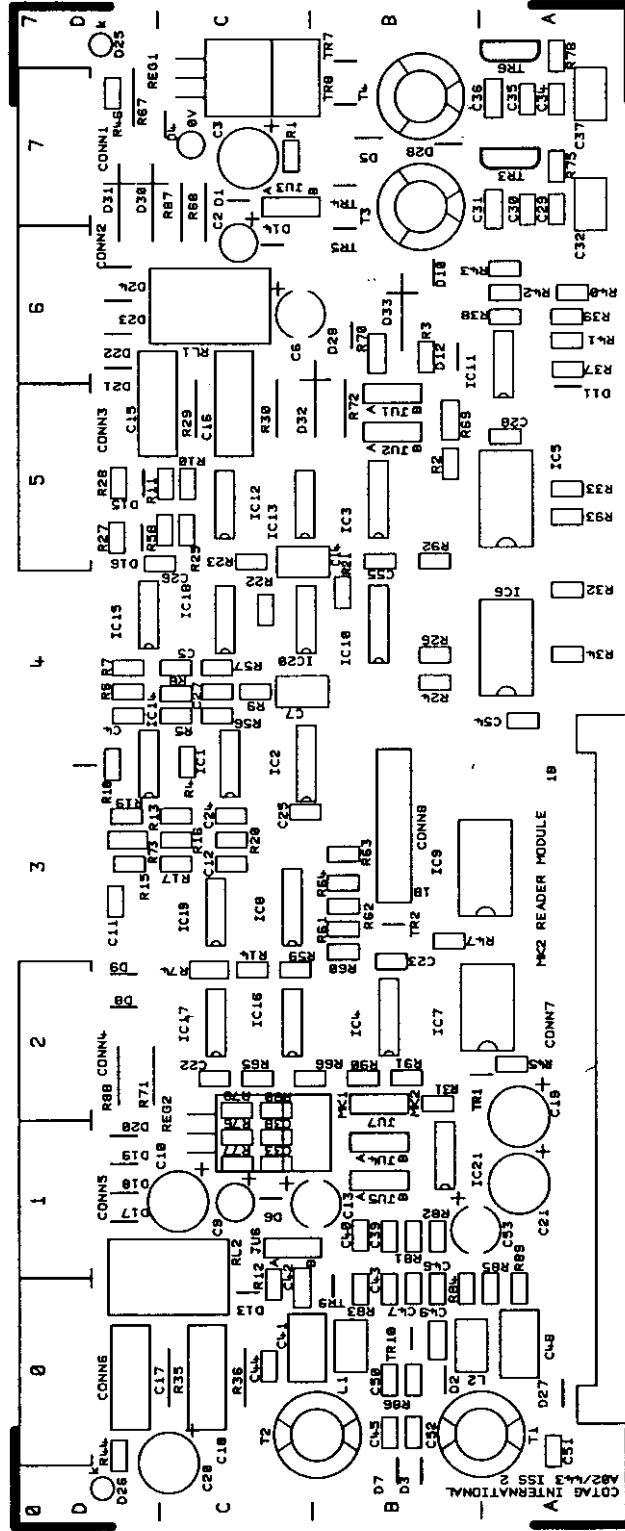
1	05-03-98	VL-44
Iss	Date	Revision
		Init

LEGEND A/W FOR		
MEMORY EXPANSION MODULE 2MB		
DRN	Visorlake PCB Designs Ltd.	ISSUE
01702 58446	05-03-98	1
CHKD.	DRG NO.	SH3
	A02/440	OF 5

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ISSUE RECORD	
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1	2
2	2
3	2
4	2
5	2
6	2
7	2



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