

440BX CUSTOMER REFERENCE DESIGN

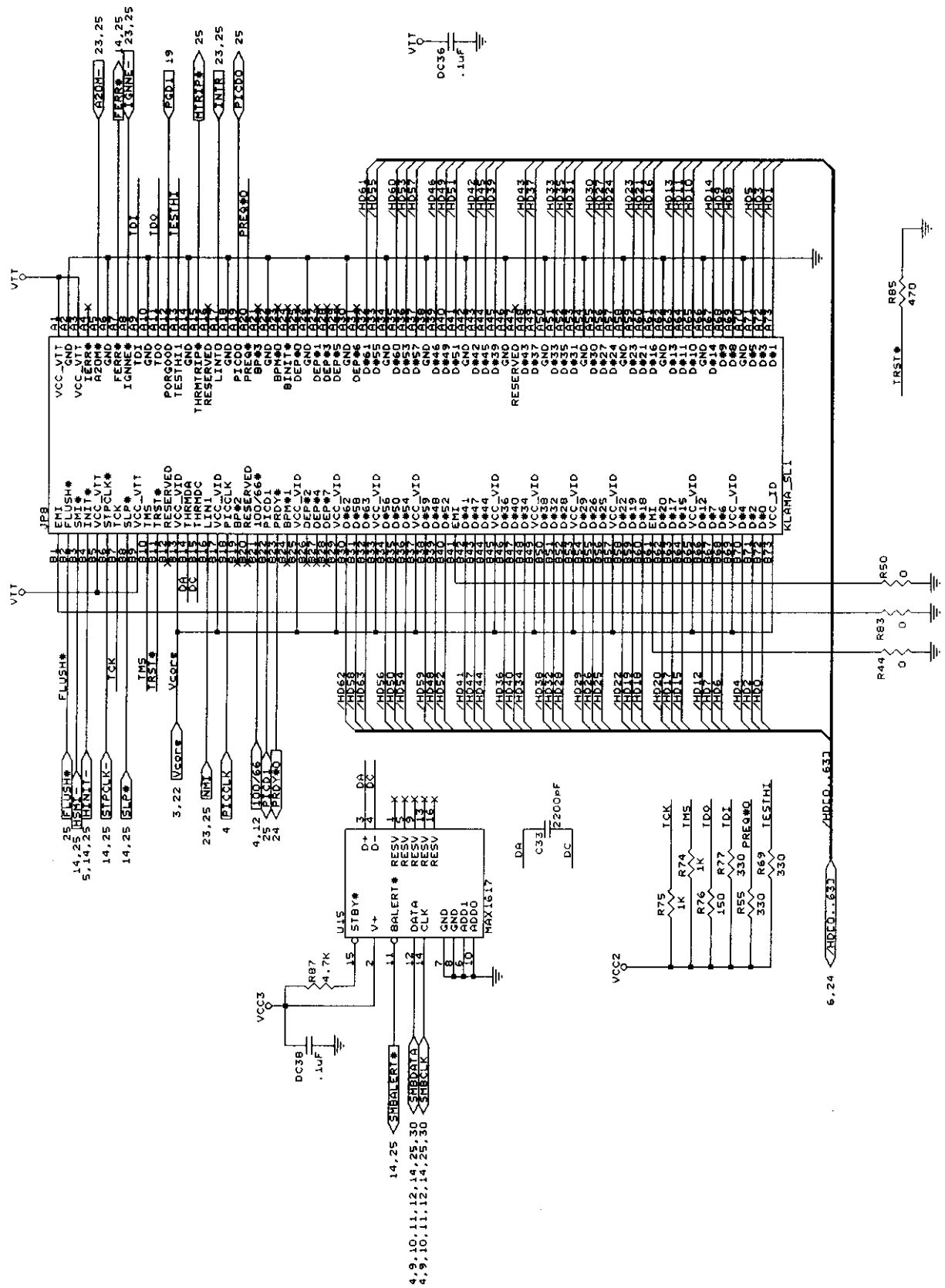
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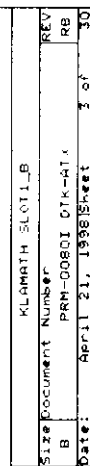
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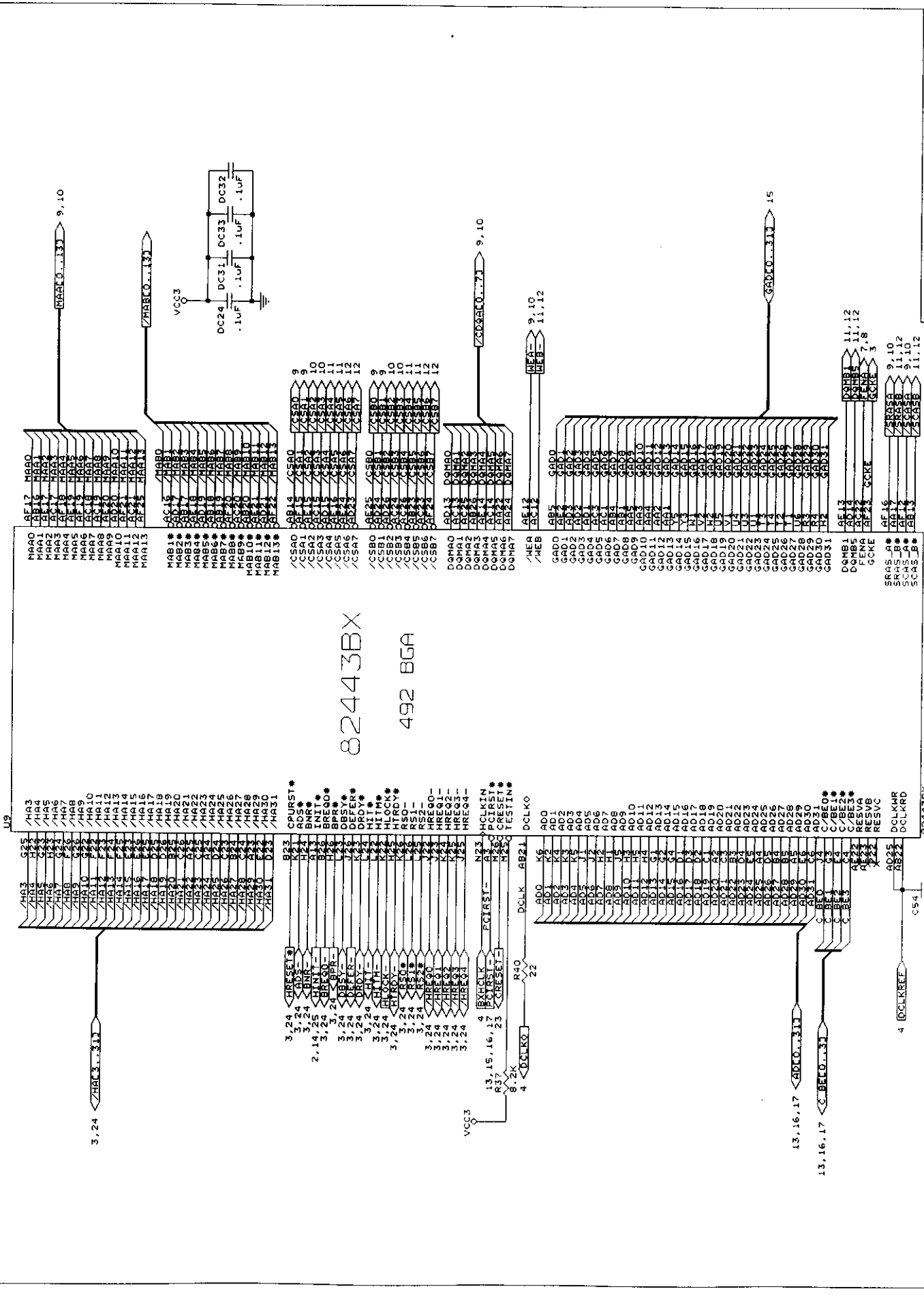
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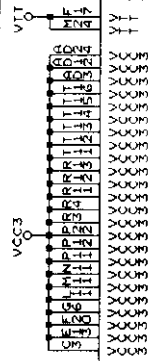
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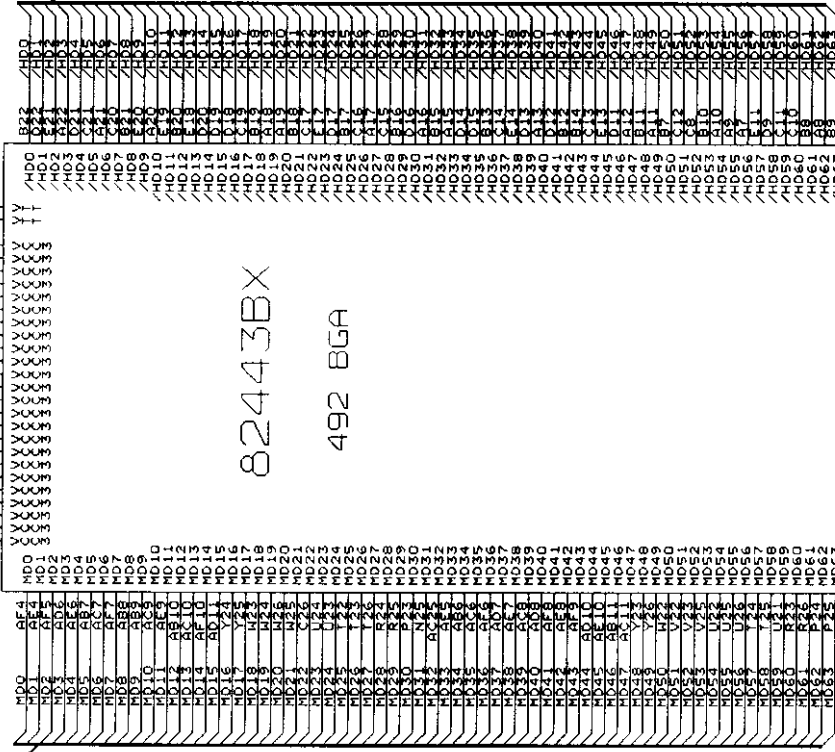
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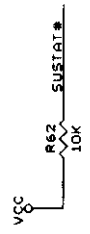


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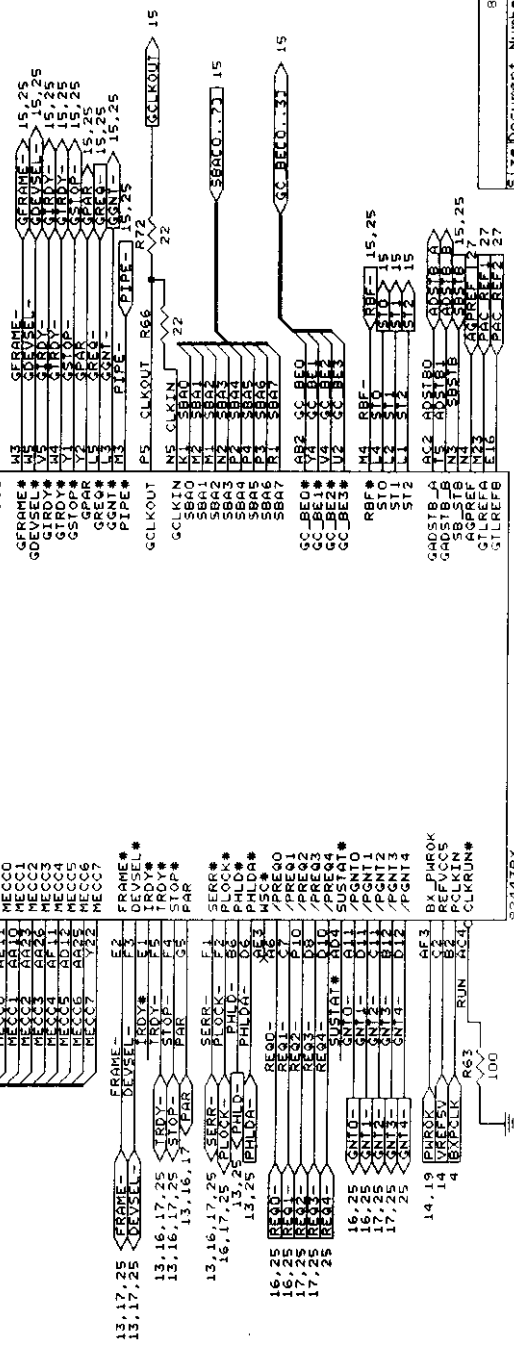


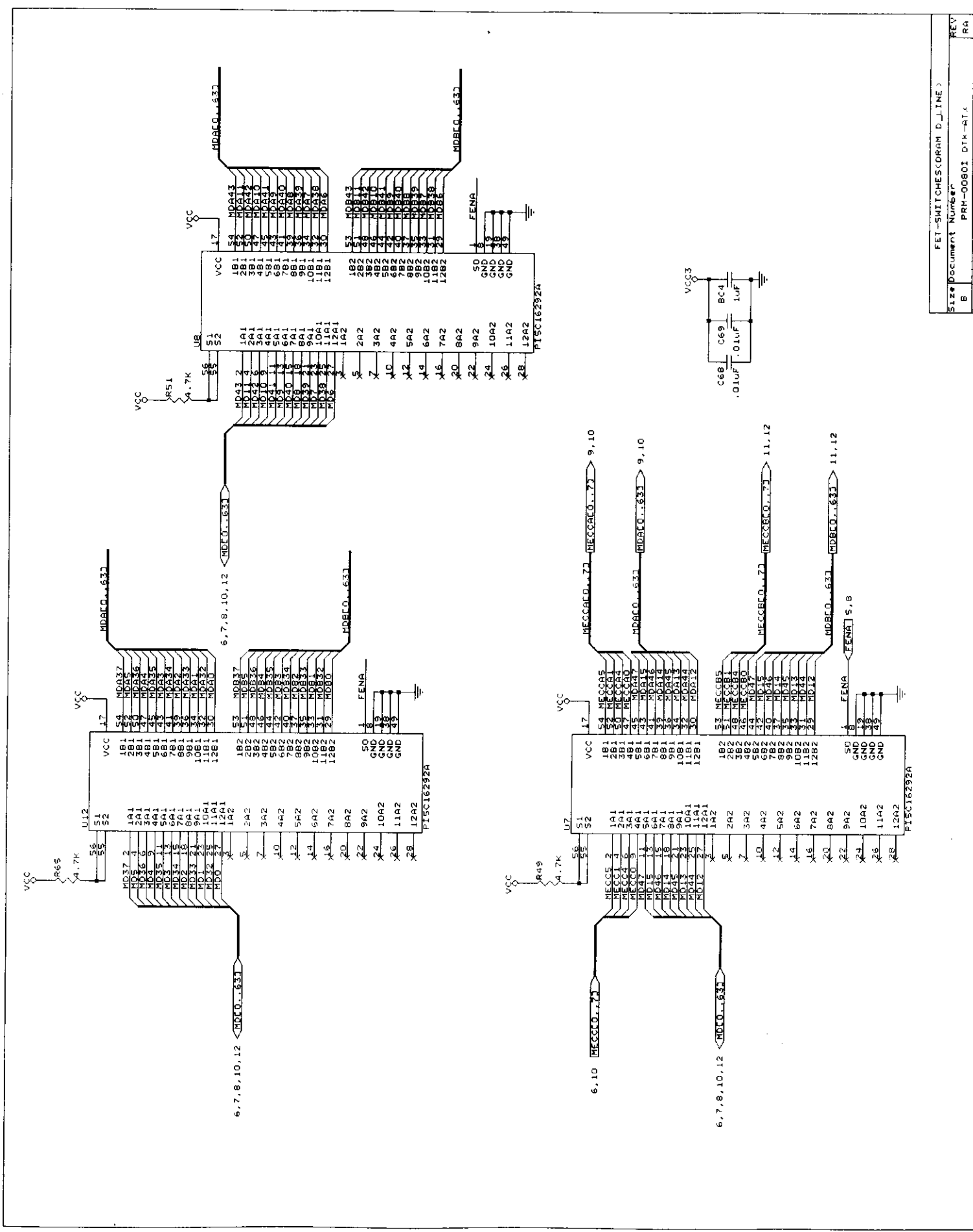
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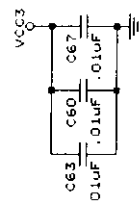
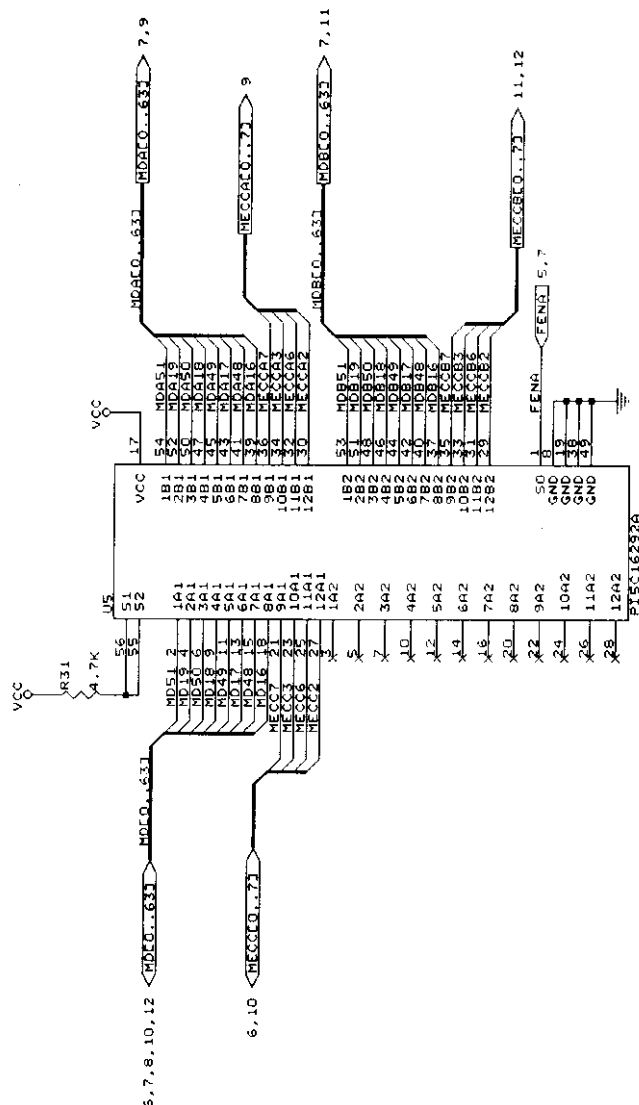
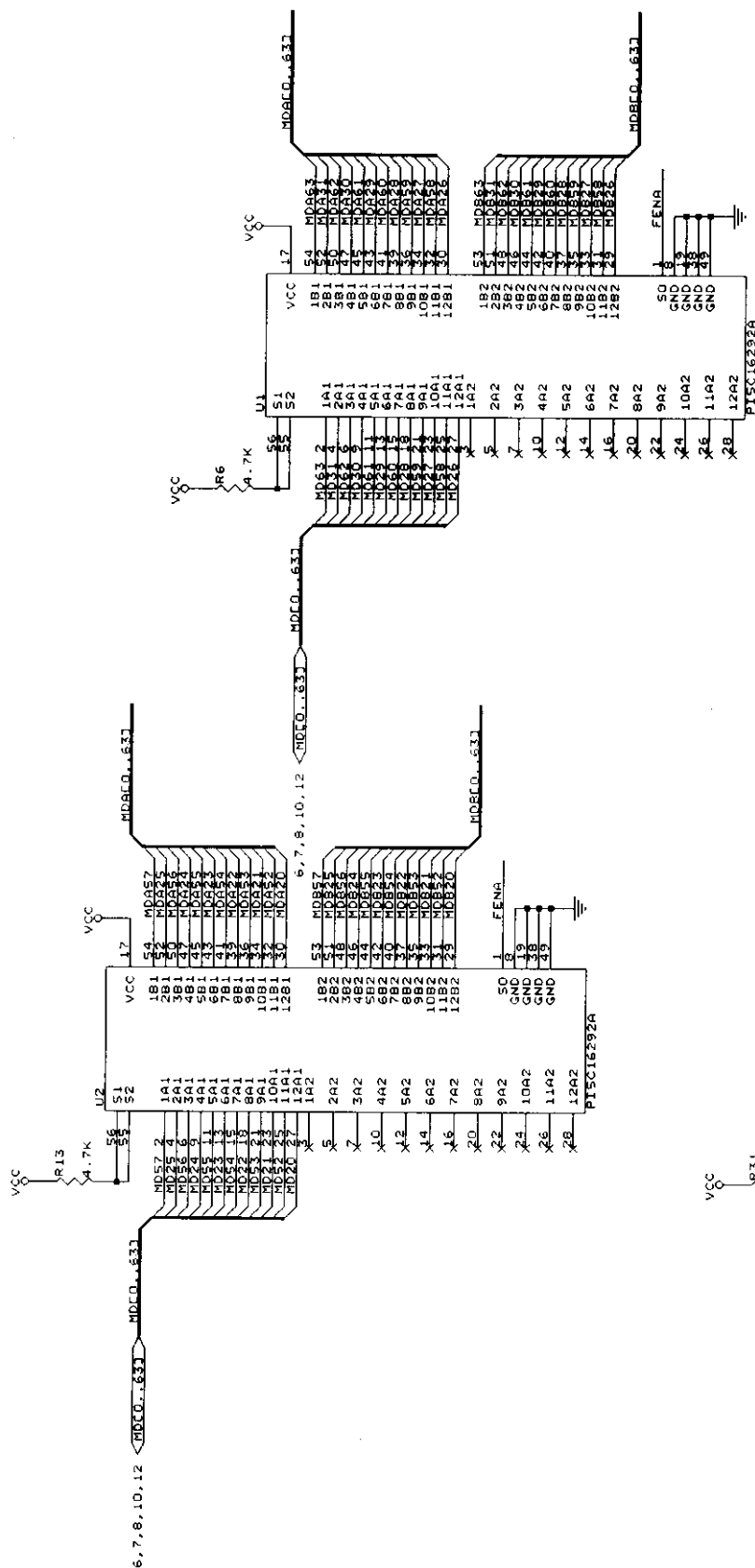
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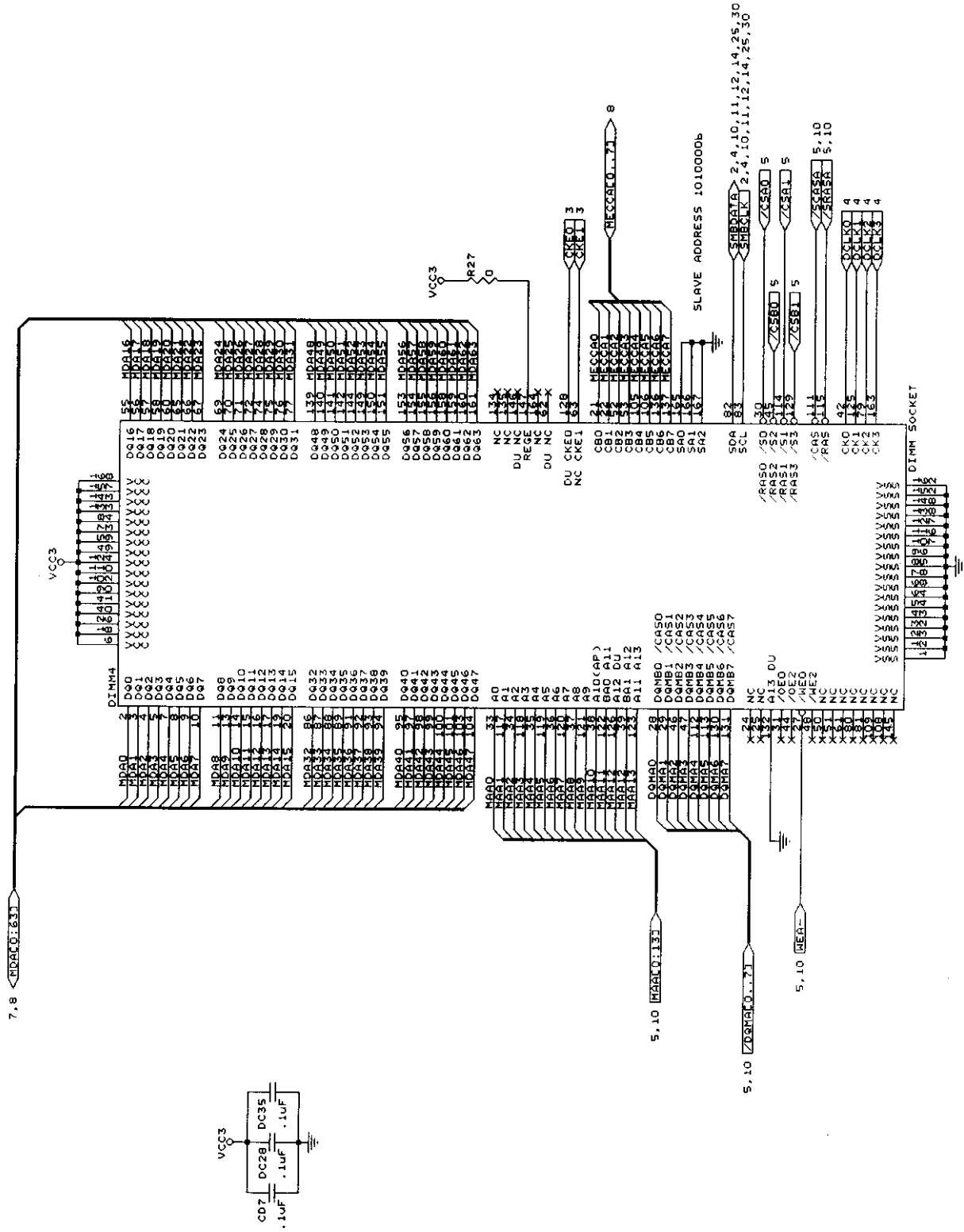
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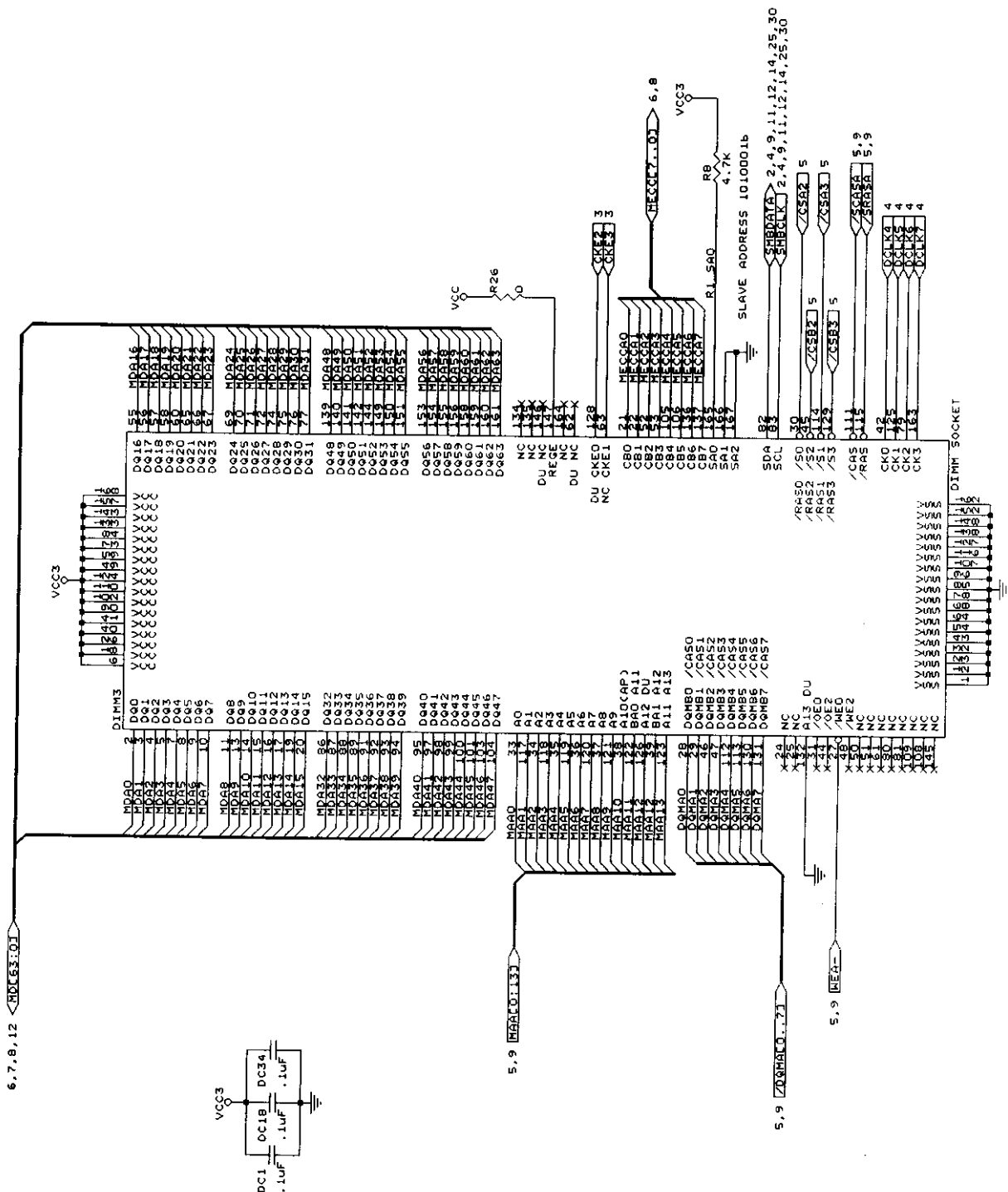




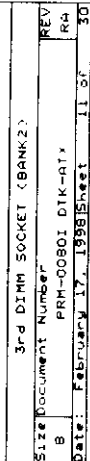
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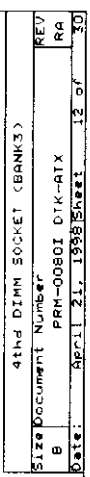
DIMM CONNECTOR 1

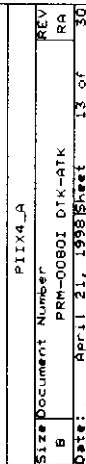


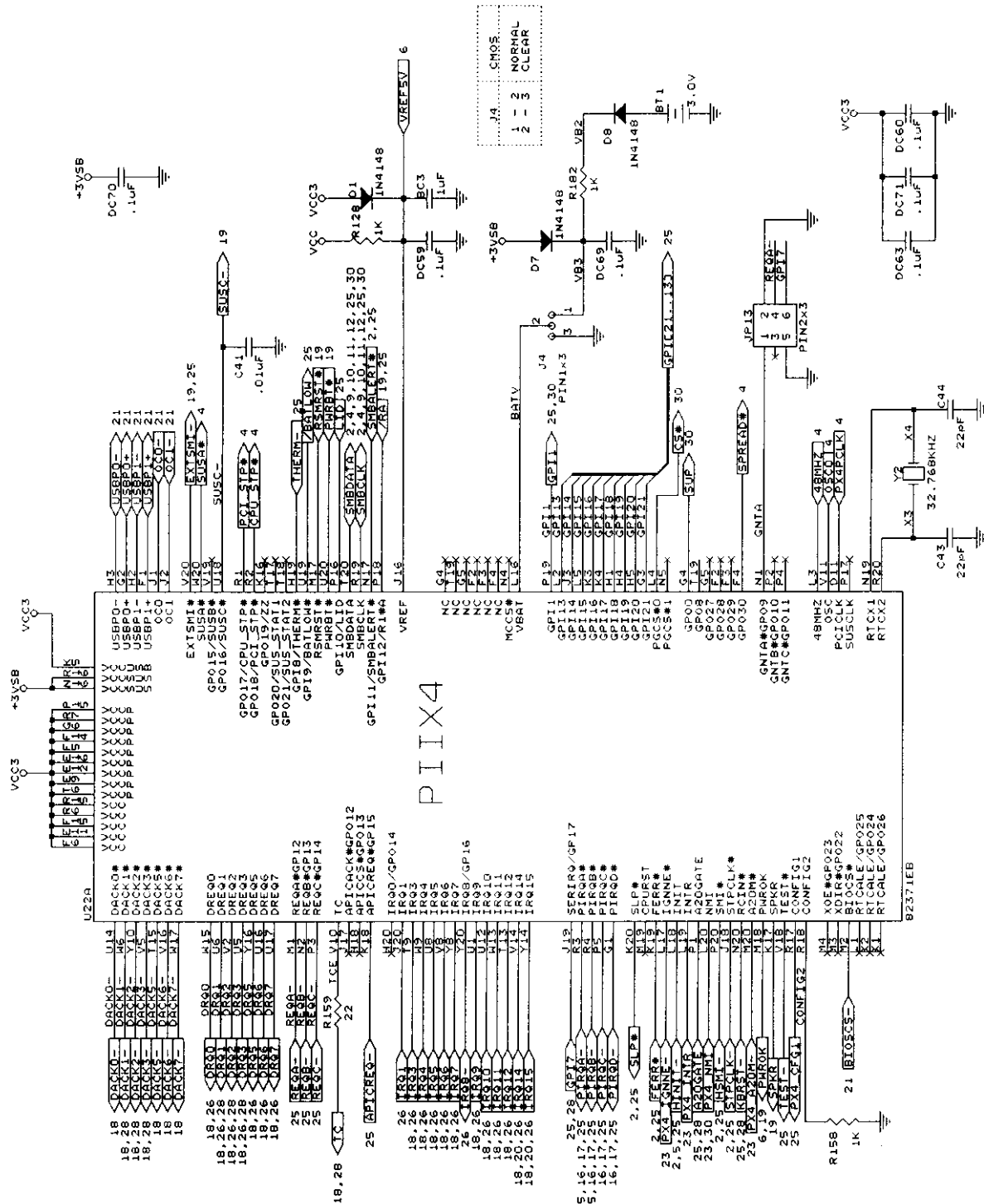
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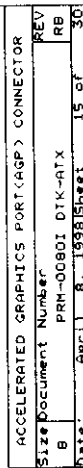


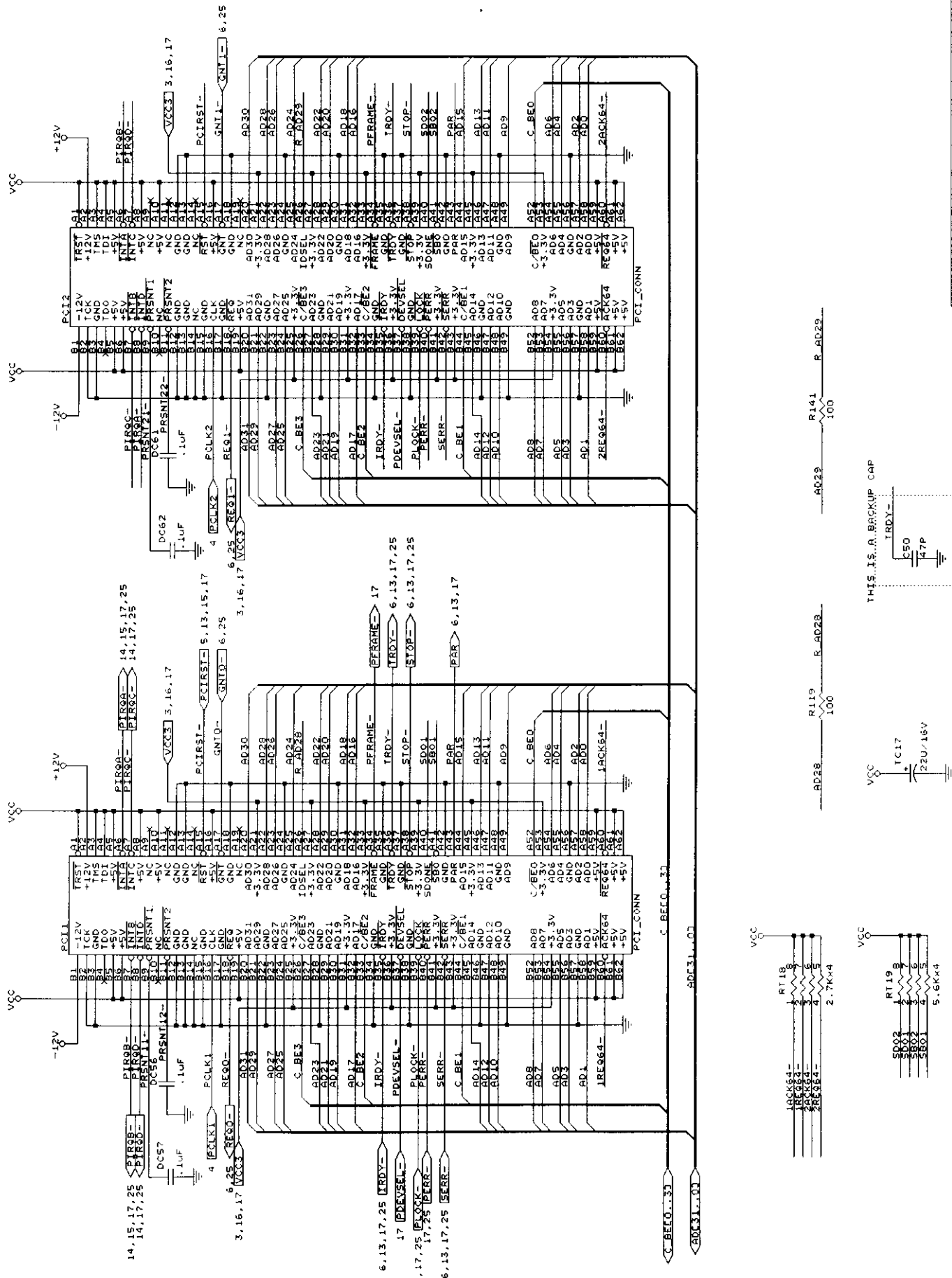
6, 7, 8, 10

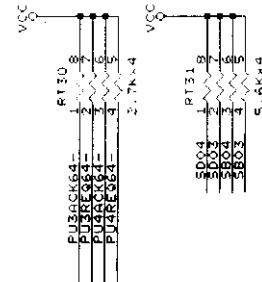








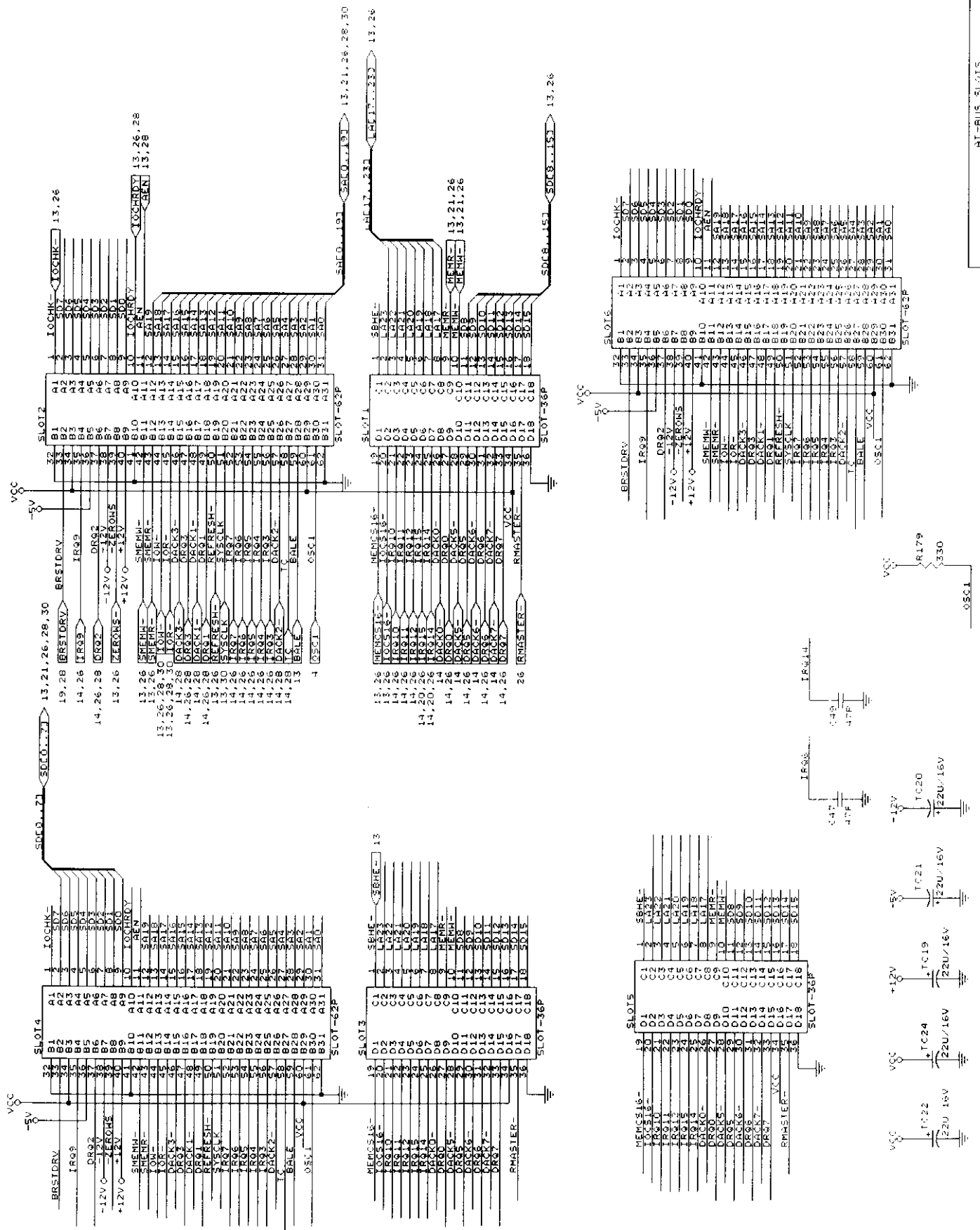


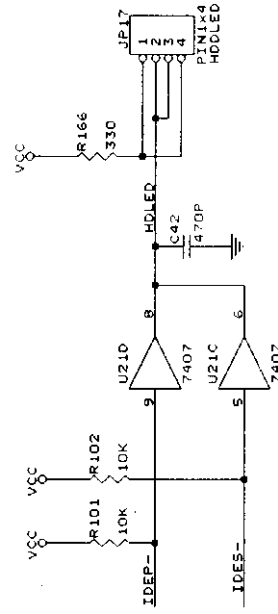
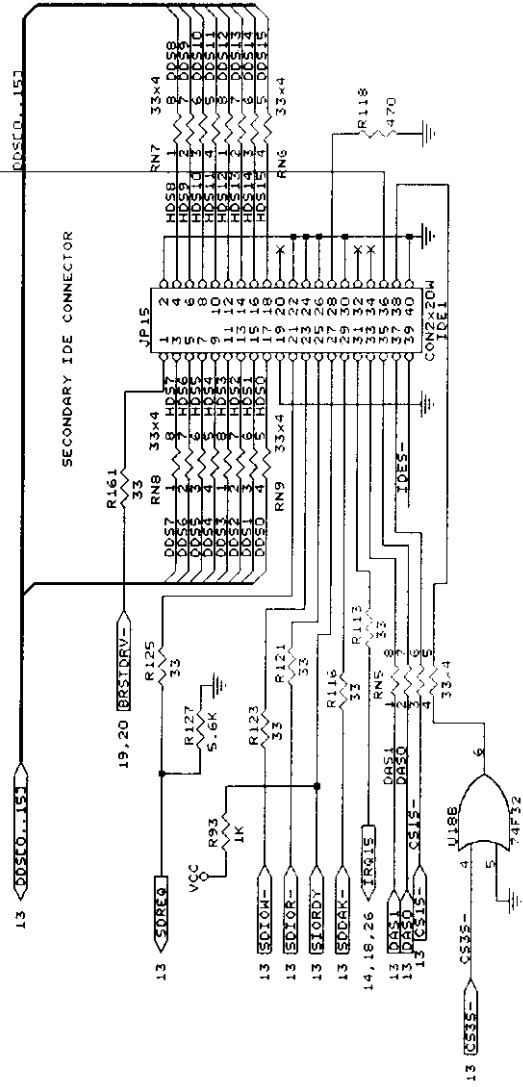
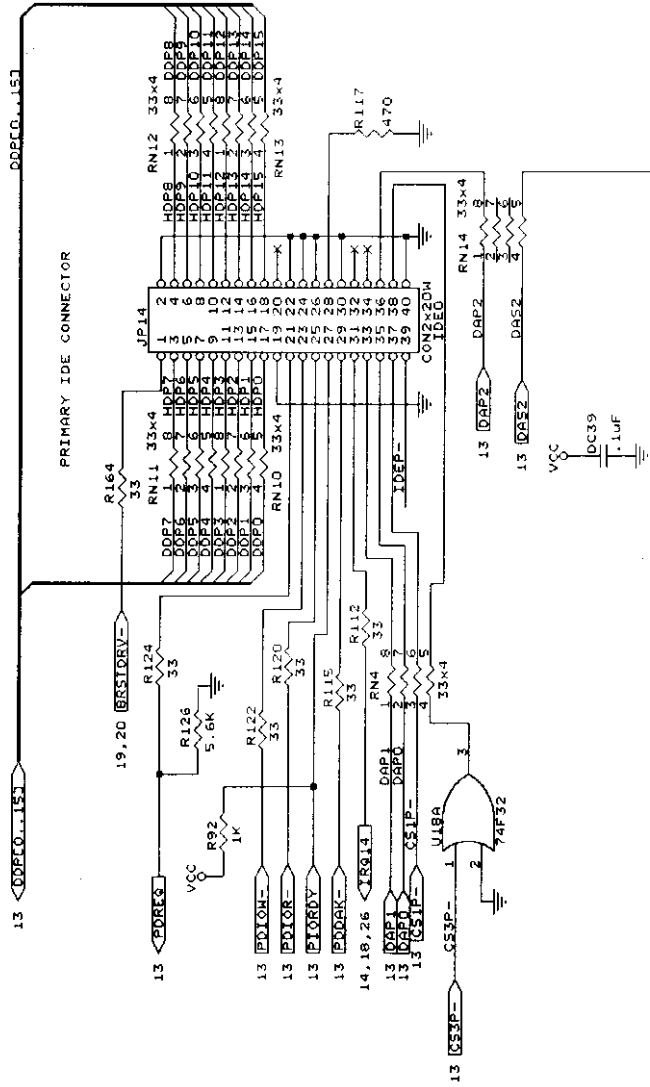
[illegible]

6, 13, 25 DEVSEL - R 140 PDEVSEL -
75

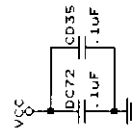
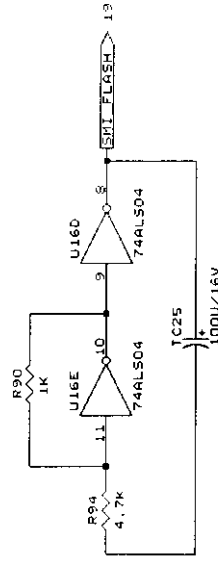
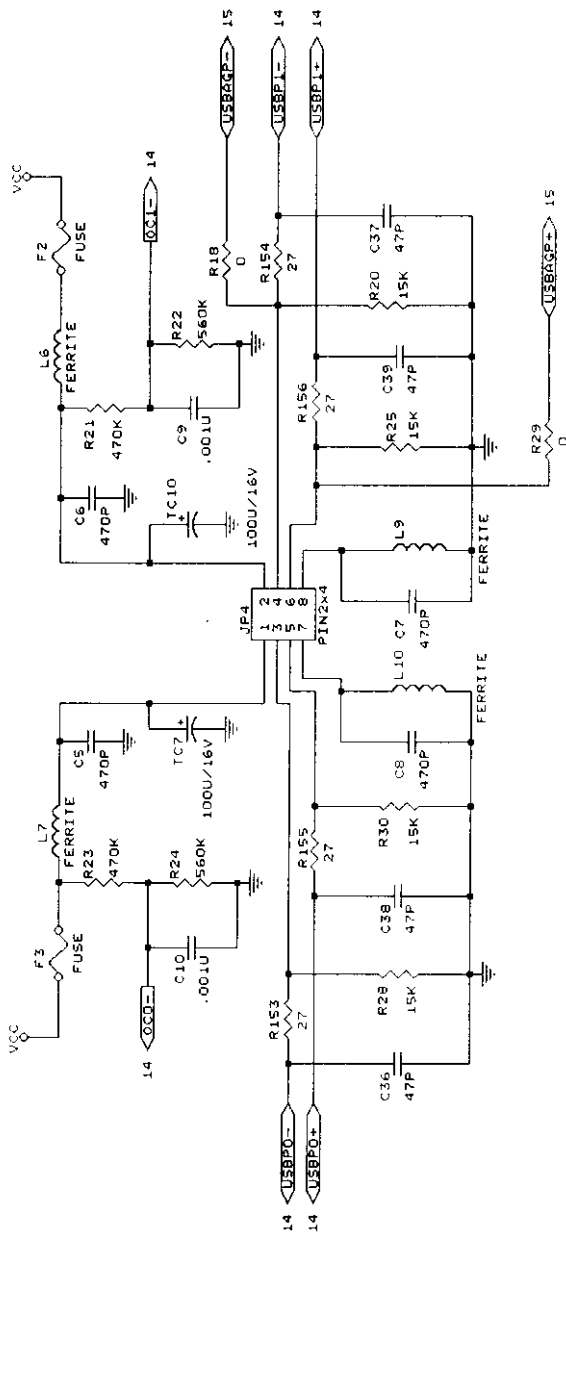
R 4030

6, 13, 25 FRAME - R 142 PFRAME -

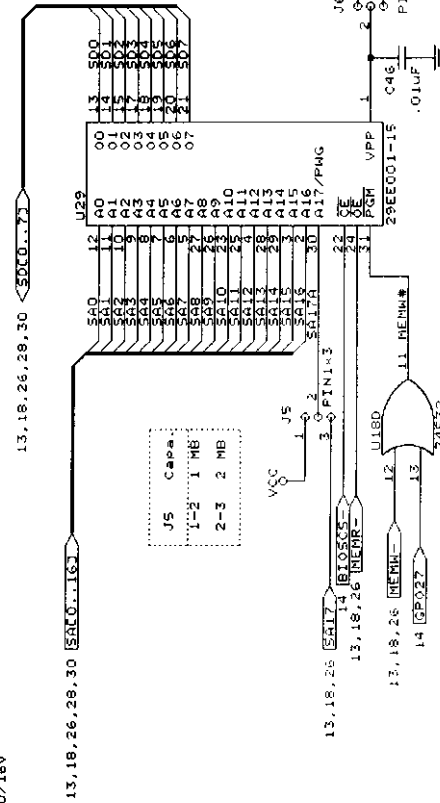


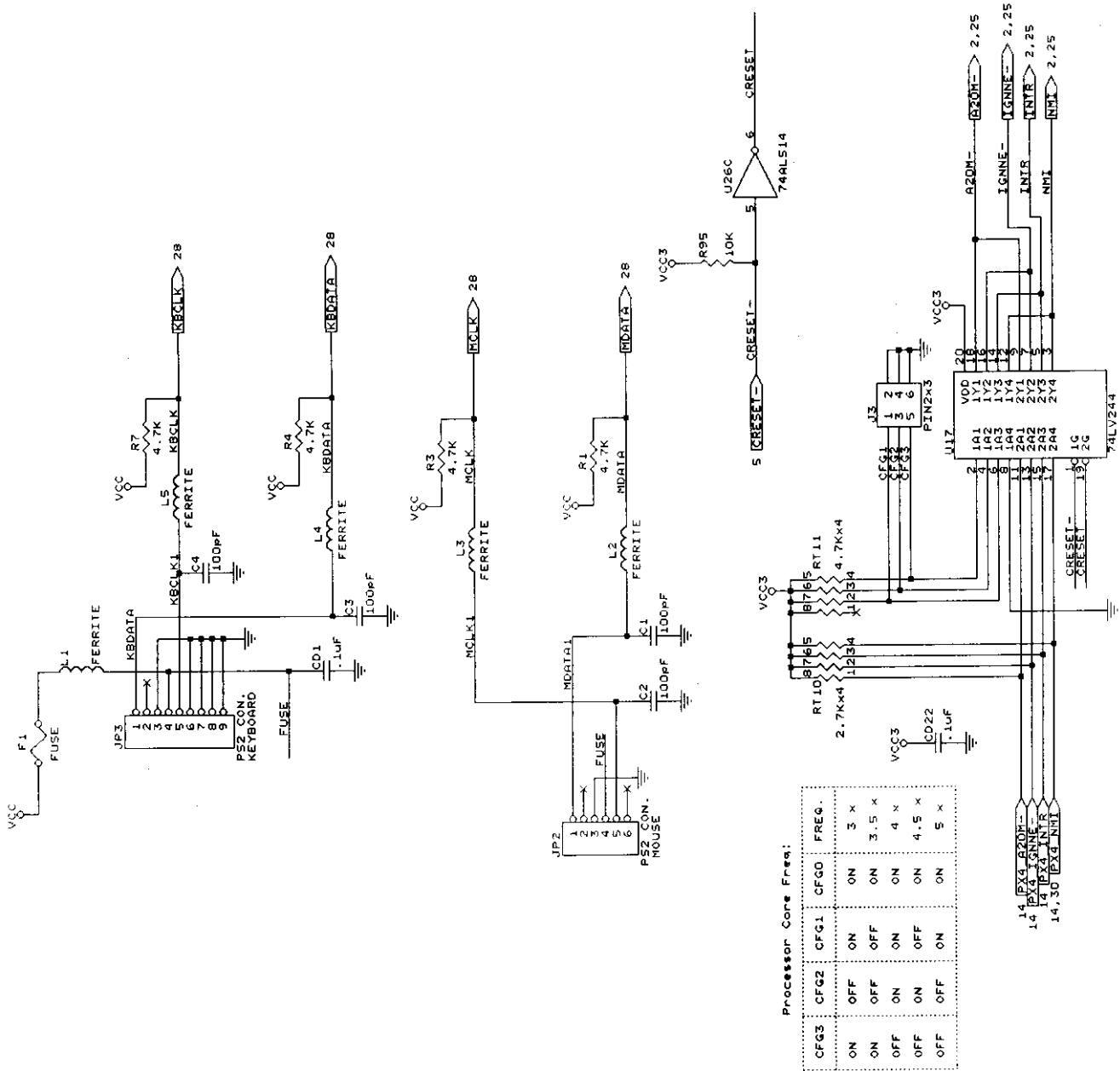


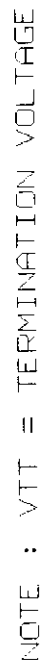
IDE CONNECTORS

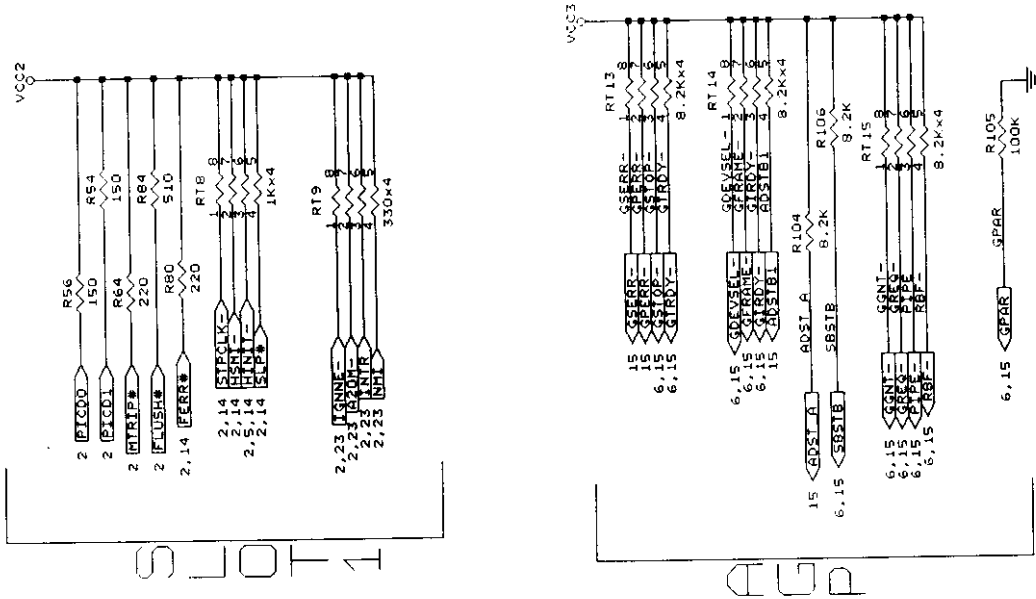
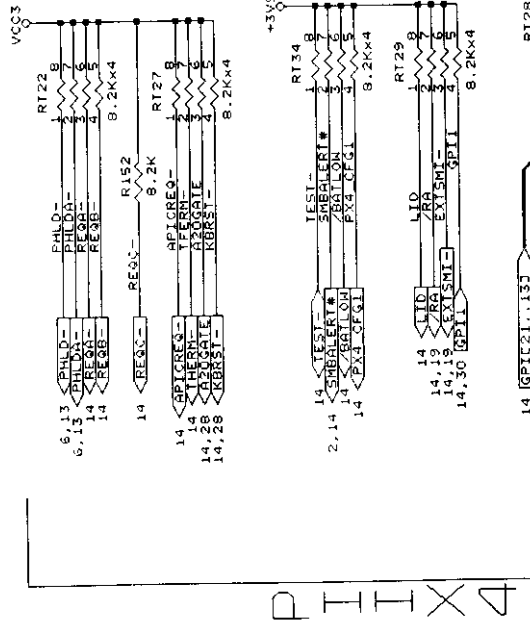
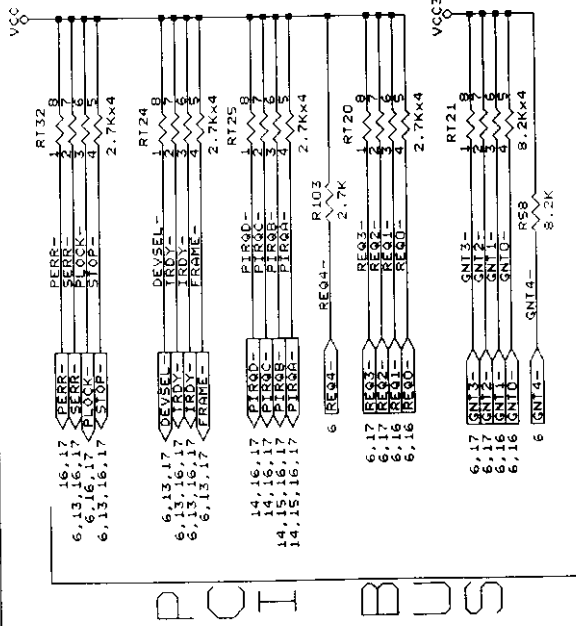


MODE	J6
SST, Winbond	1-2
Intel, MXIC	2-3

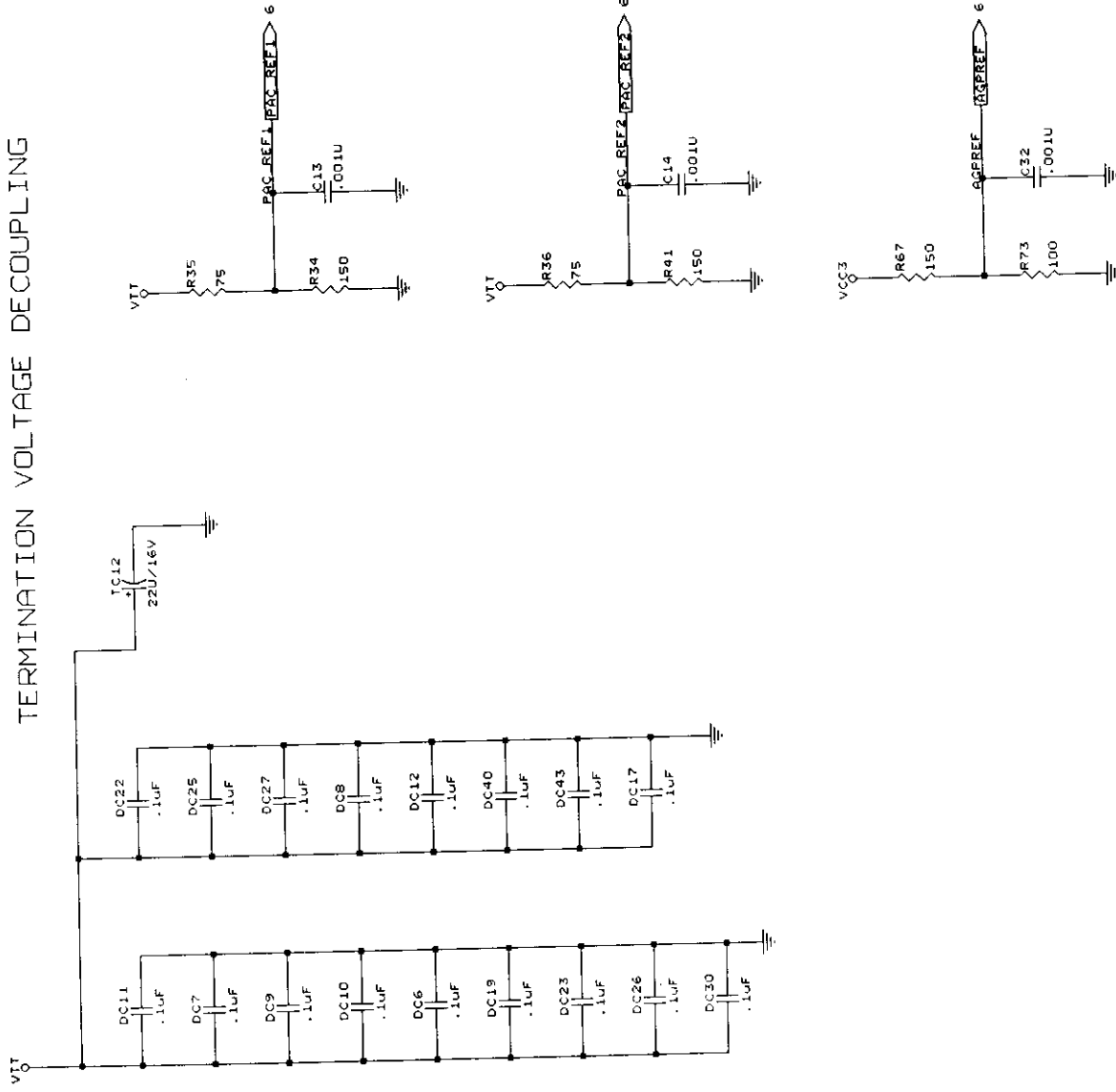




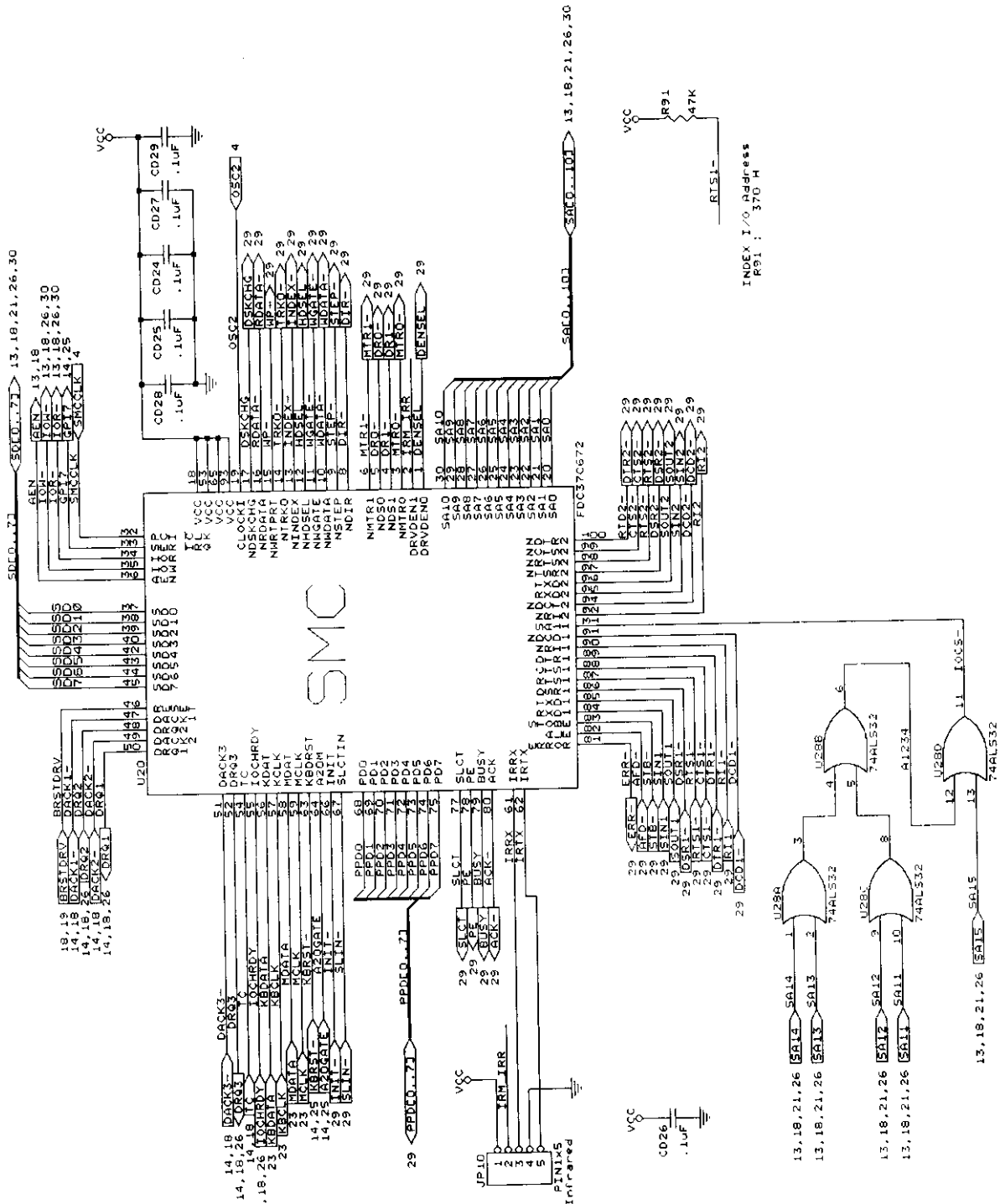




TERMINATION VOLTAGE DECOUPLING



[illegible]



In order to minimize the crosstalk in ECP mode, the termination circuits shown below are recommended. Place all termination circuits close to the connector to avoid EMI

