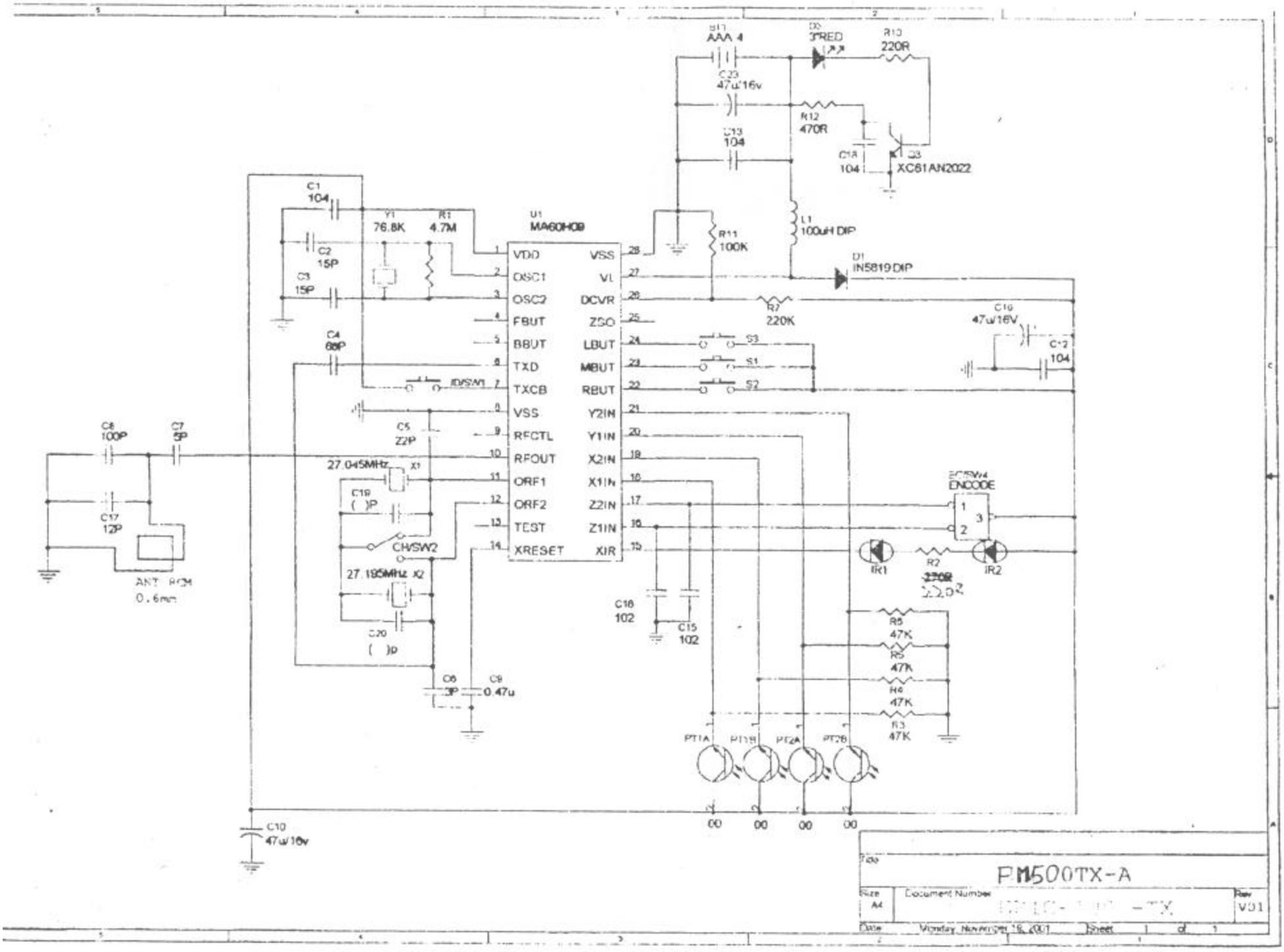

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D2
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R12
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C13
104
C14
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Q3
XC61AN2022
L1
100uH DIP
D1
IN5819 DIP
R11
100K
R7
220K
C1
104
C2
15P
C3
15P
Y1
76.8K
R1
4.7M
U1
MA60H09
VDD
OSC1
OSC2
FBUT
BBUT
TXD
TXCB
VSS
RFCTL
RFOUT
ORF1
ORF2
TEST
XRESET
VSS
VL
DCVR
ZSO
LBUT
MBUT
RBUT
Y2IN
Y1IN
X2IN
X1IN
Z2IN
Z1IN
XIR
C4
68P
C5
Z2P
C8
100P
C7
5P
C17
12P
ANT RCM
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X1
C19
()P
CHSW2
27.195MHz X2
C20
()P
C6
3P
C9
0.47u
C10
47u/16v
C10
47u/16V
C12
104
S3
S1
S2
ENCODE
IR1
R2
220R
IR2
C16
102
C15
102
R5
47K
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R4
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R3
47K
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PT2B
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Rev
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Date
Monday, November 19, 2001
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1