

EXHIBIT D

[FCC Ref. 2.1033(b)(5)]

"Block and Schematic Diagrams"

The schematic diagram illustrates the internal architecture of a mobile phone, centered around the TAD Platform and MCU. The TAD Platform (DSP(D36529)) is connected to various peripheral components:

- RF Section:** Includes the TB31262F RF IC, PLL & Frequency Synthesizer, Data Latch Control, RX VCO, TX VCO, Frequency Doubler, TX Power Amp, TX Loop Filter, and RX Loop Filter. It also features a Reference Frequency Oscillator Circuit (X2, 4MHz).
- MCU Section:** The MCU EM78908 is connected to the TAD Platform and manages the phone's operation. It includes a 3V3 DC Regulator, a 5V DC Regulator, and a Hi/Lo Charge Circuit.
- Power Management:** The system uses a 9V DC Jack, a 5V DC Regulator, and a 3V3 DC Regulator to provide power to the various components. A Hi/Lo Charge Circuit is also present.
- Audio and Control:** The TAD Platform is connected to a Ring Decoding Circuit, a Parallel Phone Detection circuit, a Speech Circuitry, and a Seize Line Control. It also interfaces with a Microphone, Speaker, and a Display/ANS Function Keys.
- Memory and I/O:** The TAD Platform is connected to Flash Memory, a Reset Circuit, and a Power Down Circuit. It also interfaces with a Microphone and a Speaker.

The diagram shows the complex interconnections between these components, including signal paths, power lines, and control signals. Key components like the TAD Platform, MCU, and RF IC are highlighted, along with their respective pin connections and external components like capacitors and inductors.

Handset Functional Block Diagram Of Model 26999A Cordless Phone

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