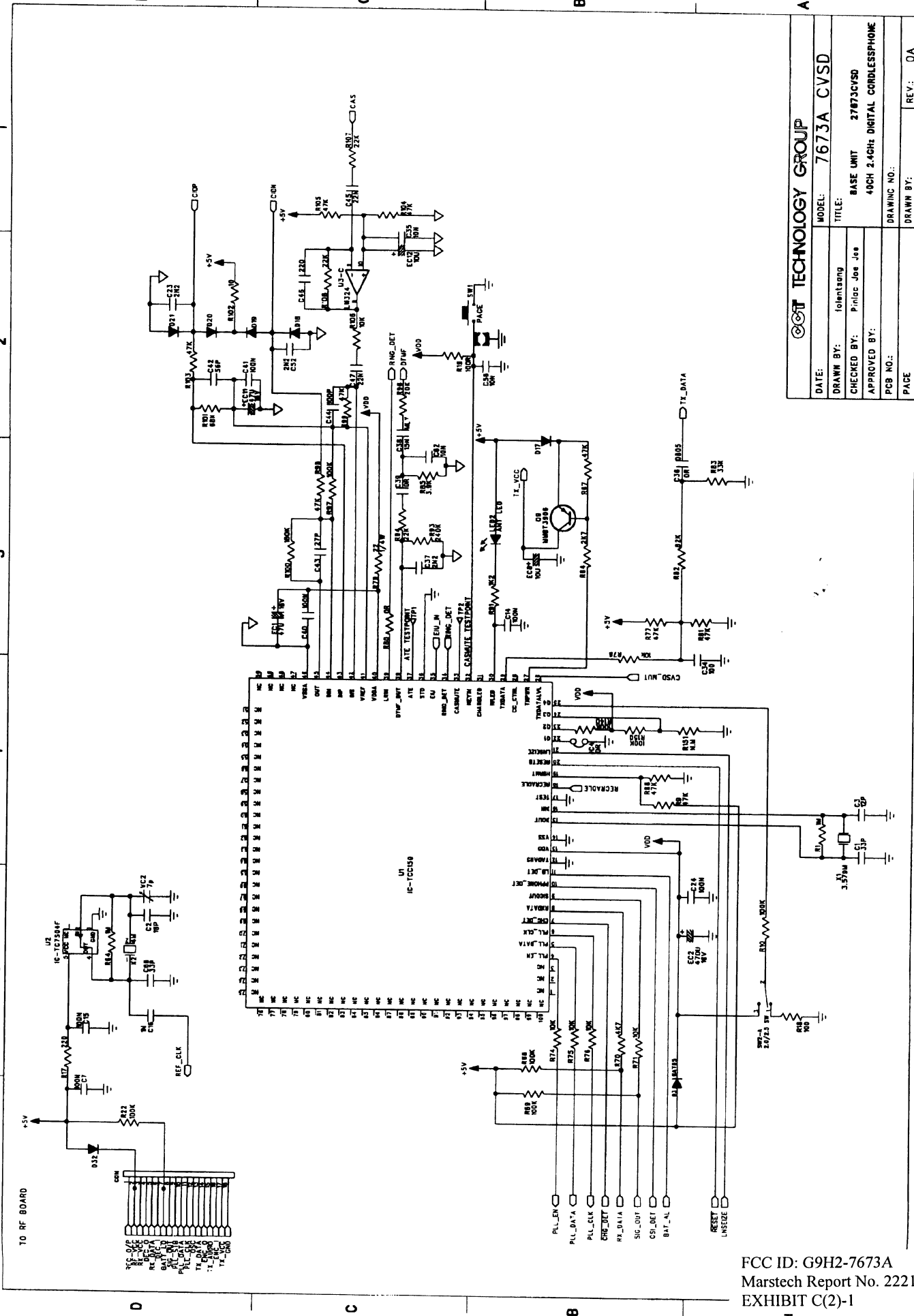
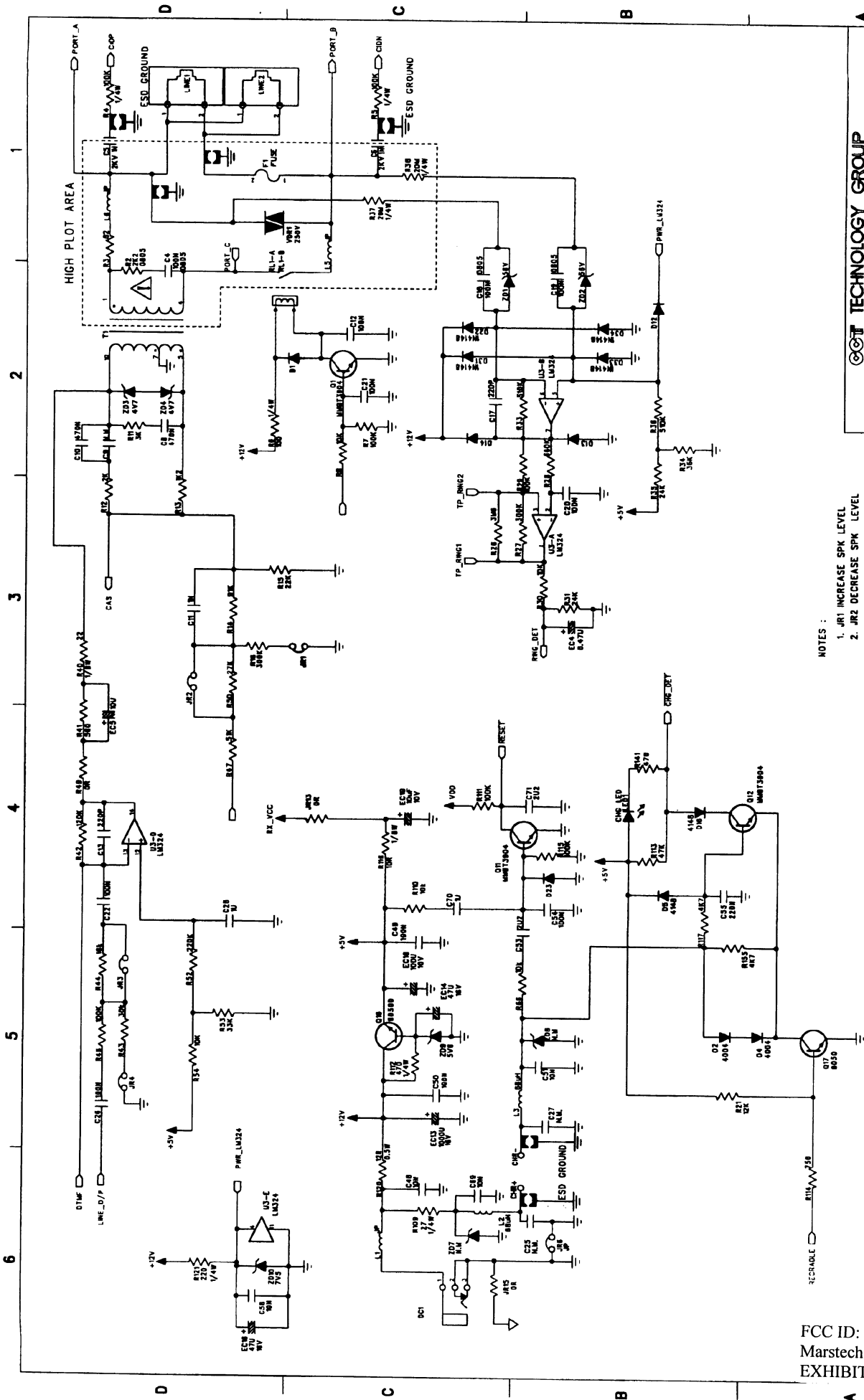




 COT TECHNOLOGY GROUP	
DATE:	MODEL: 7673A CVSD
DRAWN BY: Iolentzang	TITLE:
CHECKED BY: Pinlac Joe Joe	BASE UNIT 27673CVSD
APPROVED BY:	40CH 2.4GHZ DIGITAL CONDLESSPHONE
PCB NO.:	DRAWING NO.:
PAGE	DRAWN BY: REV: 0A

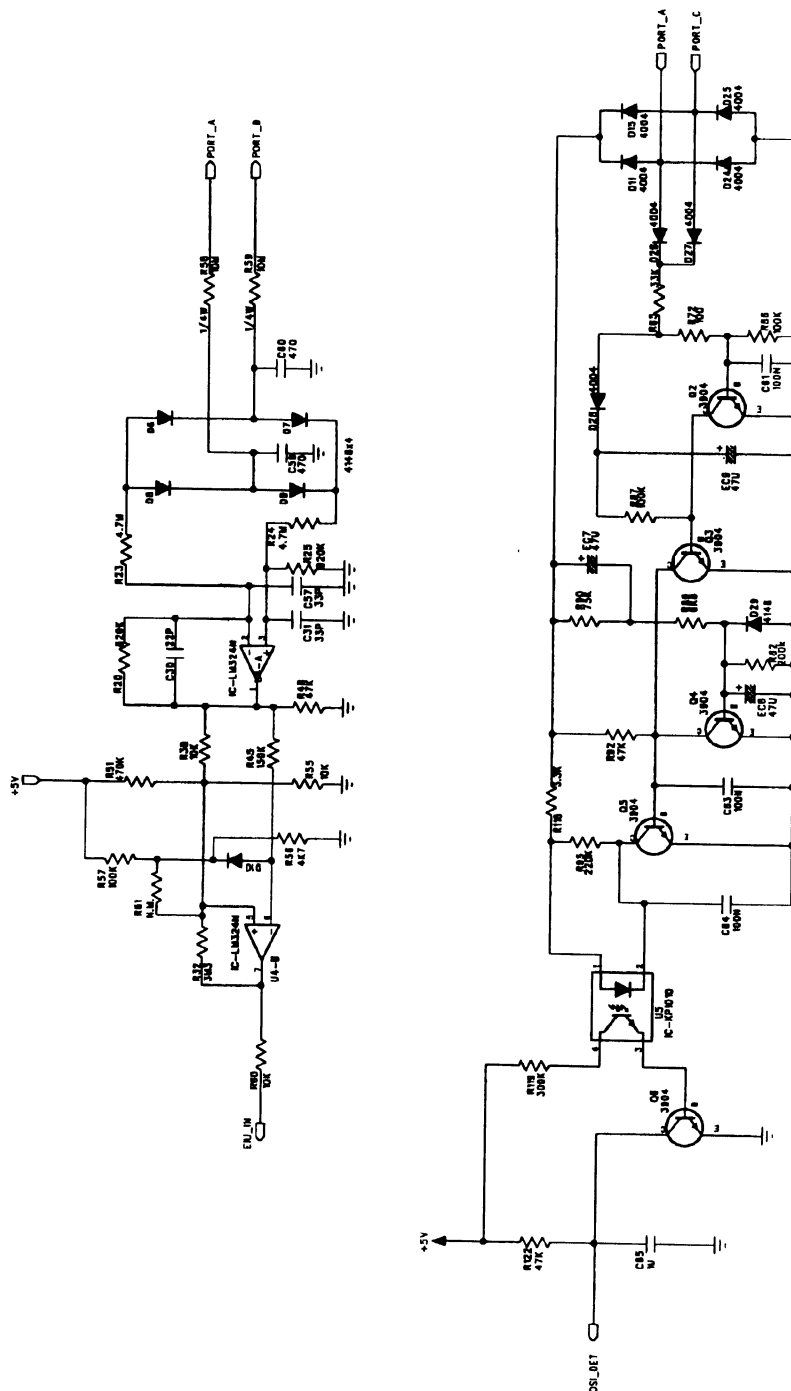


- NOTES :
1. JR1 INCREASE SPK LEVEL
 2. JR2 DECREASE SPK LEVEL
 3. JR4 INCREASE MIC LEVEL
 4. JR3 DECREASE MIC LEVEL
 5. SAFETY COMPONENTS
 6. RESISTOR VALUE : OHM
 7. CAPACITOR VALUE : uF OR F
 8. DIODE : 1N4148 UNLESS SPECIFIED
 9. N.M : NOT MOUNT

GOT TECHNOLOGY GROUP

DATE:	MODEL:	7673A	CVSD
DRAWN BY:	JOHN LING	TITLE:	
CHECKED BY:	PIN LAC JOE JOE	BASE UNIT	40CH 900MHz
APPROVED BY:		CORDESSPHONE	
PCB NO.:		DRAWING NO.:	
PAGE		REV.:	0A

1 2 3 4 5 6



GOT TECHNOLOGY GROUP

MODEL: 7673A CVSD

TITLE:

Base EIU detect

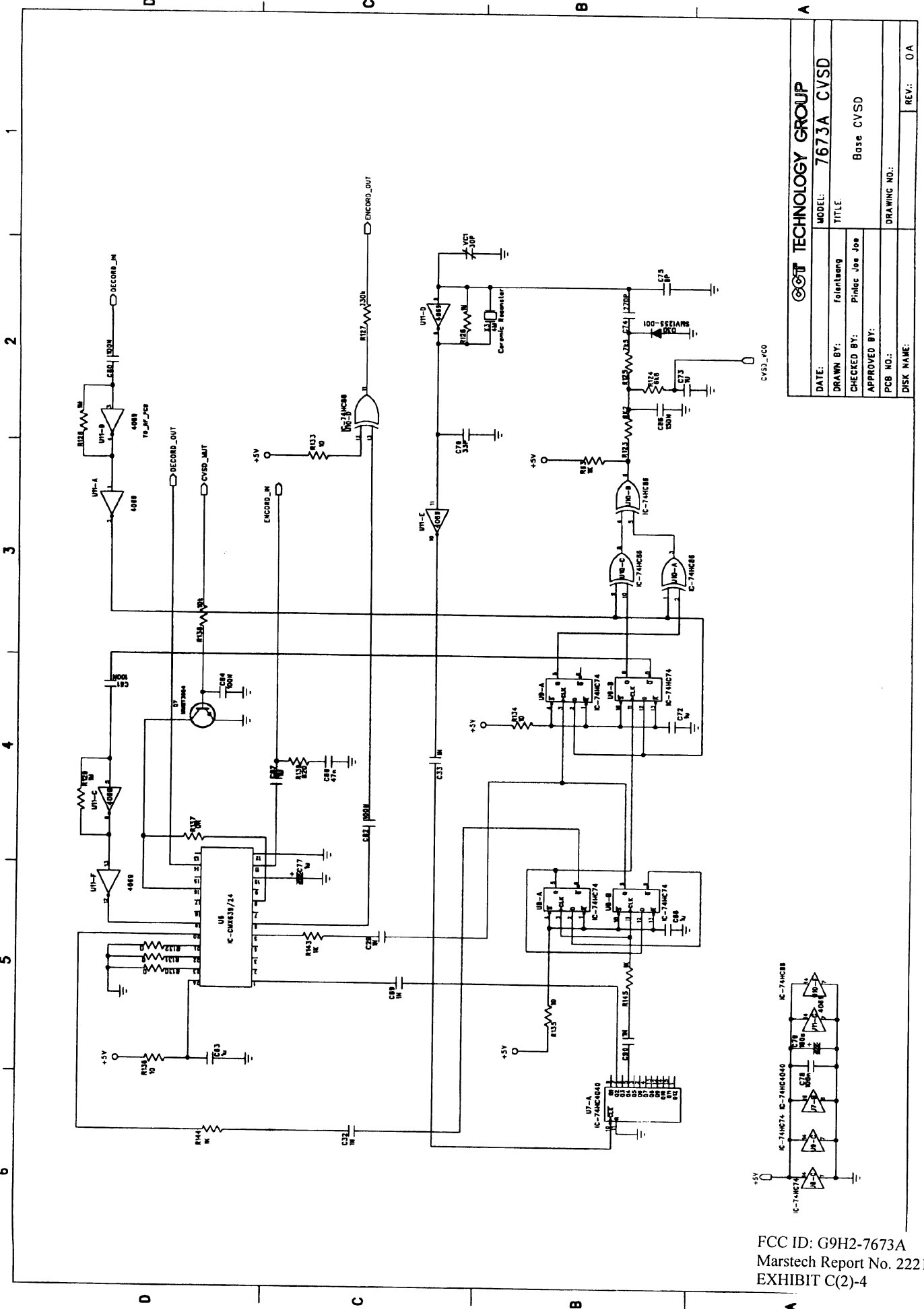
CHECKED BY: Pines Joe Joe

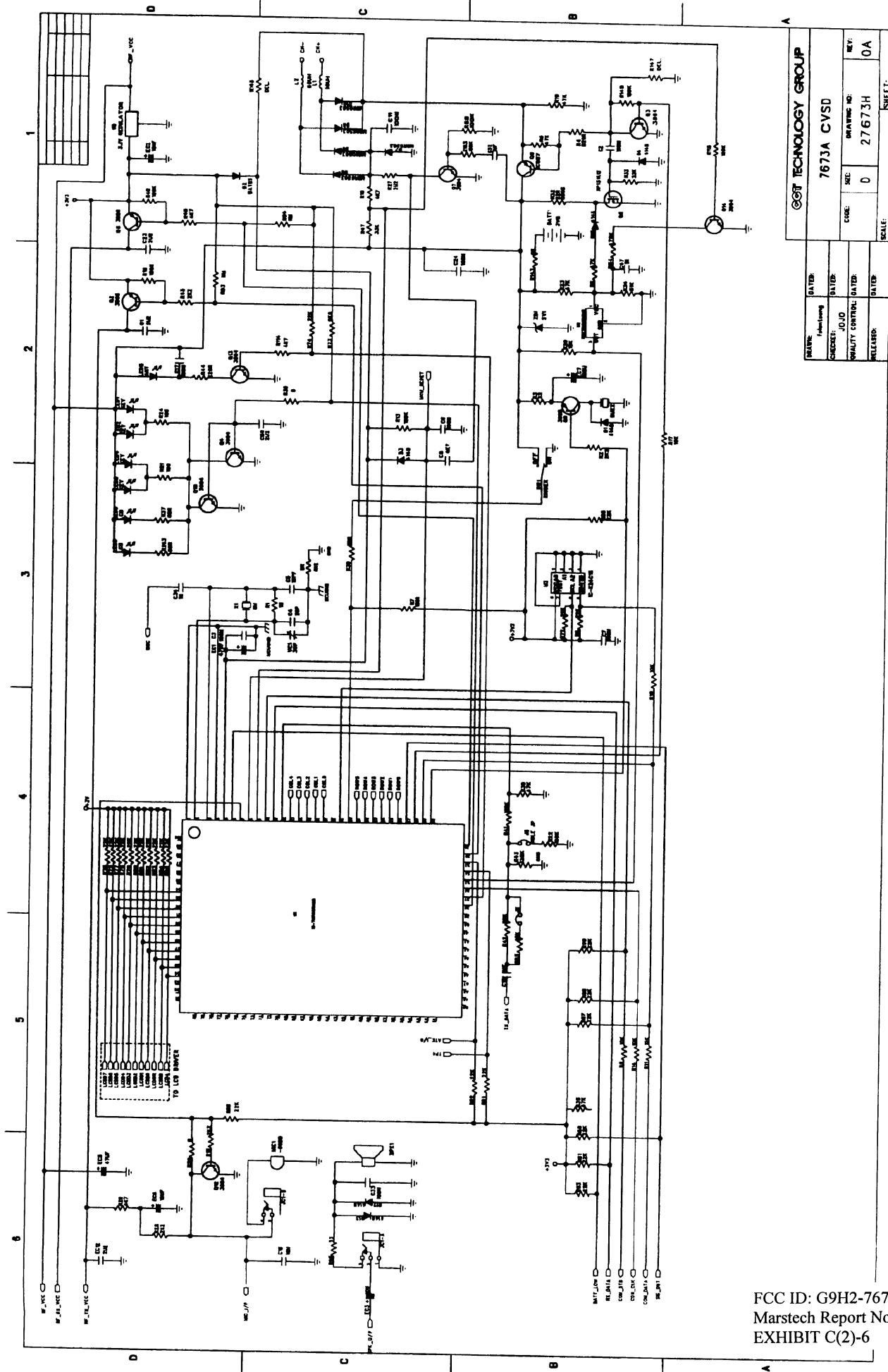
APPROVED BY:

PCB NO.:

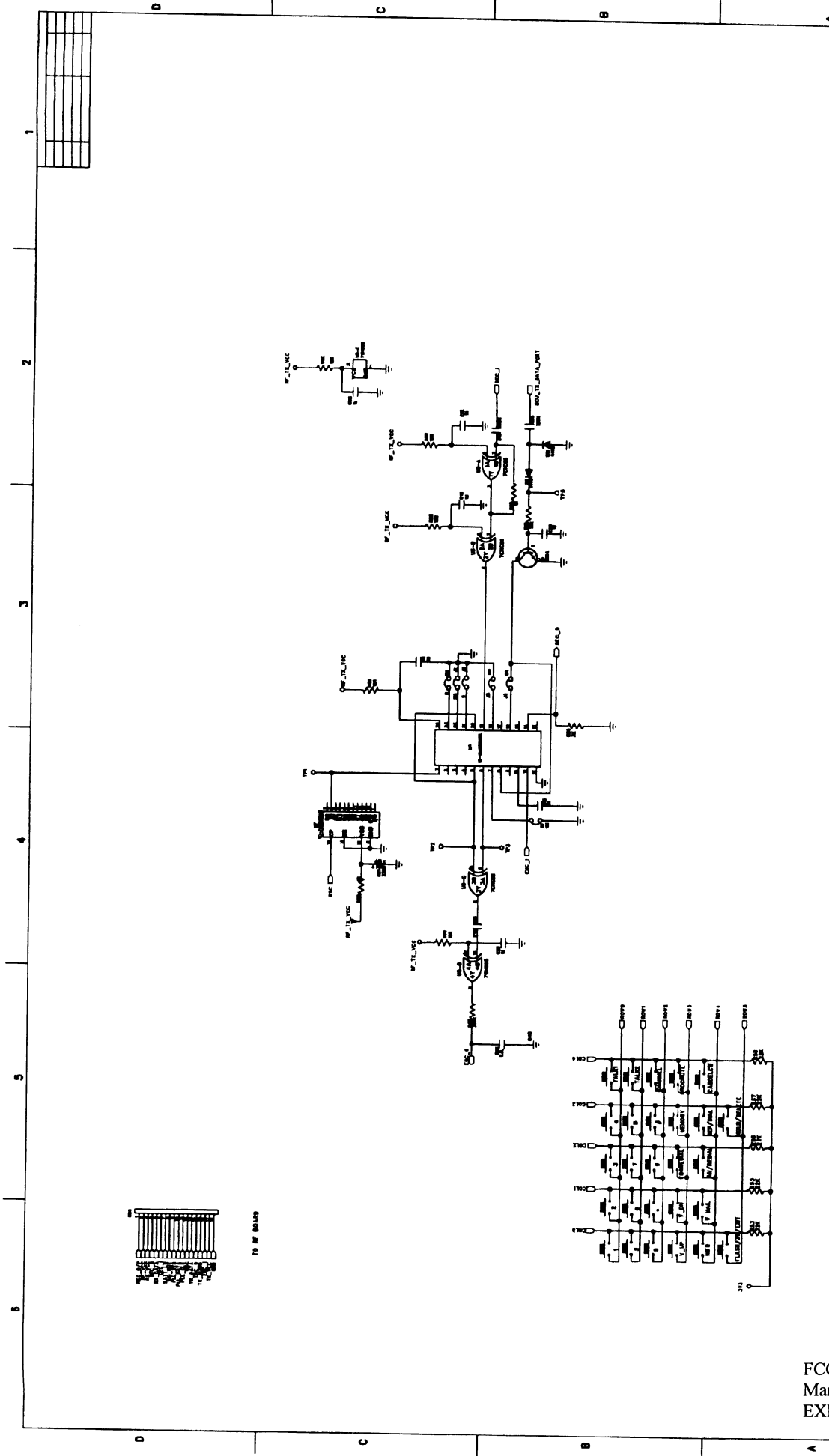
DRAWING NO.:

REV.: 0A





GOT TECHNOLOGY GROUP			
DATE:	FUNCTION:	DATE:	DATE:
CHECKED:	JOJO	CHECKED:	DATE:
QUALITY CONTROL:	DATE:	QUALITY CONTROL:	DATE:
RELEASED:	DATE:	RELEASED:	DATE:
7673A CVSD		DRAWING NO. 27673H	
REV. 0A		SHEET: 1	



GPT TECHNOLOGY GROUP			
DATE:	DATE:	TITLE:	7673A CVSD
DESIGNED BY:	DATE:	CODE:	SIZE: D
CHECKED BY:	DATE:	QUALITY CONTROL:	DATE:
RELEASED:	DATE:	SCALE:	REV: 0A
			SHEET:

