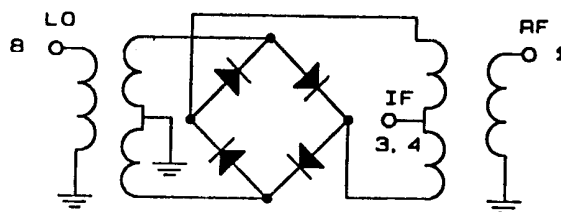




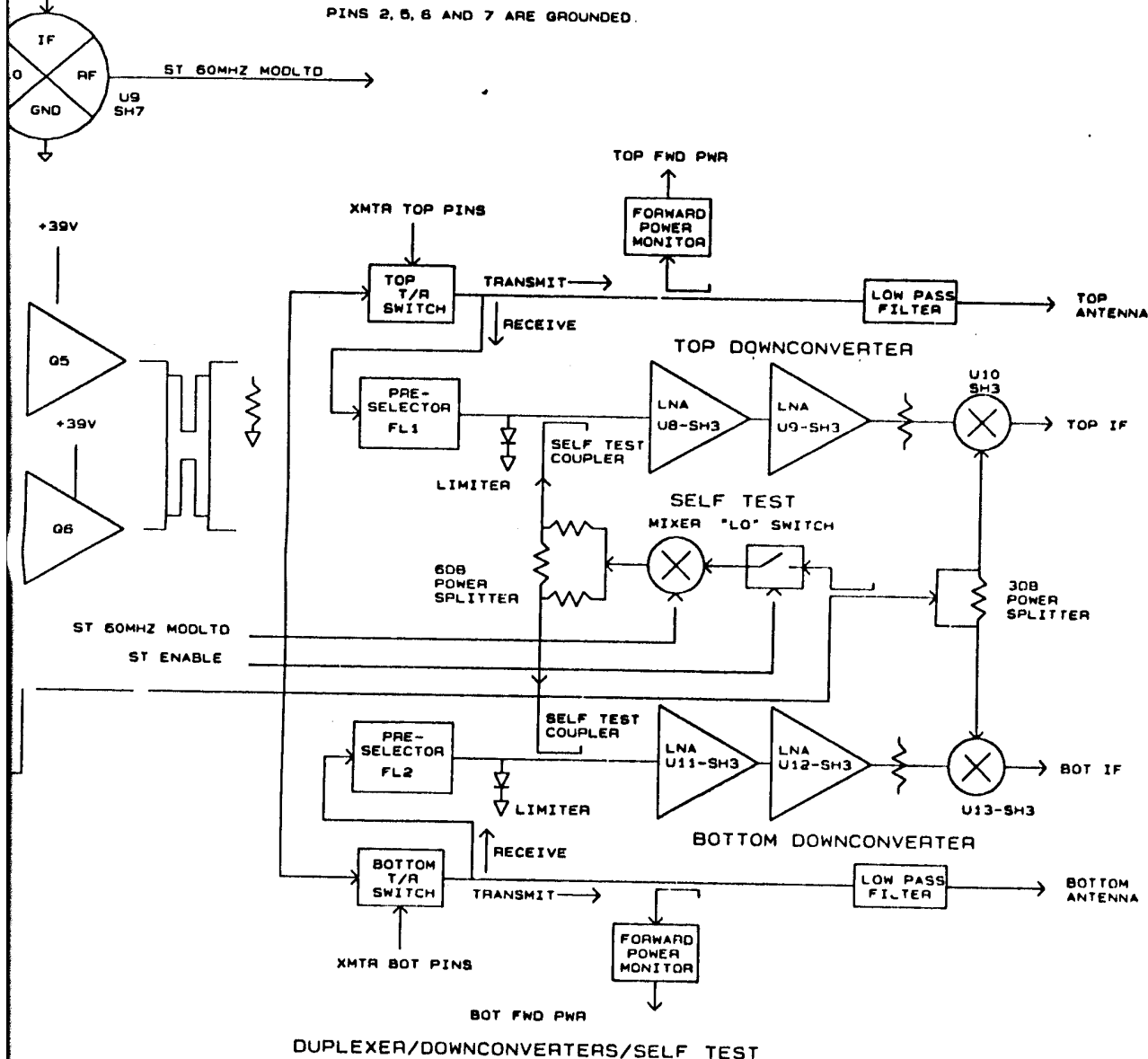
Blank Page

Don't Forget To Delete Me!



SSM-2-01 MIXER

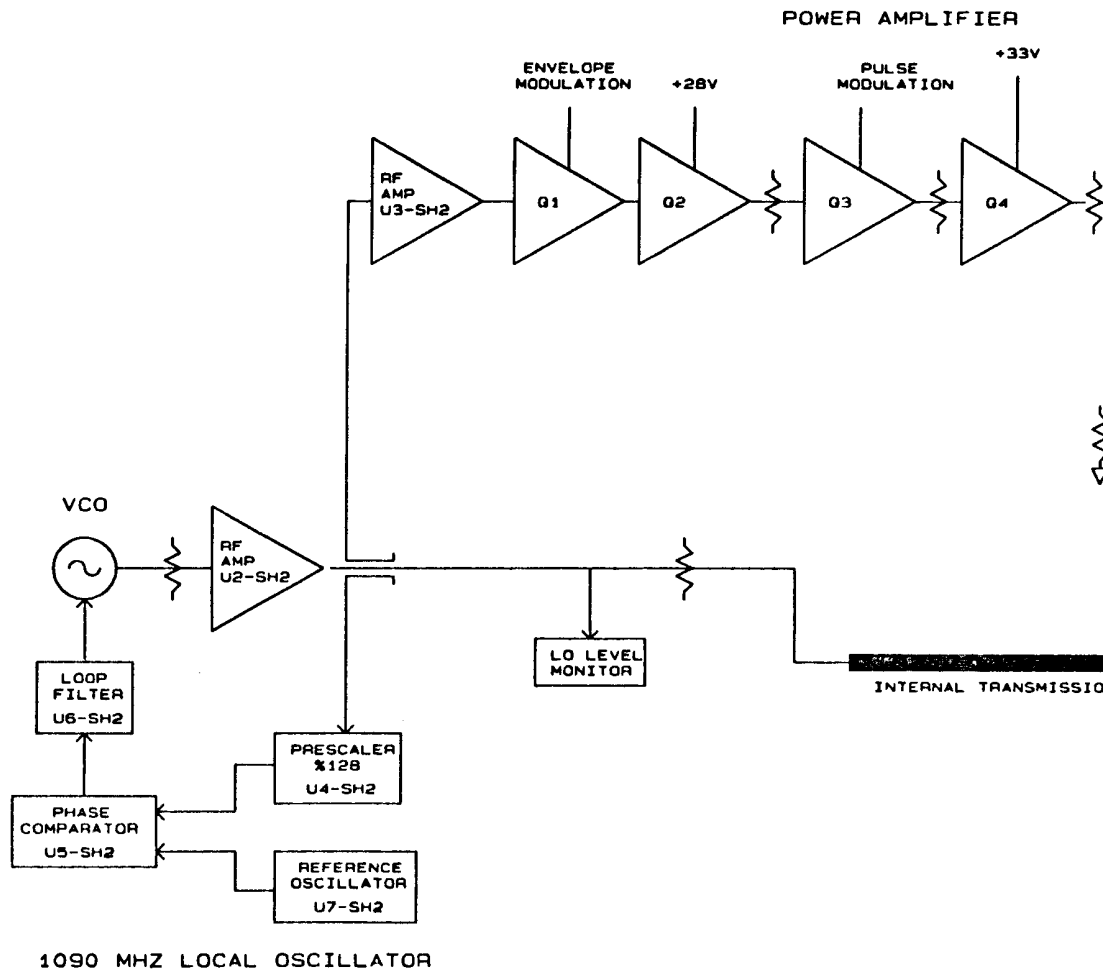
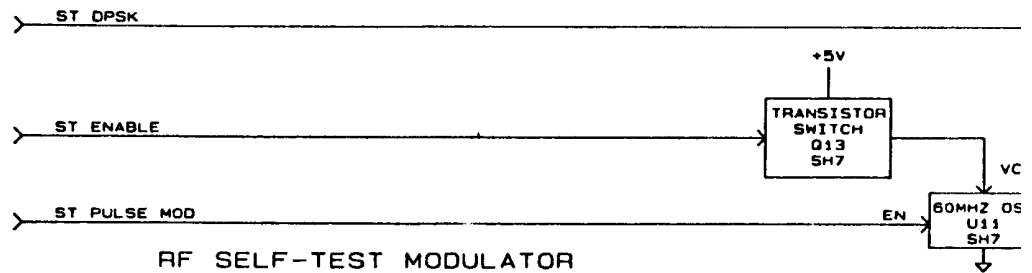
PINS 2, 5, 6 AND 7 ARE GROUNDED.



BLOCK DIAGRAM

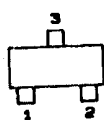
SCHEMATIC DIAGRAM SHEET NO. 13

COMPUTER GENERATED

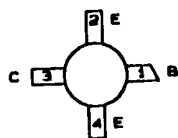


REDUCED SIZE
DO NOT SCALE

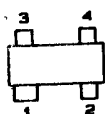
COMMON LABELS ON
TOP OF SMT DEVICES
ON TRANSMITTER/RECEIVER CCA



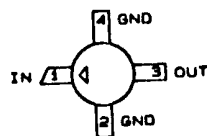
SOT23



AT00535

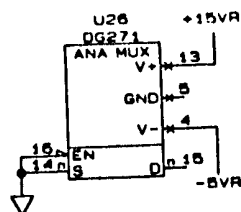
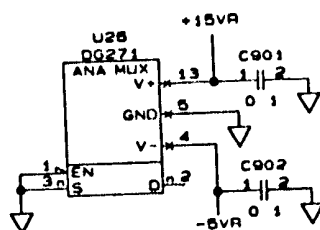
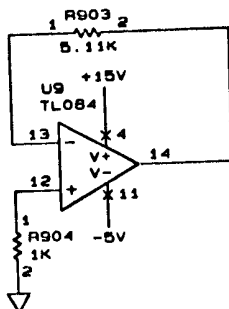
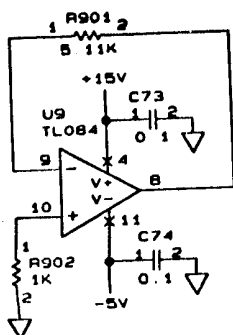


80T143

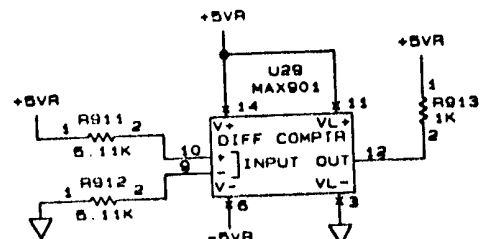
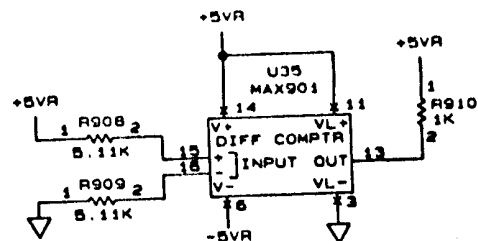
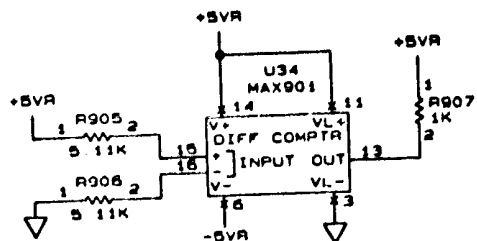


MSA0388
MSA0888

PART NAME	LABEL
BAS16	A6
BAR18	5F, D76
BSR14	U8
BSR16	T8
BBY31	S1
HSMP3800	D0
HSMP3820	F0
CGY50	G2
MSA0386	A03
MSA0686	A06
AT00535	NONE

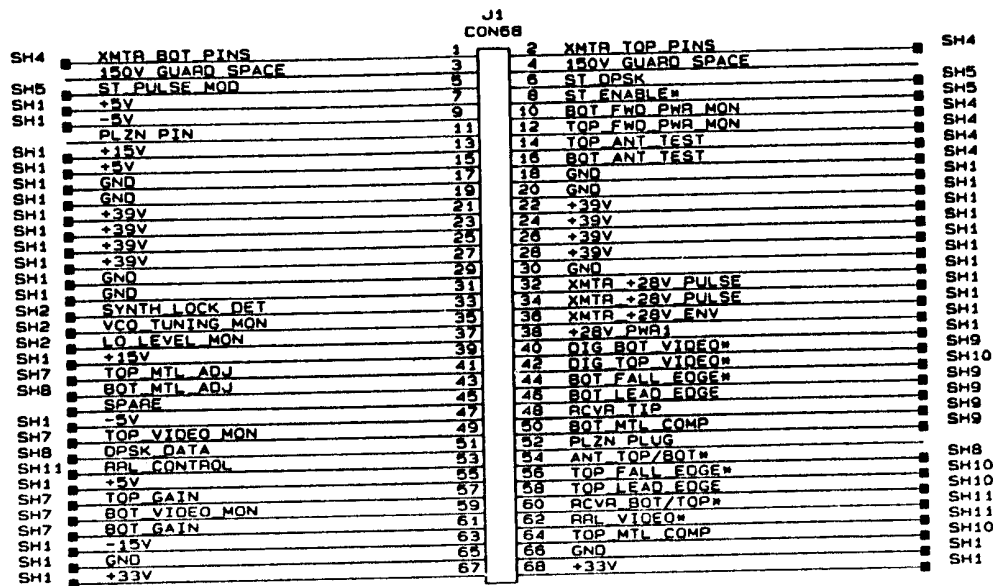


SPARES

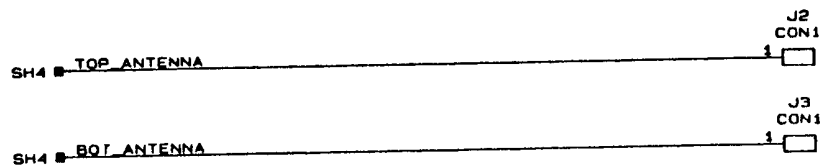


CONNECTOR PIN SUMMARY
AND SPARE COMPONENTS
CAD REFERENCE/REQUIREMENT
SCHEMATIC DIAGRAM SHEET NO. 12

COMPUTER GENERATED

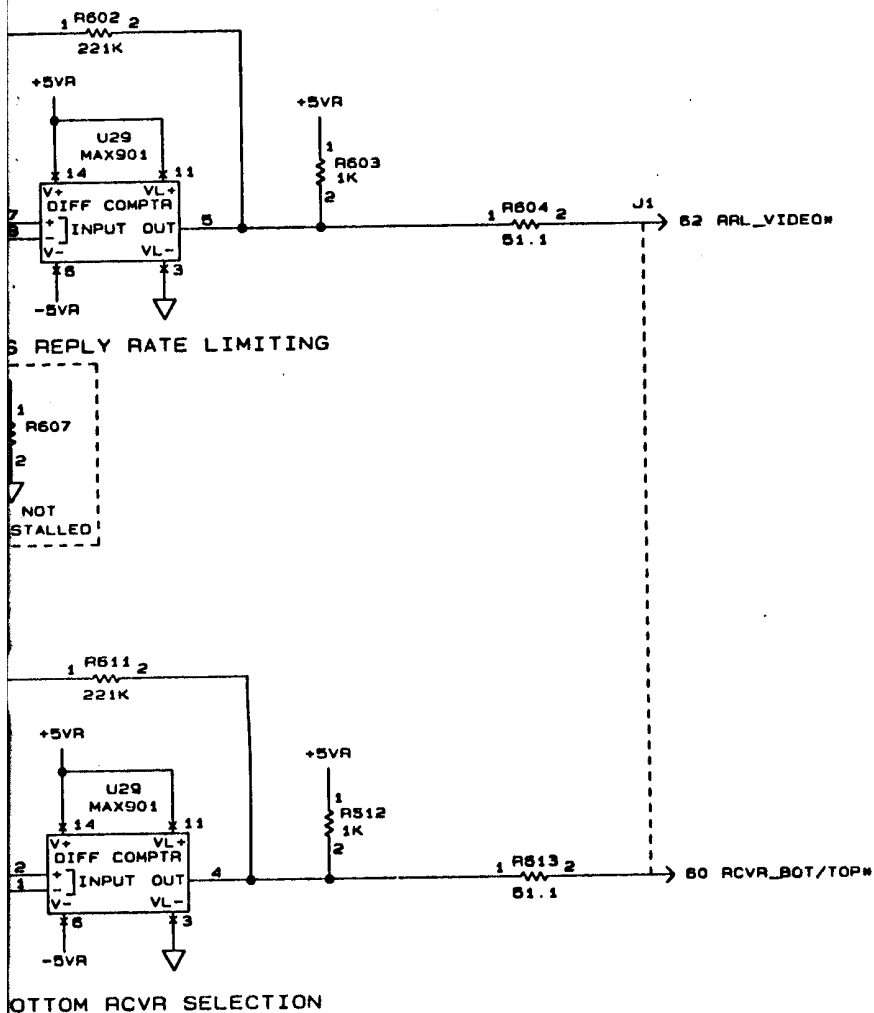


TRANSMITTER/RECEIVER TO POWER SUPPLY/MODULATOR INTERCONNECT



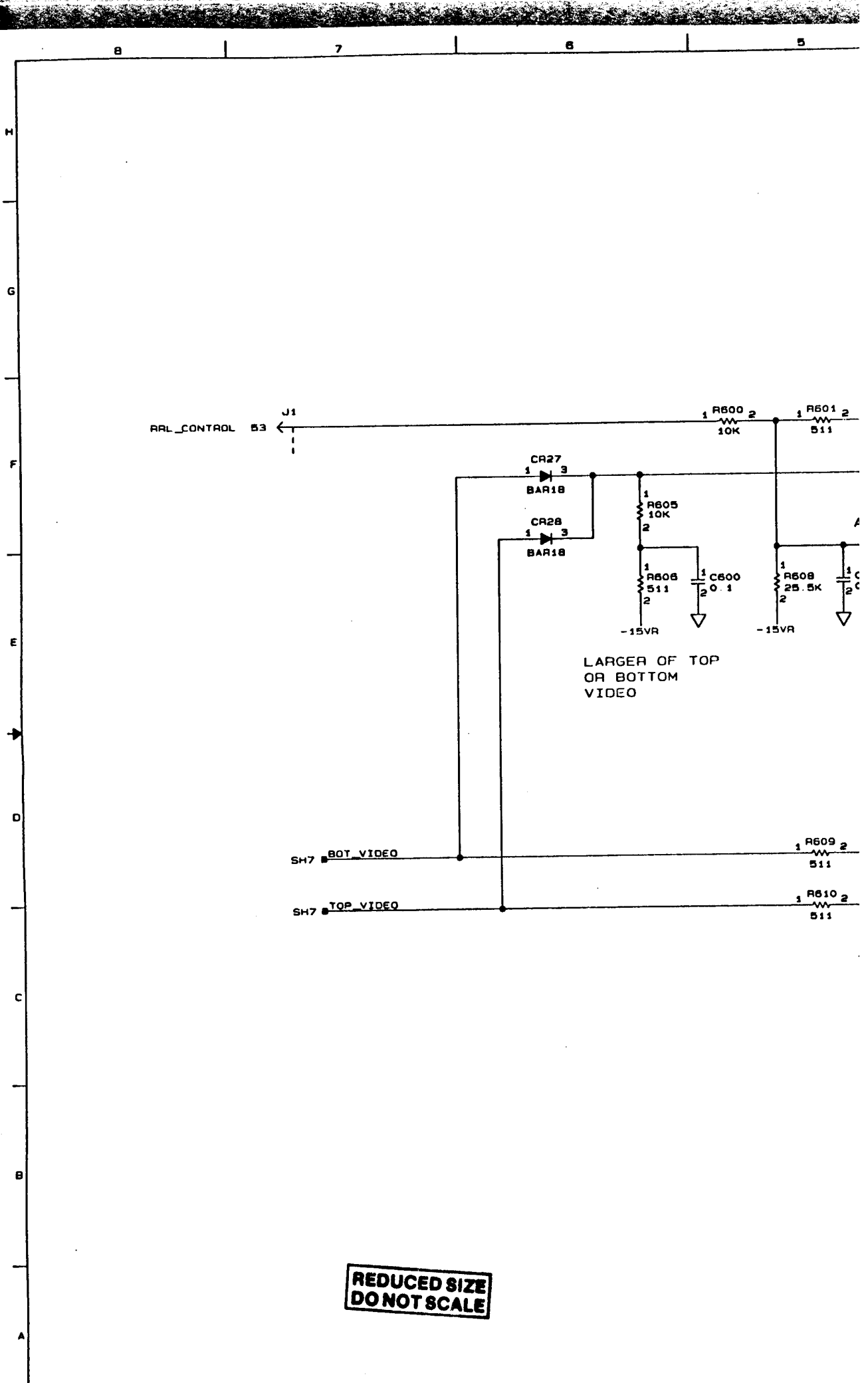
REDUCED SIZE
DO NOT SCALE

4		3		2		1	
						REVISIONS	
ZONE	REV	DESCRIPTION		DATE		APPROVED	



ATCRBS REPLY RATE LIMITING/TOP &
 BOT RCVR SELECTION
 SCHEMATIC DIAGRAM SHEET NO. 11

COMPUTER GENERATED



REDUCED SIZE
DO NOT SCALE

REVISIONS			
DATE	APPROVED	DESCRIPTION	REV

TCRBS
TCRBS/

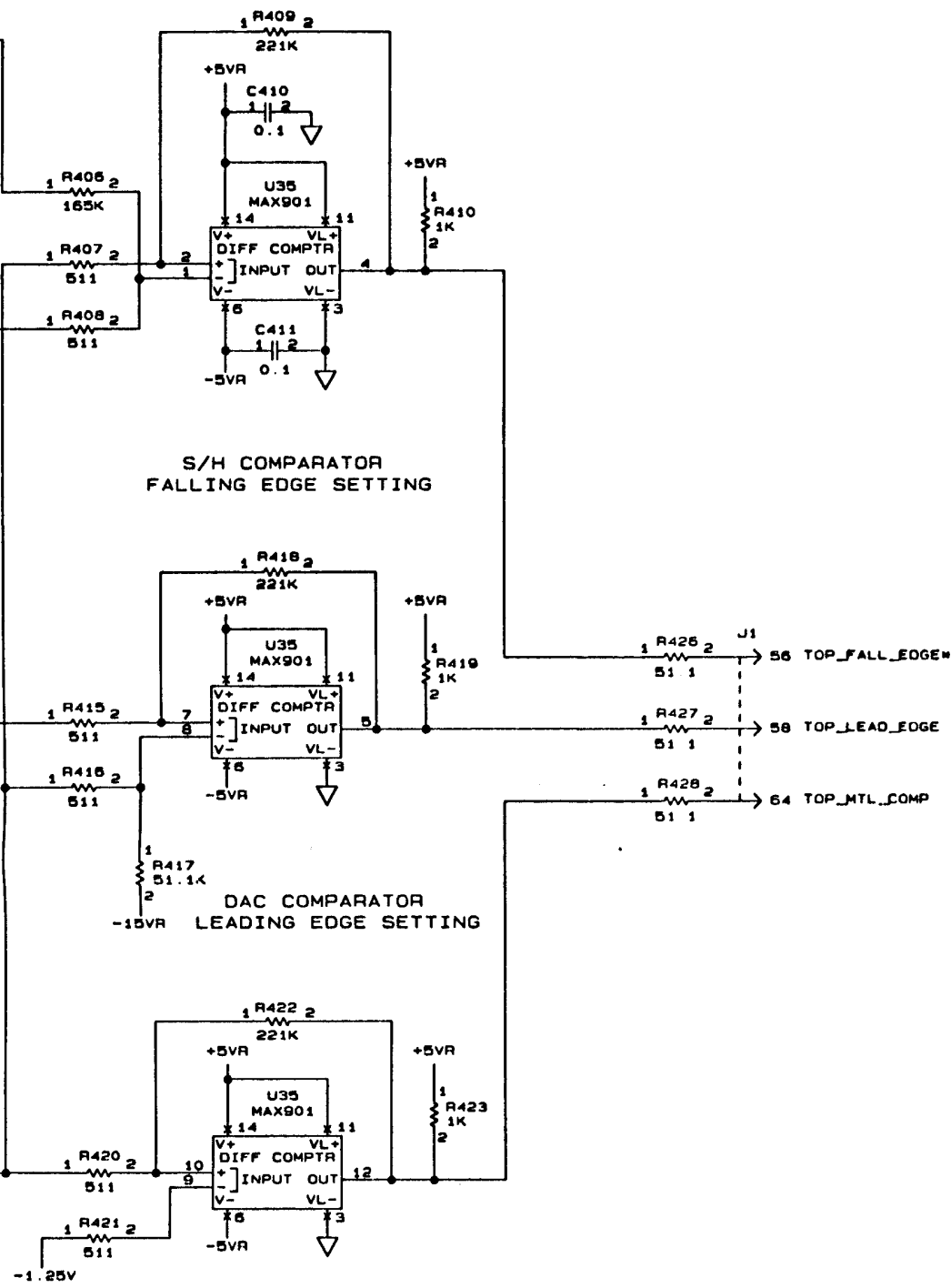
NOT
INSTALLED

415
000PF

S/H COMPARATOR
FALLING EDGE SETTING

DAC COMPARATOR
LEADING EDGE SETTING

MTL COMPARATOR



TOP VIDEO PROCESSOR

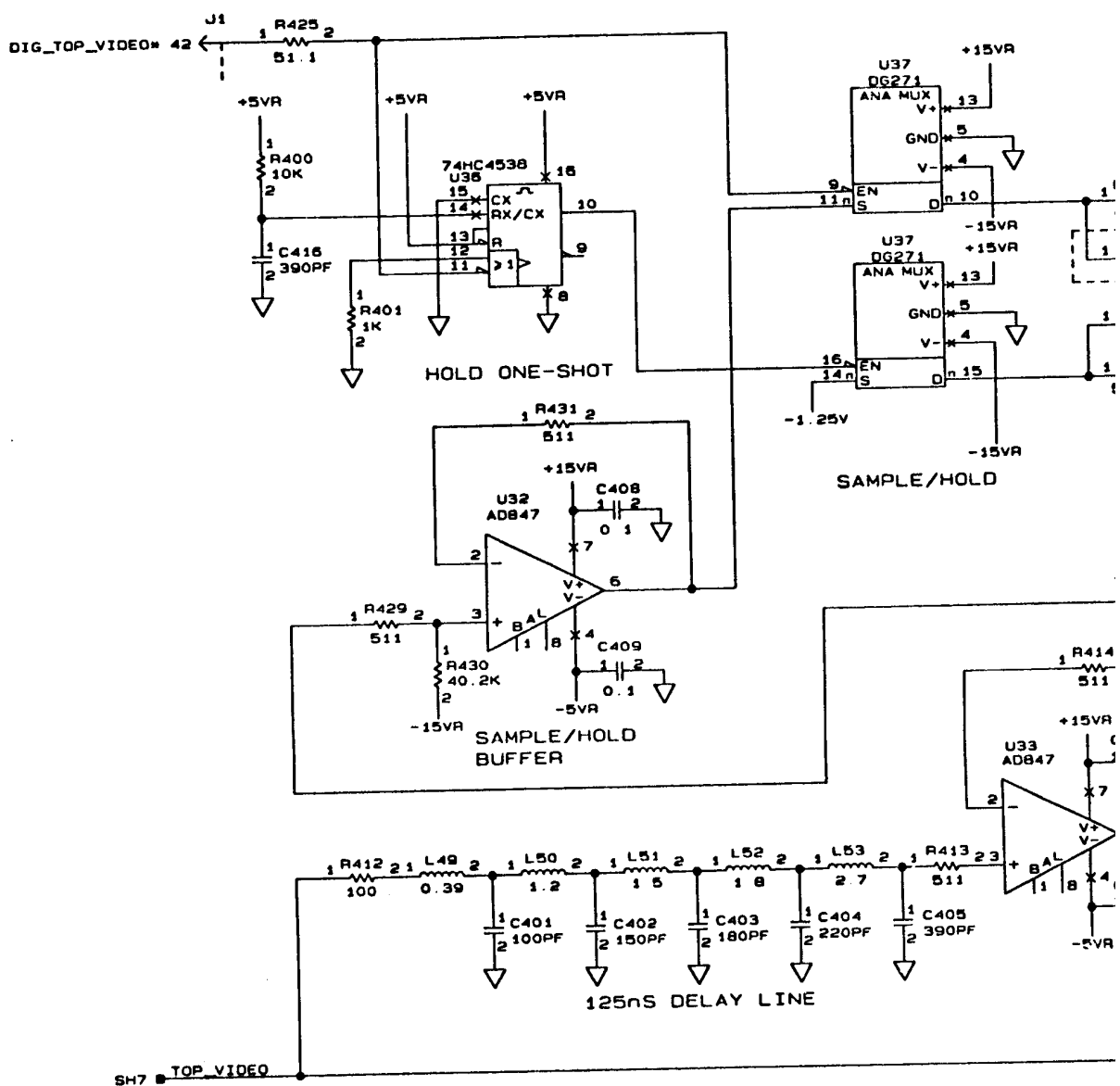
SCHEMATIC DIAGRAM SHEET NO. 10

COMPUTER GENERATED

H
G
F
E
D
C
B
A

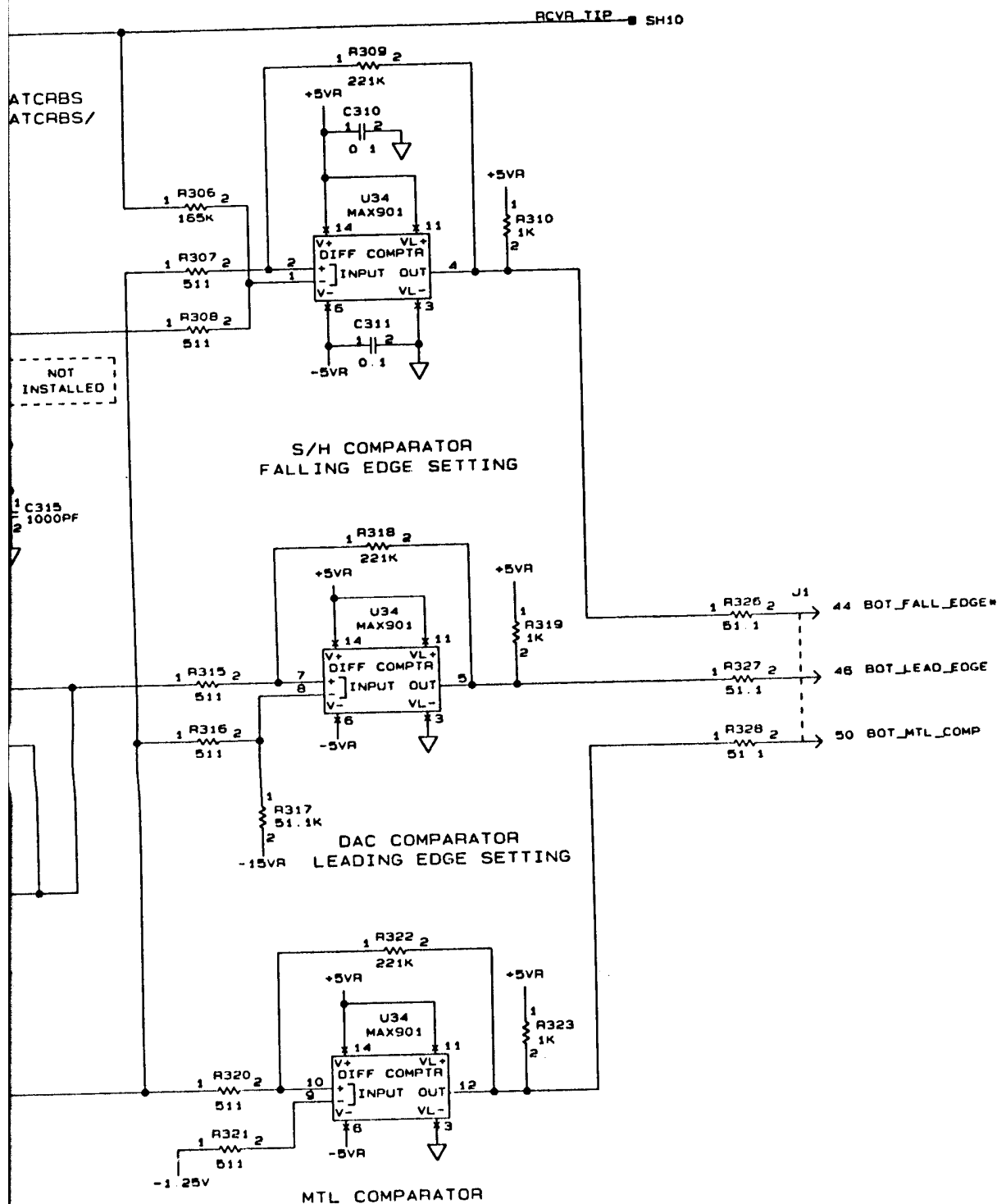
SH9 RCVR_TIP

P1 TO P2 COMPAR
P3 TO P4 COMPAR
MODE S ALLCALL



TOP VIDEO PROCESSOR

REDUCED SIZE
DO NOT SCALE

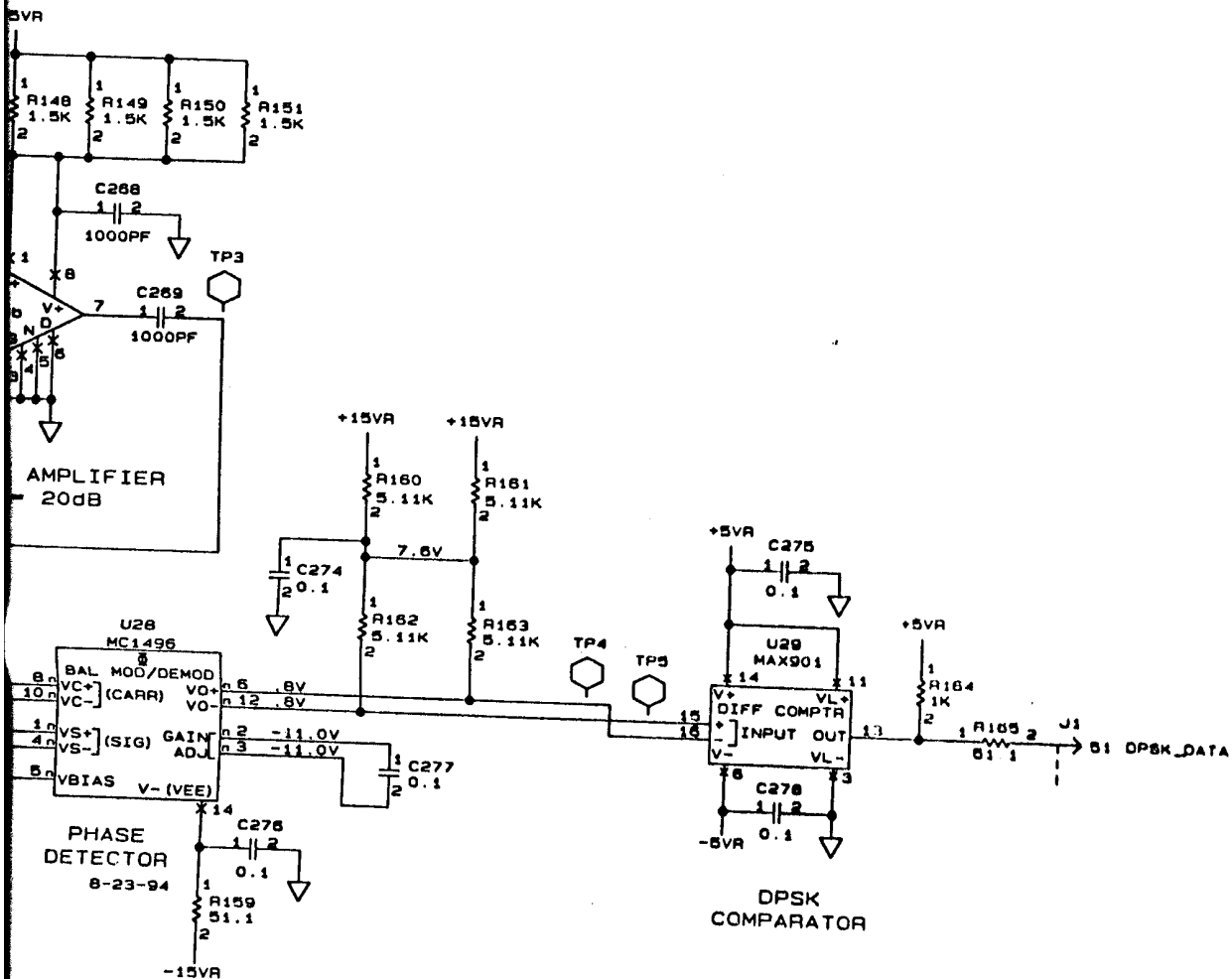


BOT VIDEO PROCESSOR

SCHEMATIC DIAGRAM SHEET NO. 9

COMPUTER GENERATED

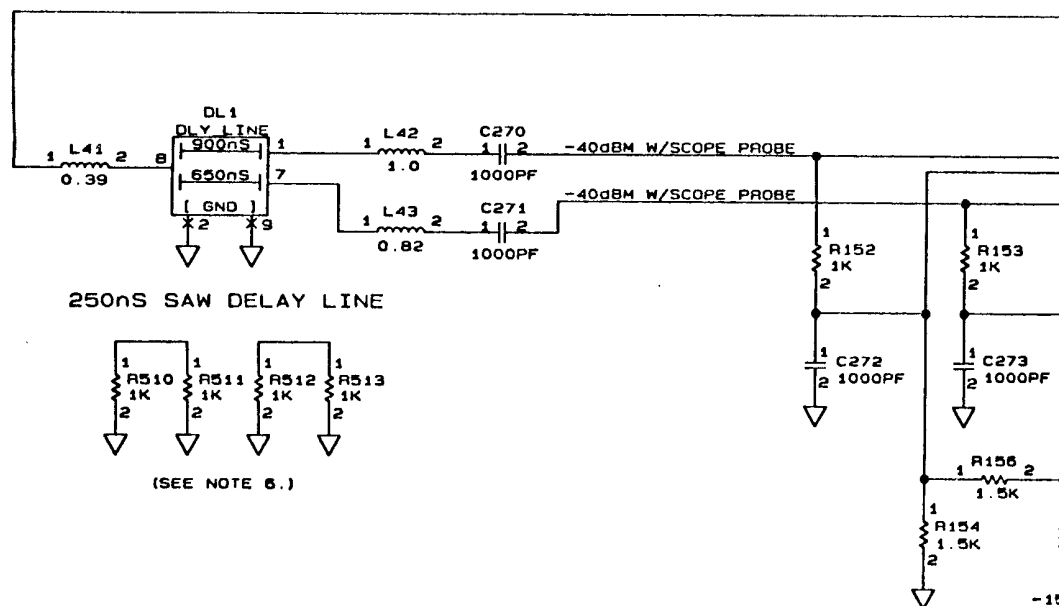
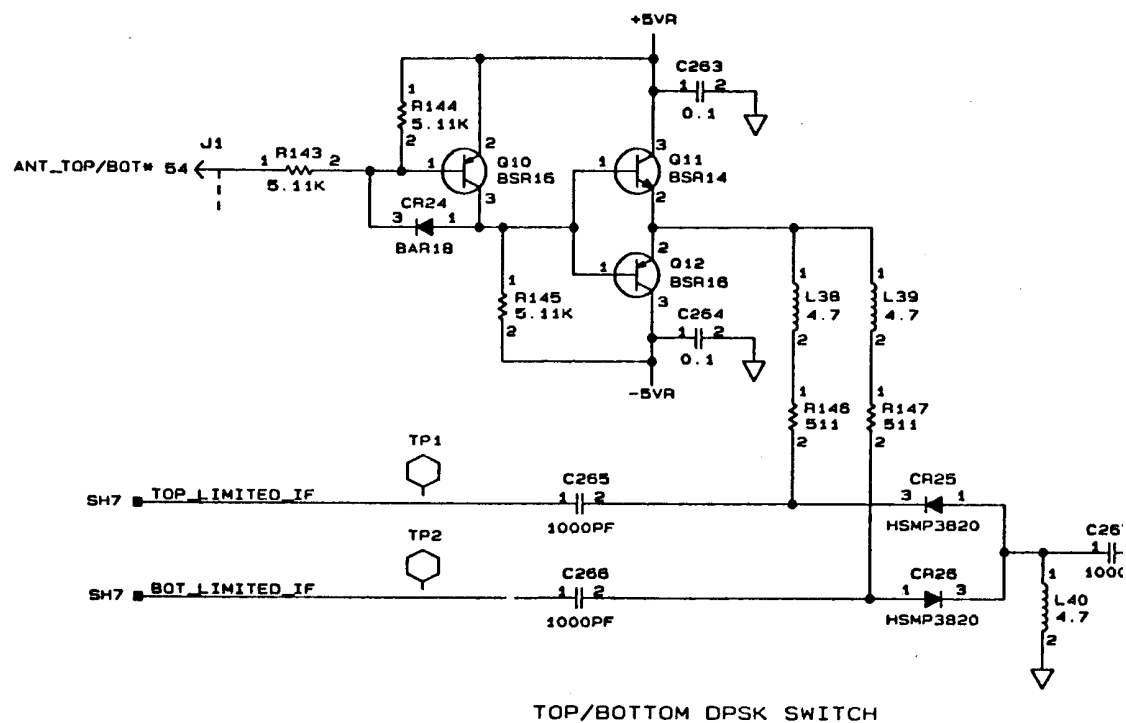
REVISED		DATE	APP
TIME	REV	DESCRIPTION	
D7	A	UOTO SCH: REF DES L42 VALUE WAS 0.82. 95935 (M) SEE DWG SHEET 1	5/8/97 C.L. R.R. M



MODULATOR

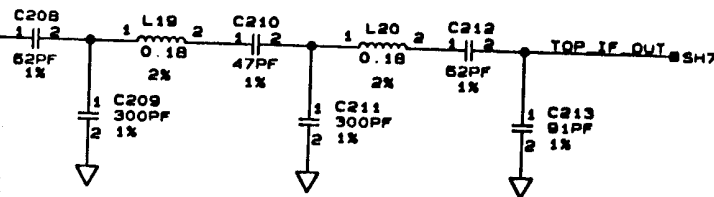
TOP AND BOT
DPSK SWITCH/DPSK DEMODULATOR
SCHEMATIC DIAGRAM SHEET NO. 8

COMPUTER GENERATED

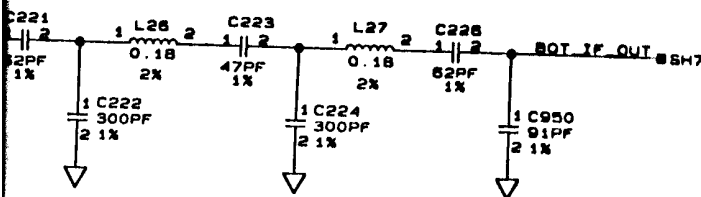


REDUCED SIZE
DO NOT SCALE





TOP 2ND STAGE IF FILTER
 = 60MHZ, BW = 8.0MHZ, LOSS = 1.5dB

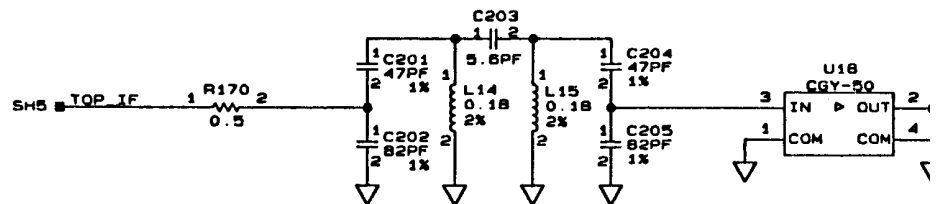


BOTTOM 2ND STAGE IF FILTER
 60MHZ, BW = 8.0MHZ, LOSS = 1.5dB

60MHZ

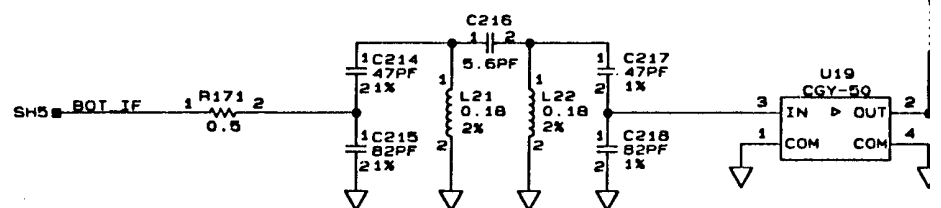
TOP AND BOT IF AMPLIFIER AND
 IF FILTER
 SCHEMATIC DIAGRAM SHEET NO. 6

COMPUTER GENERATED



TOP 1ST STAGE
IF FILTER

TOP IF AMPLIF:
GAIN = 12dB



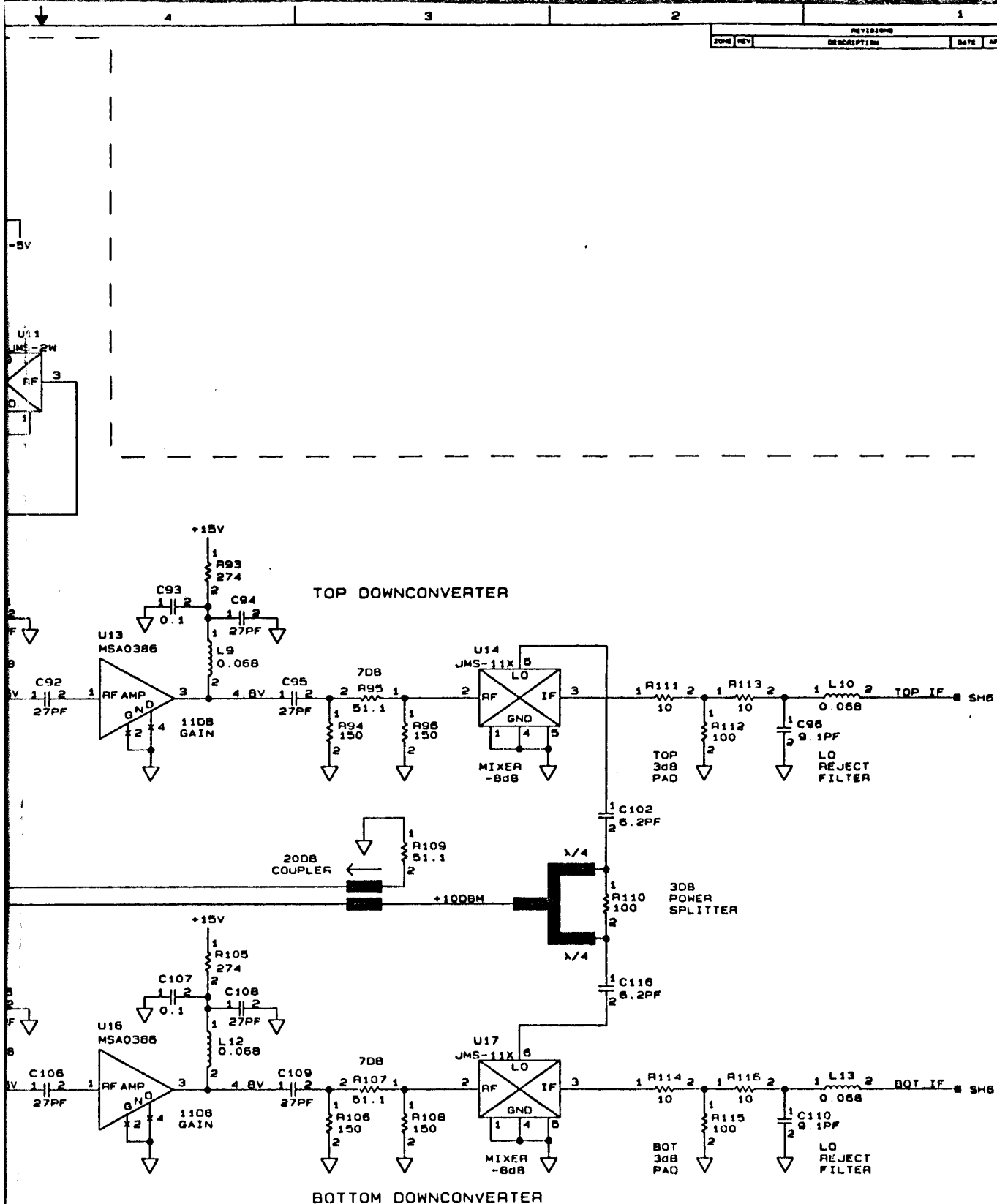
BOT 1ST STAGE
IF FILTER

BOTTOM IF AMPL
GAIN = 12dB

SHIELD

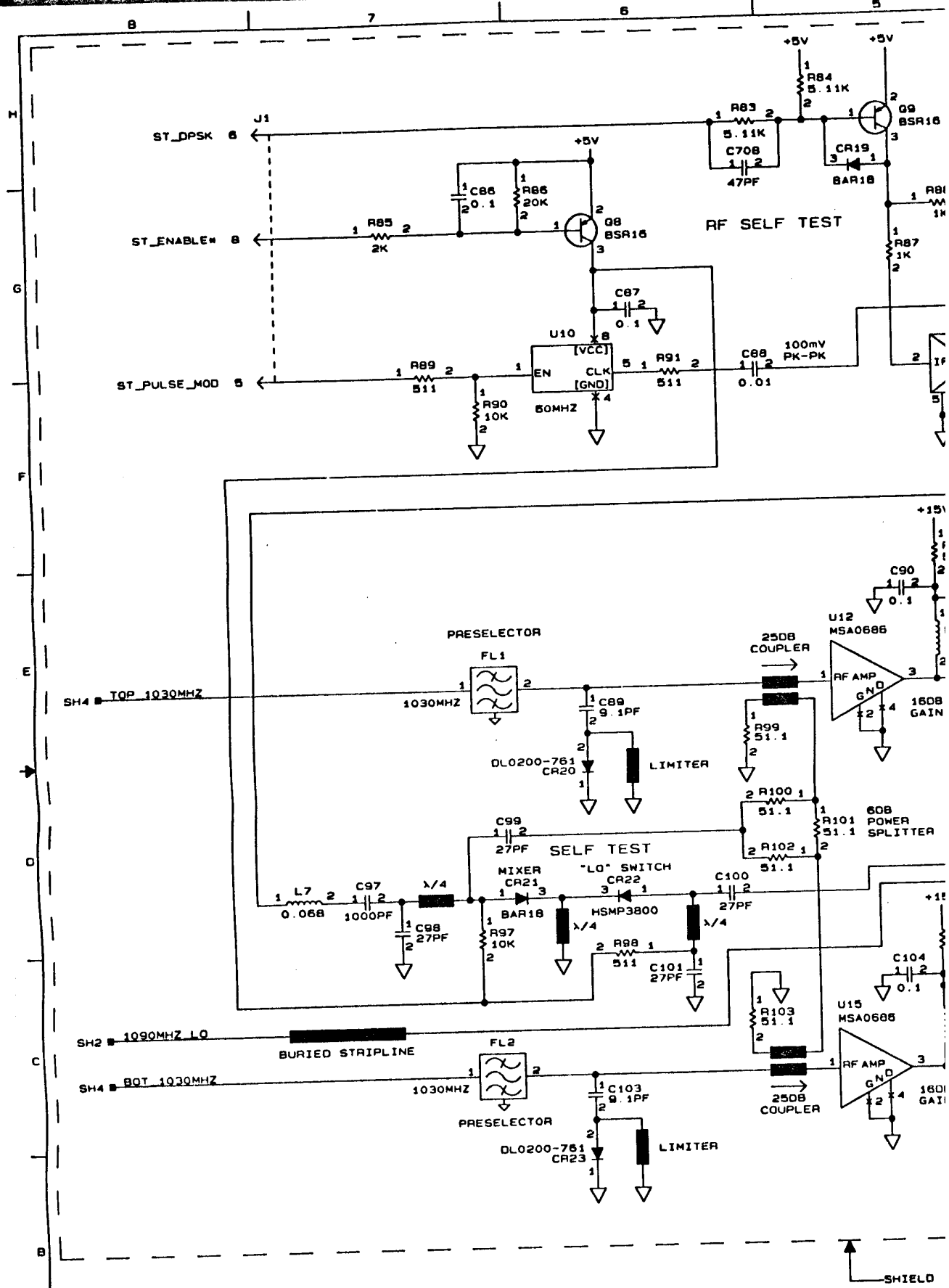
I.F. AMPLIFIER/

REDUCED SIZE
DO NOT SCALE

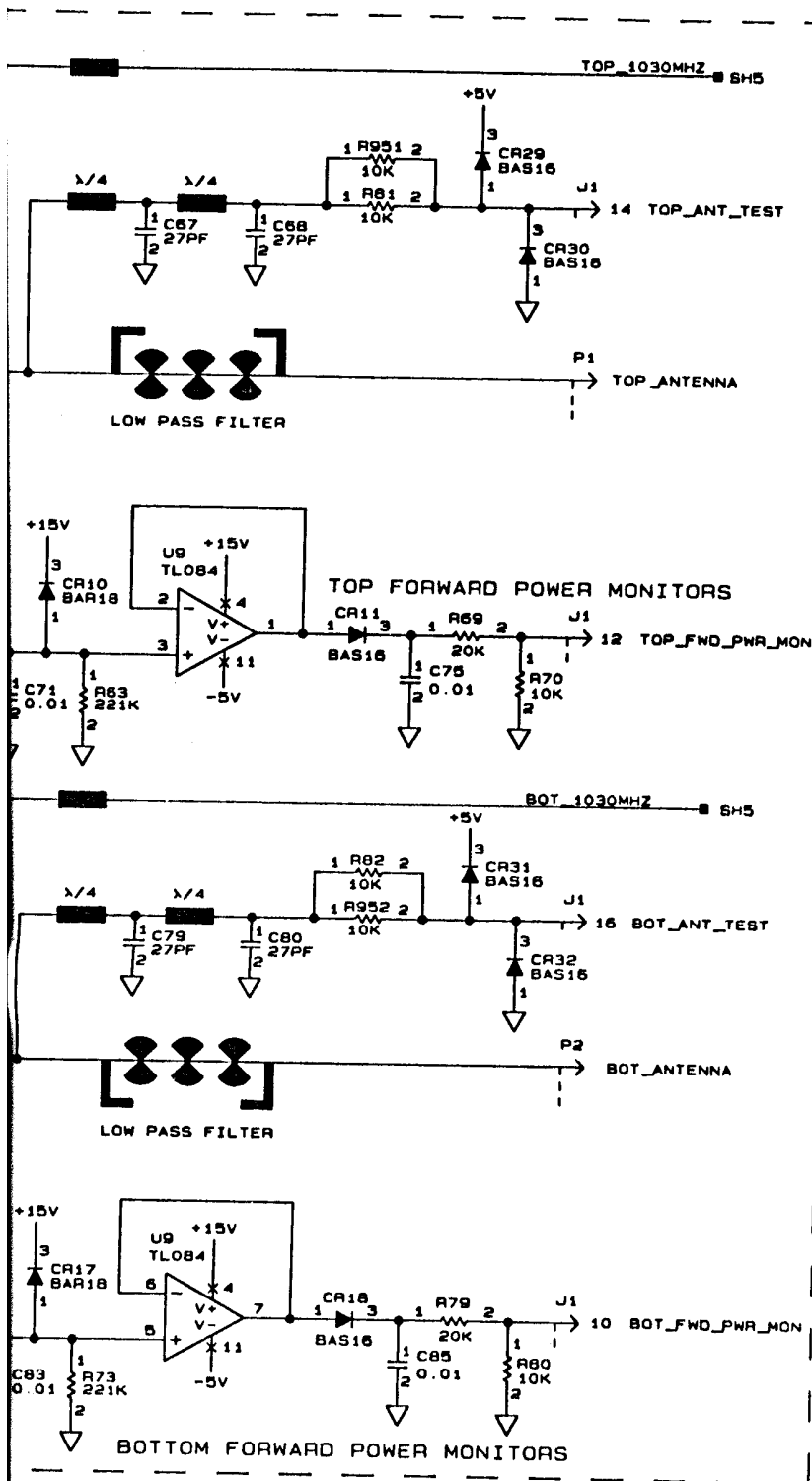


DUPLEXER/DOWNCONVERTERS/SELF-TEST
 SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED



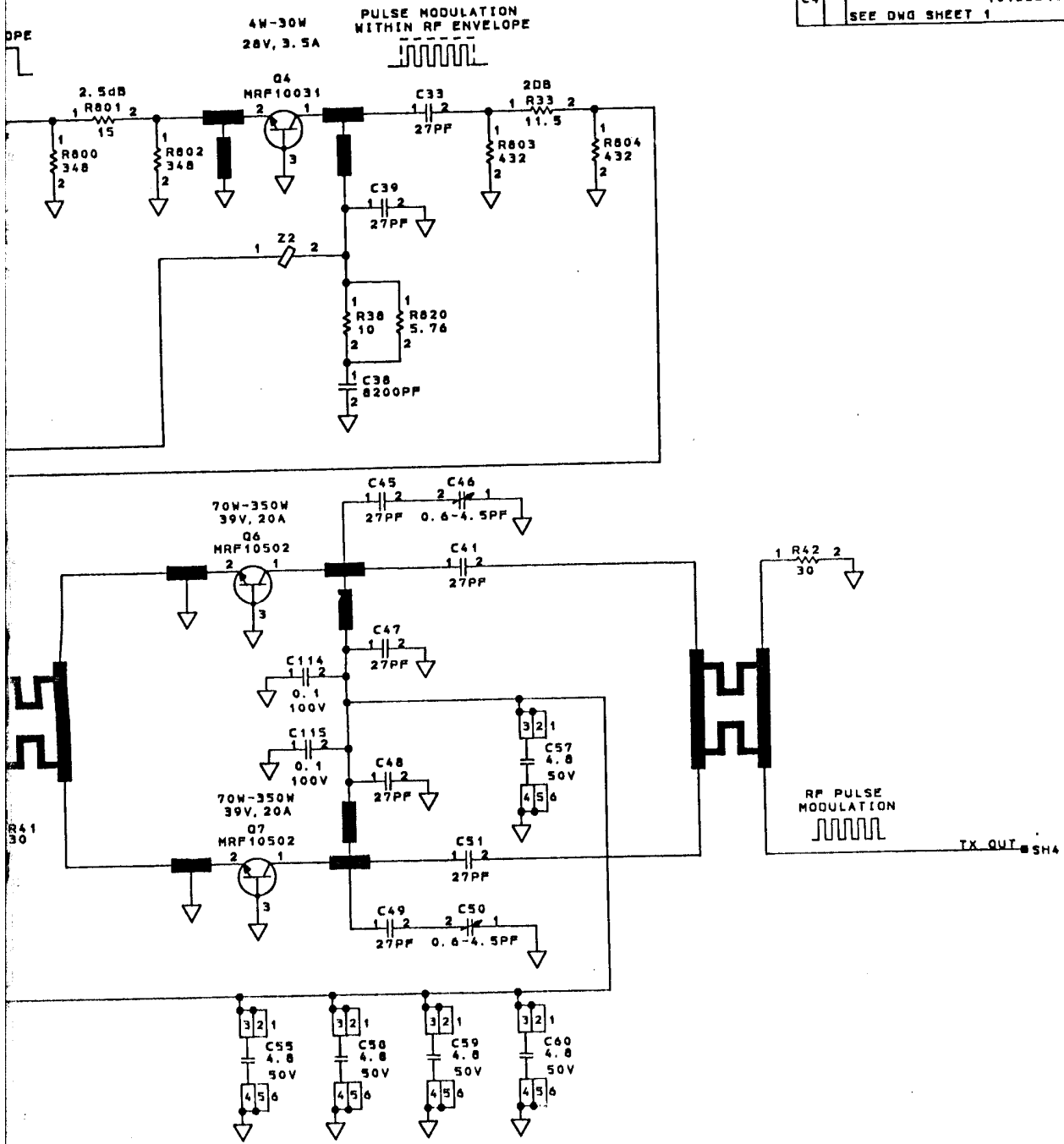
REDUCED SIZE
DO NOT SCALE



DUPLEXER/FORWARD POWER MONITORS
 ANTENNA MONITORS
 SCHEMATIC DIAGRAM SHEET NO. 4

COMPUTER GENERATED

REVISIONS		DATE	APPROVED
A	PARTS LIST DATA BASE CHANGE ONLY.	05 FEB 97	C. DOBODALL
	95935 (M)		R. RICHARD
B	UOTO SCH. REF DES R43 VALUE WAS 0.5. REF DES R800, R801 AND R802 VALUES WERE 866, 5.76 AND 866 RESP. TEXT AT R801 WAS "10B".	02 JUN 98	C. DOBODALL
	96251 (M)		R. RICHARD
C	UOTO SCH. CORRECTED PICTORIAL OF REF DES Q4 AND Q5. REF DES Q6 AND Q7 TYPE WAS MRF10501.	30 Sept 98	C. DOBODALL
	101852 (U)		R. RICHARD
SEE DWD SHEET 1			



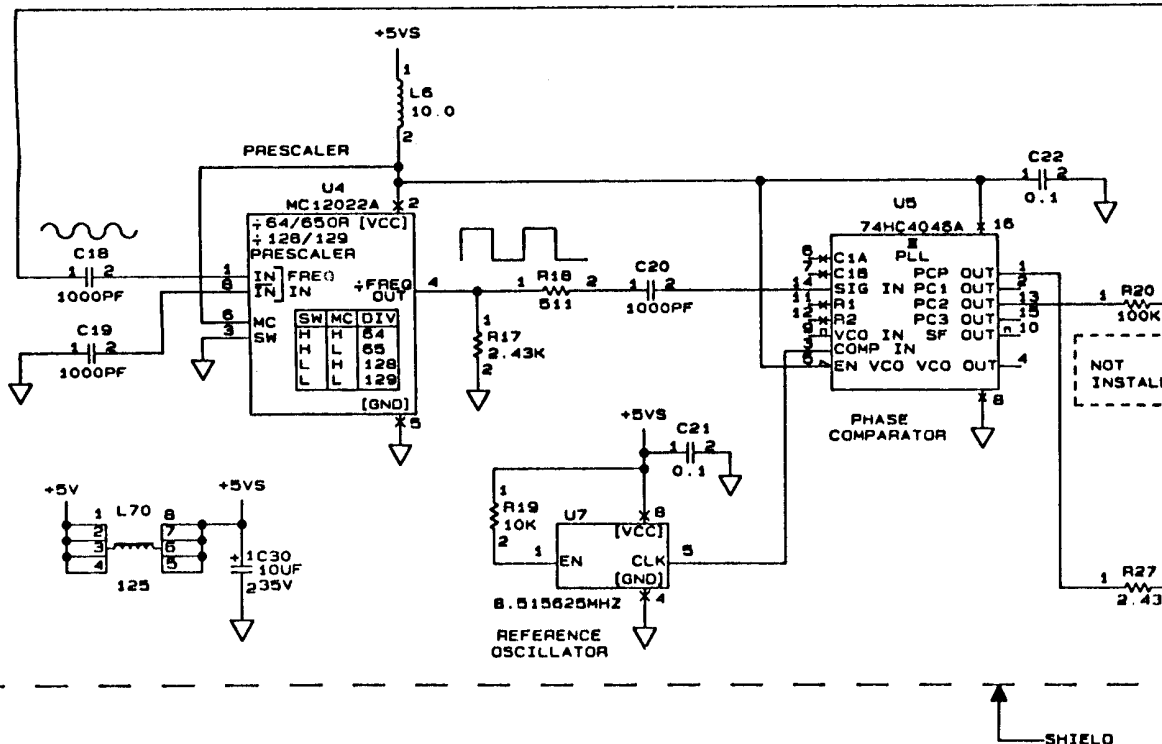
POWER AMPLIFIER

SCHEMATIC DIAGRAM SHEET NO. 3

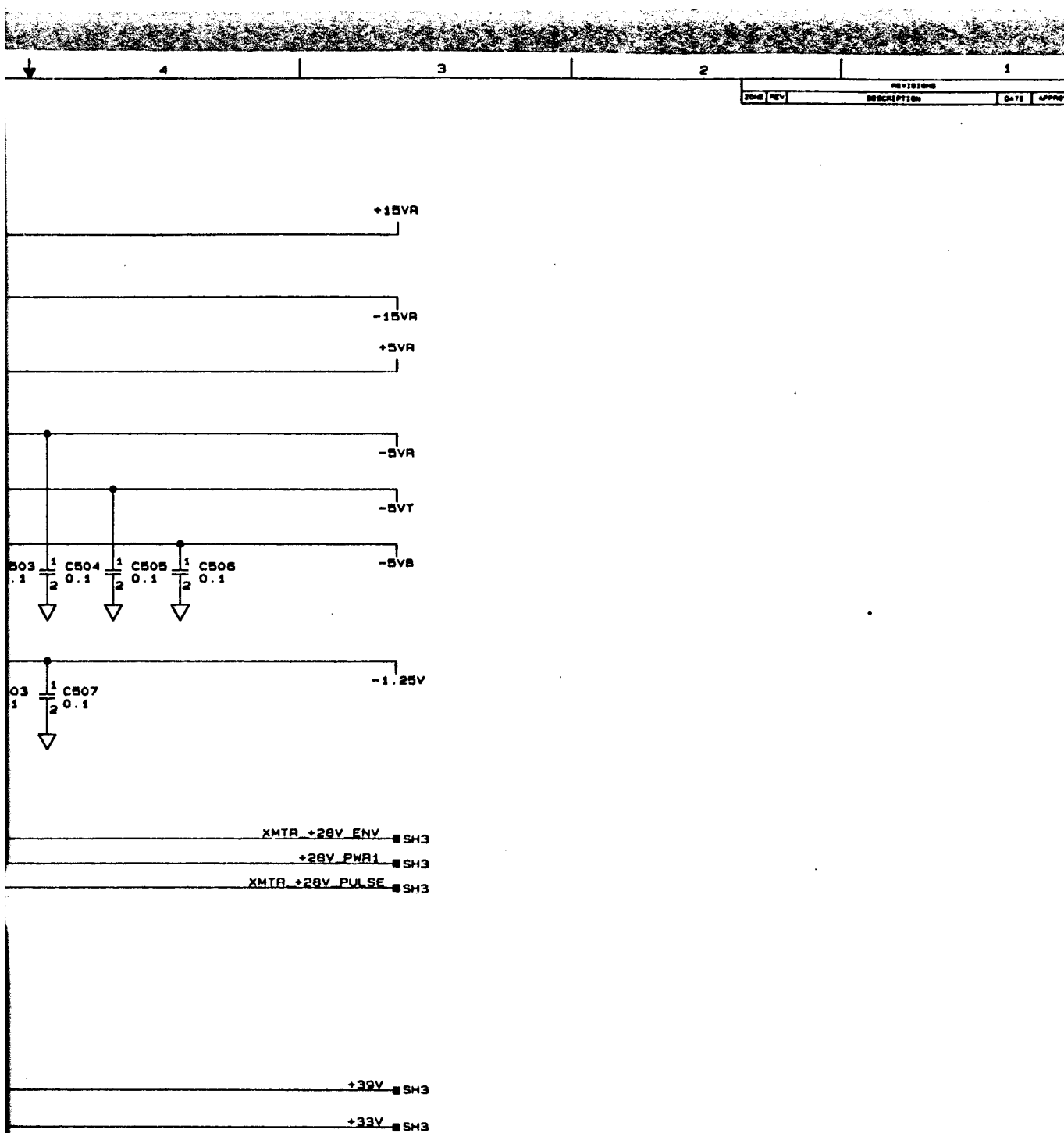
COMPUTER GENERATED

1090 MHZ LOCAL OSCILLATOR
SCHEMATIC DIAGRAM SHEET NO. 2

COMPUTER GENERATED



**REDUCED SIZE
DO NOT SCALE**

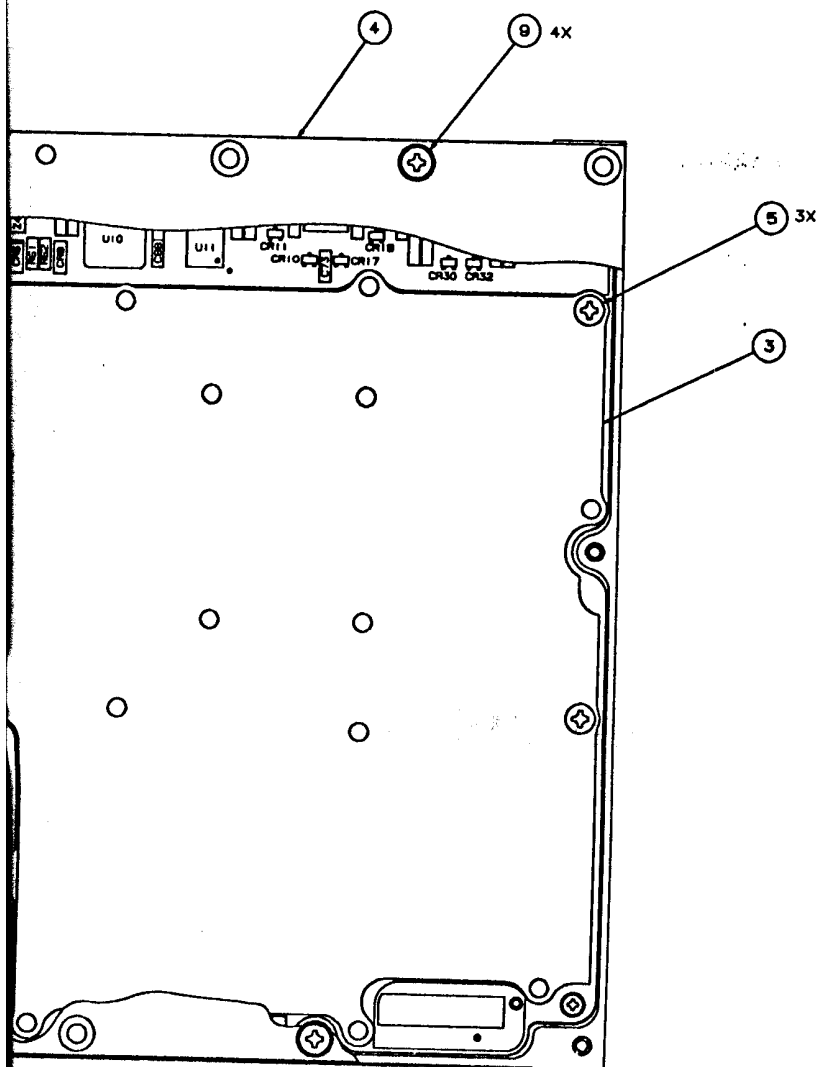


POWER LINE FILTERS

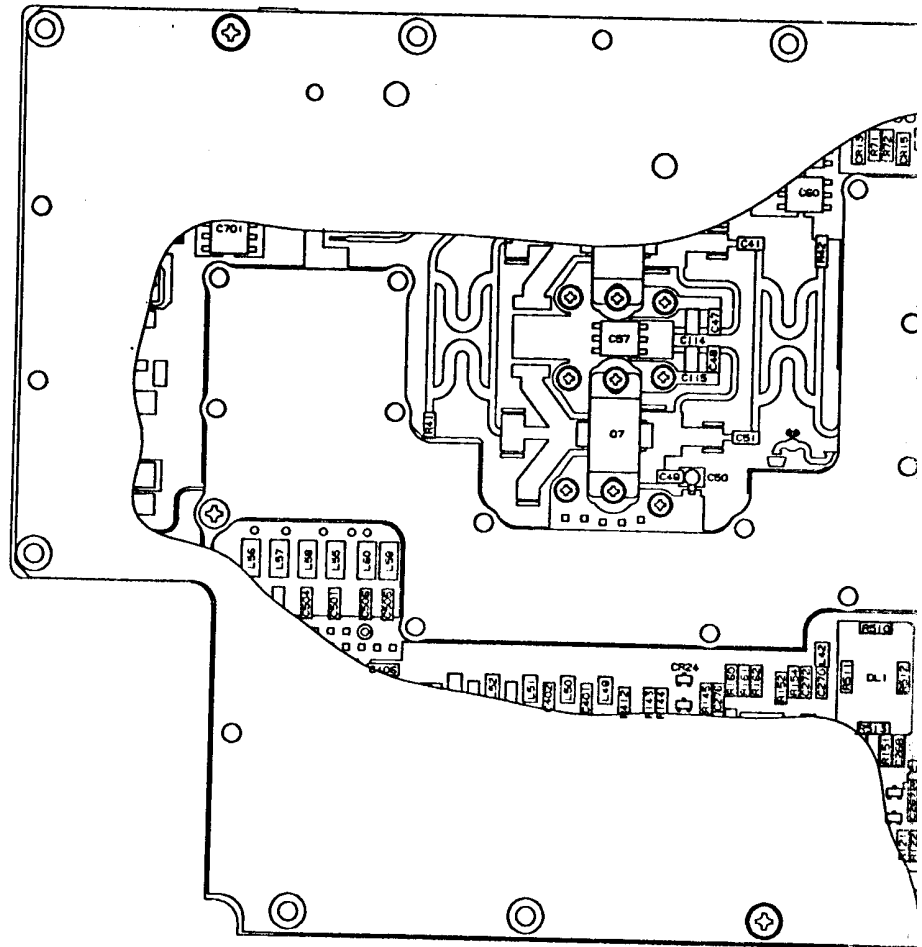
SCHEMATIC DIAGRAM SHEET NO. 1 OF 16

COMPUTER GENERATED

ZONE		REV		REVISIONS		DATE		APPROVED	



7517835-904 3



**REDUCED SIZE
DO NOT SCALE**

TIGHTENING SCREWS, MOVE PART AS CLOSE AS POSSIBLE AGAINST INDICATED EDGE OF BOARD CUT OUT.

TABS MUST BE SOLDERED UP TO EDGE OF PAD

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
A	1	LOTO DETAILS C AND M. FIND NOTE CALLOUT 21.	08030 (M)	C. OGDALL
D	1	LOTO VIEW. ADD DETAIL N AND NOTE 14 CALLOUT.	102062 (M)	R. RICHARDS

GENTLY BEND END OF TRANSISTOR LEAD DOWN AGAINST PAD. SOLDER THE END OF THE LEAD ONLY. DO NOT LET SOLDER FLOW INTO STRESS RELIEF AREA OF THE LEAD.

CUT .040 ± .010 FROM BODY

30° ± 10° SLANT CUT DENOTES BASE LEAD NOTE ORIENTATION

01
DETAIL D

SECTION P-P
SCALE: NONE

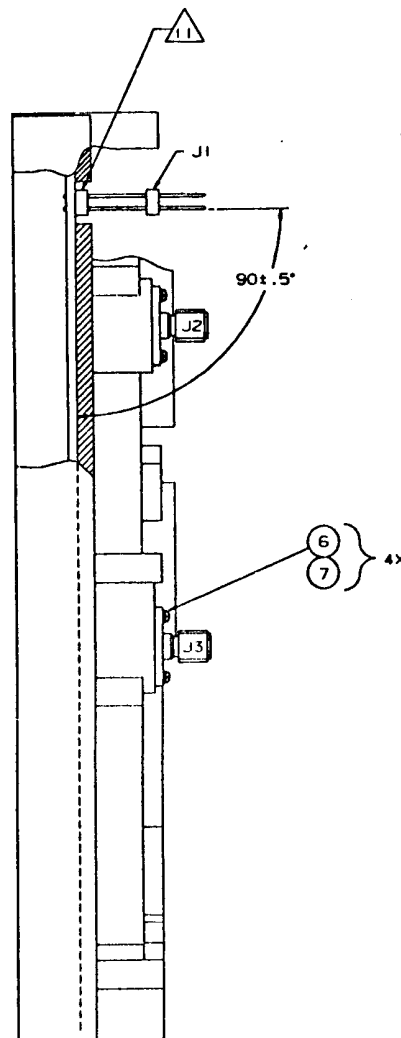
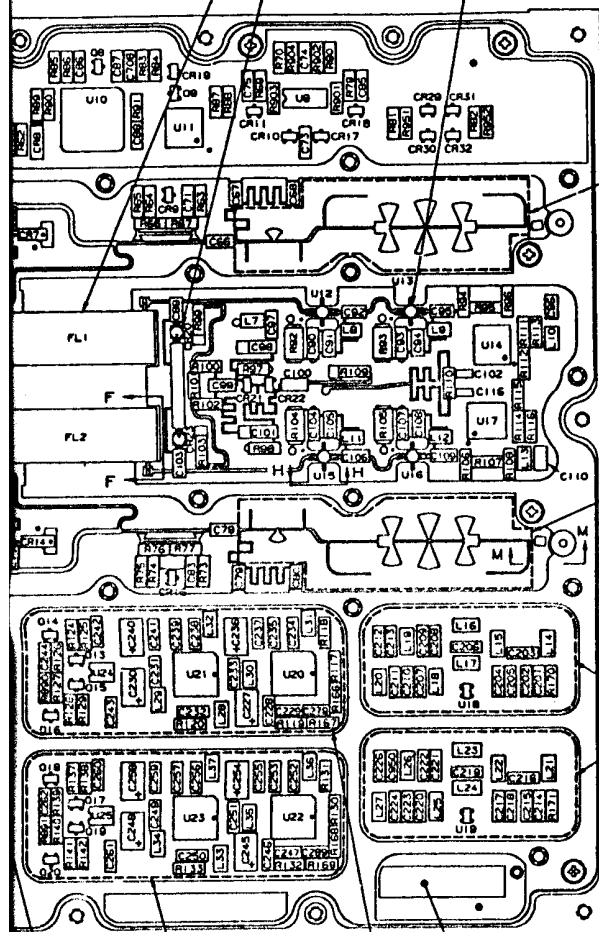
04 AND 05
DETAIL C

IDENTIFICATION LABEL SIDE OF ITEM 2. ORIENTATELY DOWN

SEE DETAIL E

SEE DETAIL G

SEE DETAIL J



BEFORE TIGHTENING SCREWS, MOVE PART AS CLOSE AS POSSIBLE AGAINST INDICATED EDGE OF BOARD CUT OUT.

GENTLY BEND END OF TRANSISTOR LEAD DOWN AGAINST PAD. SOLDER THE END OF THE LEAD ONLY. DO NOT LET SOLDER FLOW INTO STRESS RELIEF AREA OF THE LEAD.

NOT SHOWN

MUST BE SOLDERED TO EDGE OF PAD

C43, C45, C50 AND C702
SECTION L-L

WRAP SILVER RIBBON ITEM 703 AROUND CENTER PIN AND ATTACH TO PWB

06 AND 07
DETAIL N

13, U15 AND U16
DETAIL J

ALIGN FRONT AND SIDE EDGE OF Y1 PLUSH TO WITHIN .015 OF PWB PAD EDGE

DETAIL K

J2(W5), J3(W6)
SECTION M-M

Honeywell

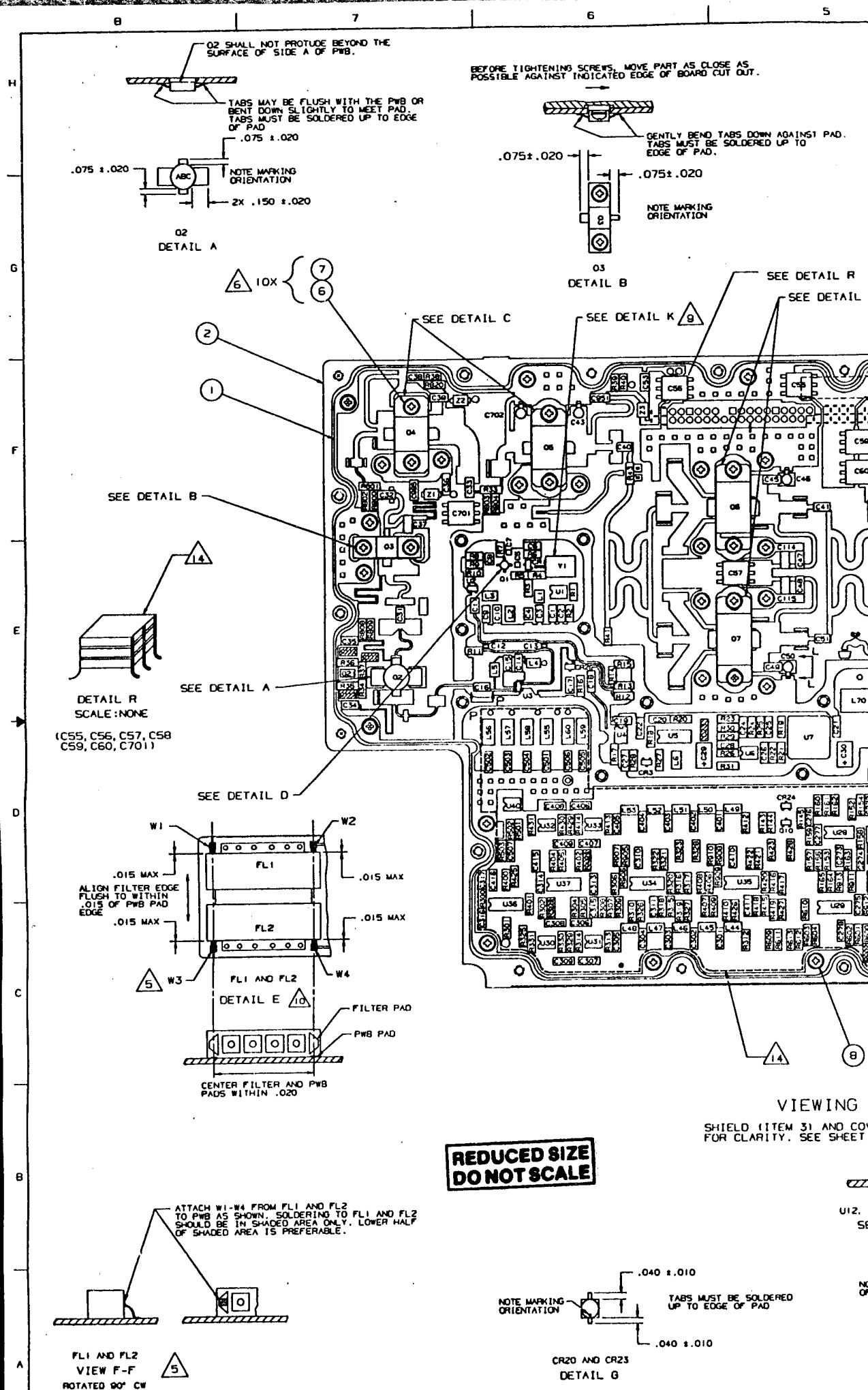
SEE FIRST SHEET FOR CONTINUED INFORMATION ON THIS DRAWING. QUALITY TO THE EXTENT OF THE DRAWING. SCALE 2/1

STANDARD CODE 55939 7517835-904

REV

D

SHEET 2



CUT NO. 1 2

23 LOCATE ITEM 707 APPROXIMATELY AS SHOWN AND ADHERE TO ITEM 2. ITEM 707 SHALL NOT CONTACT SURFACES
A, B OR C AND SHALL NOT EXTEND PAST THE EDGES OF ITEM 2.

24 ITEM 172 SHALL NOT EXTEND BEYOND THE EDGE OF ITEM 1

1. ASSEMBLE AND SOLDER PER ITEM 701, EXCEPT:
A. SOLDER PASTE: USE ITEM 705.
B. SOLDER: USE ITEM 704.

NOTES:

[illegible]

ONG
NO. 7517835-905

REV STATUS OF PARTS LIST SHALL BE SAME AS
BASIC DRAWING NUMBER
SEE SEPARATE PARTS LISTS

C/O 103859
SH 2/7

INDUCTOR		ITEM NO.	PWB SIDE	FROM CIRCUIT POINT	TO CIRCUIT POINT
1	706	B	J1-49	GND	
2	706	B	E1	R953 LEFT	
3	706	B		R953 RIGHT	R412 BOTTOM

CUT NO.	PWB	FROM	CIRCUIT POINT	TO	CIRCUIT POINT
1	8	J1-45			GND
2	8	E1			GND

TABLE I

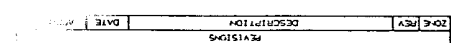
CUT RUN DATA

20

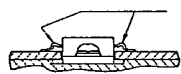
ZONE	REV	DESCRIPTION	DATE
	—	INITIAL RELEASE	1/1/78



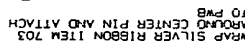
24



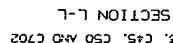
1



BEFORE TIGHTENING SCREWS, MOVE PART AS CLOSE AS POSSIBLE AGAINST INDICATED EDGE OF BOARD CUT OUT.

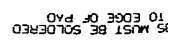


100

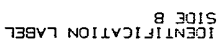


DETAIL K 714130

ALIGN FRONT AND SIDE
EDGE OF FLUSH TO
WITHIN .015 OF PWS
PAD EDGE



NOT SHOWN



IDENTIFICATION LABEL SIDE 8



NOTE MARKING ORIENTATION	ABC	
--------------------------	-----	--

IDENTIFICATION LABEL
 SIDE OF ITEM 2
 PROXIMATELY

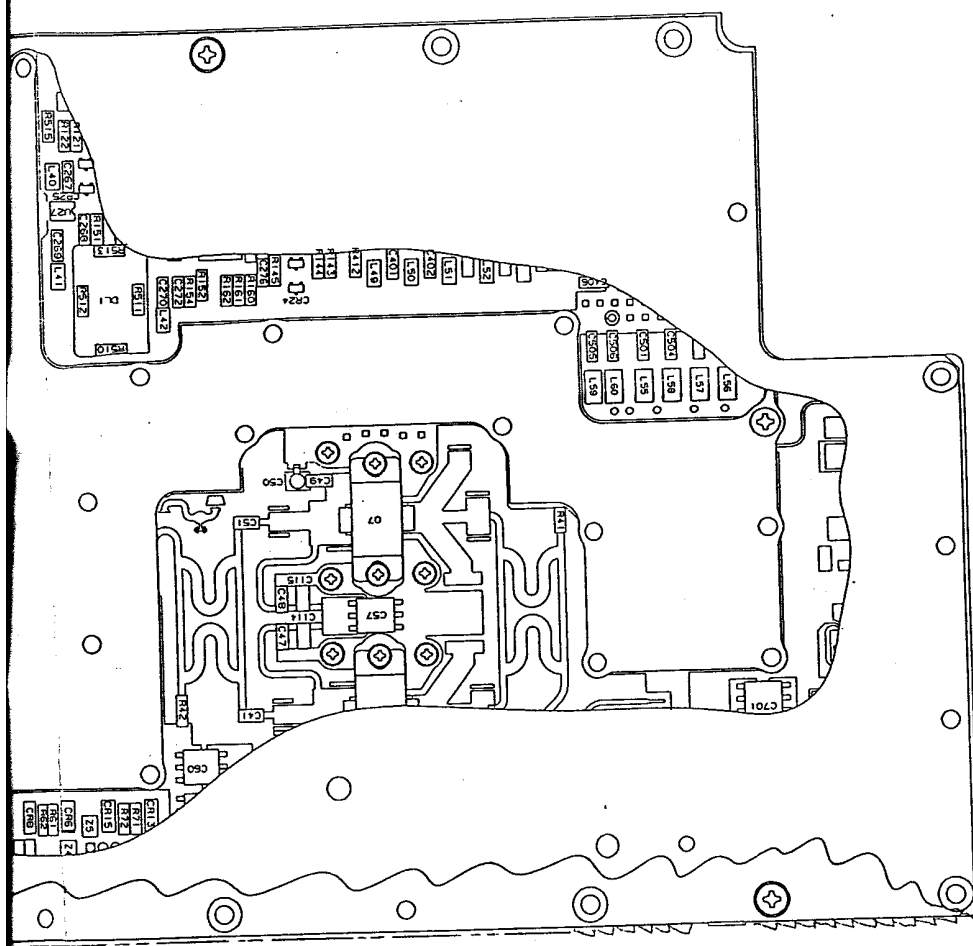
SCALE : NONE

SCALE : NONE



SOLDIER OF 10

SOLDIER OF 10



SURFACE B

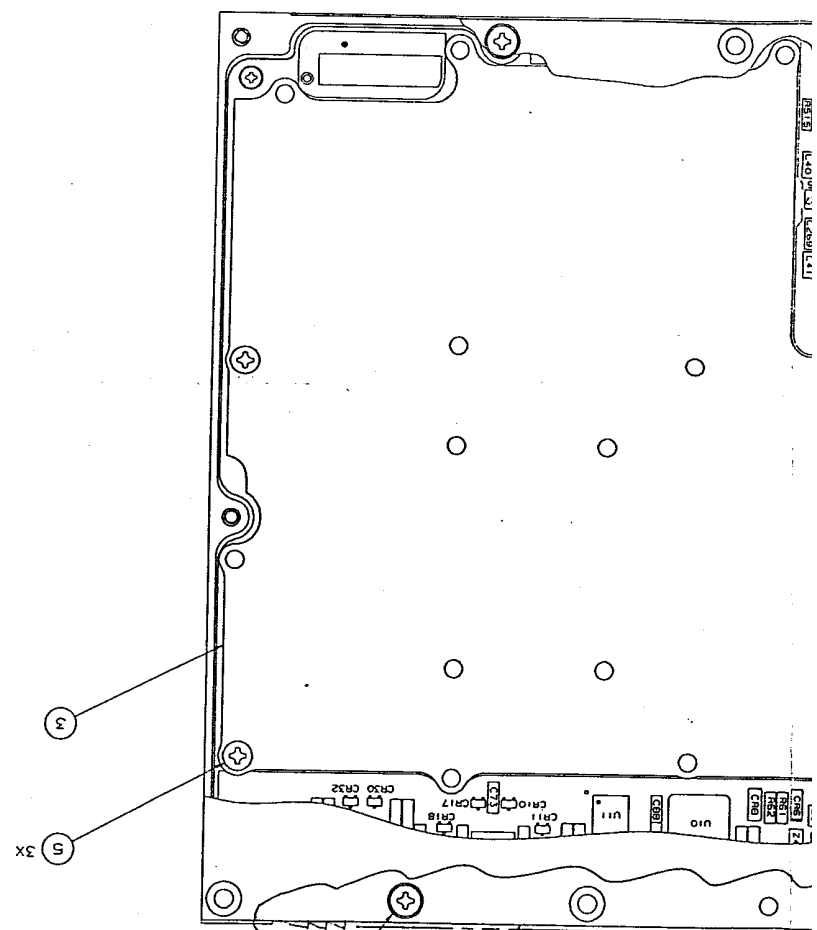
SURFACE A

23 707 10 FINGER CONTACTS

7517835-905
 SHEET 3

C/O 103859
 SH 4/7

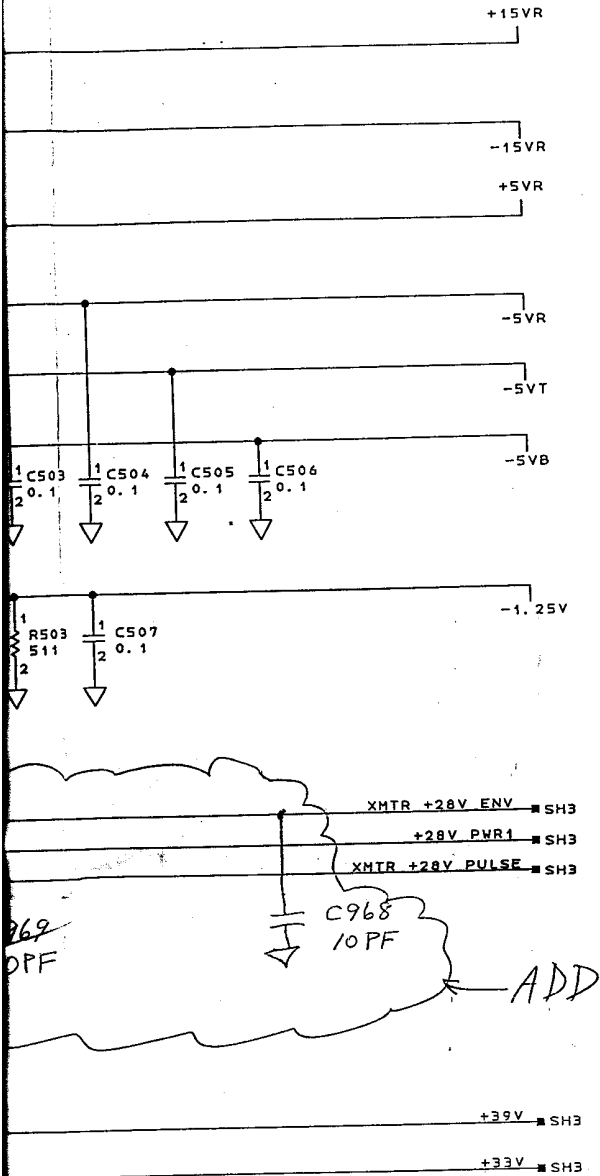
A



ADD

ADD

23 707
 56 FINGER CONTACTS



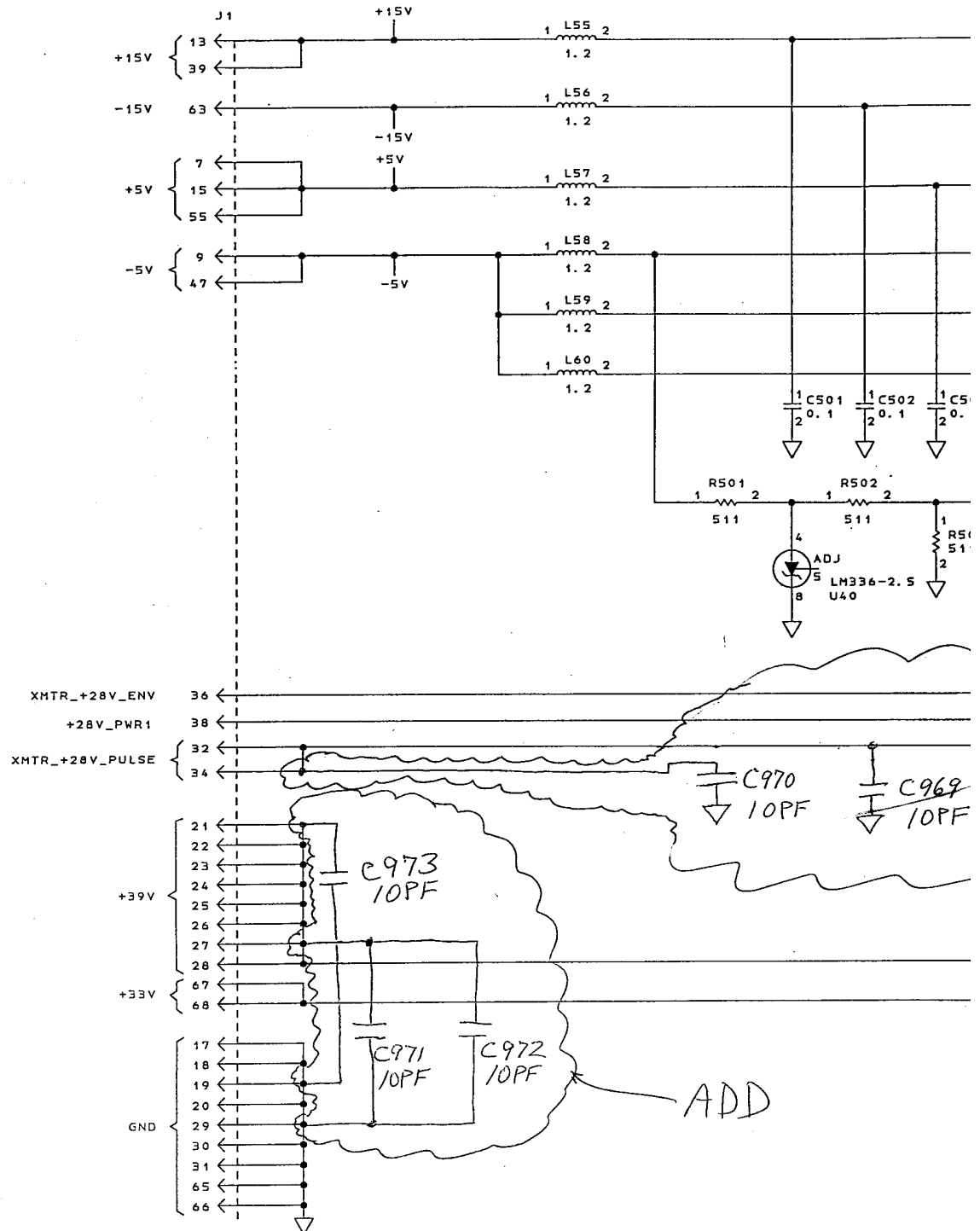
POWER LINE FILTERS

SCHEMATIC DIAGRAM SHEET NO. 1 OF 16

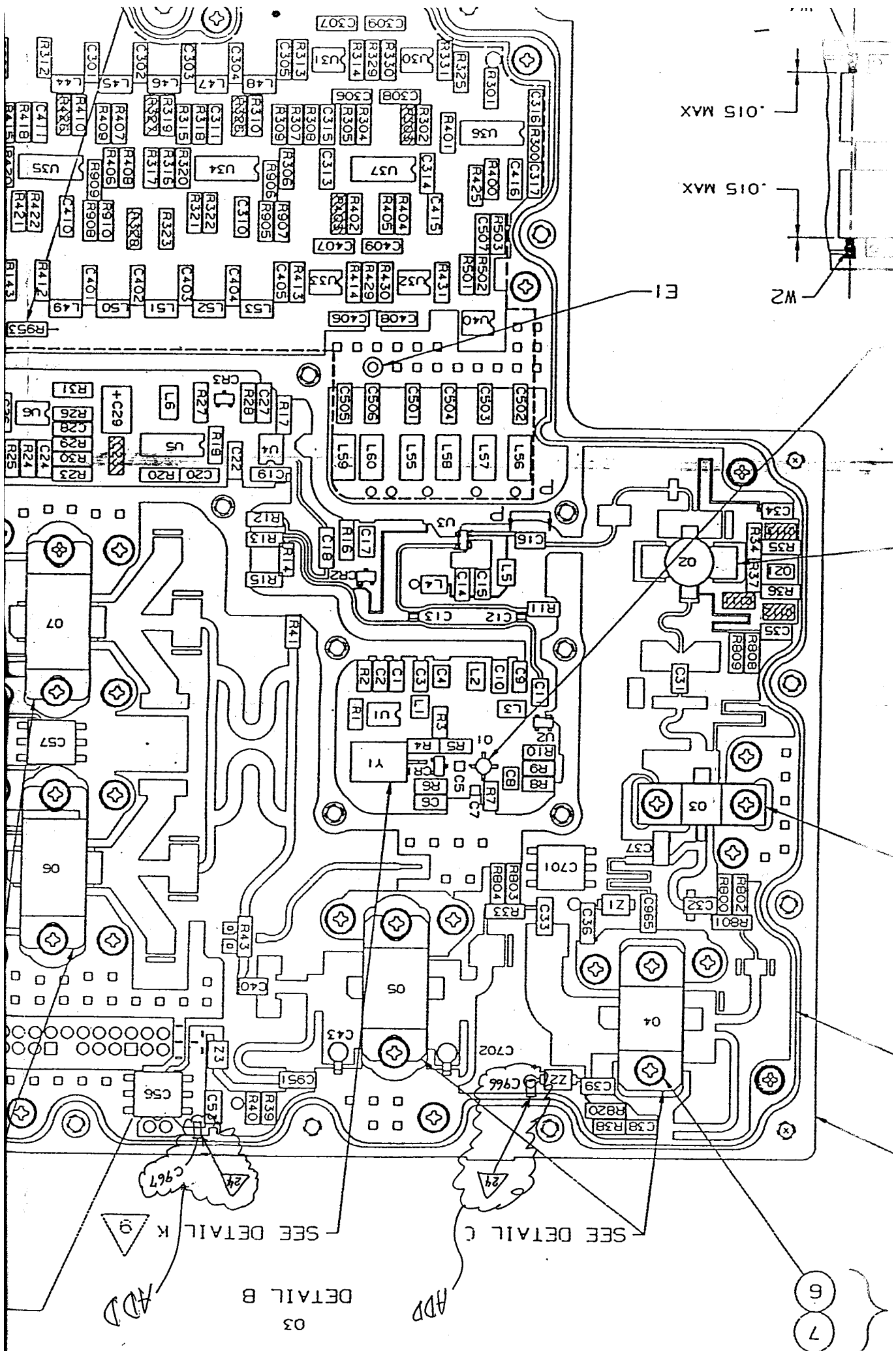
COMPUTER GENERATED

C/O 103859

A



- NOTES:
1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 2. ALL RESISTANCE VALUES ARE IN OHMS.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS PREFIX PART DESIGNATION WITH SUBASSEMBLY DESIGNATION.
 5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND ANSI/IEEE STD 991-1986. LOGIC SYMBOLS CONFORM TO ANSI/IEEE STD 91-1984.
 6. RESISTORS R68-R71 ARE USED ONLY TO SPACE DL1 OFF OF THE PWB. ELECTRICAL CONTACT BETWEEN ANY OF THESE RESISTORS AND DL1 IS PERMISSIBLE.



.015 MAX
.015 MAX

W2

F1

6
7

SEE DETAIL C

03
DETAIL B

SEE DETAIL K

ADD

ADD

04 AND 05

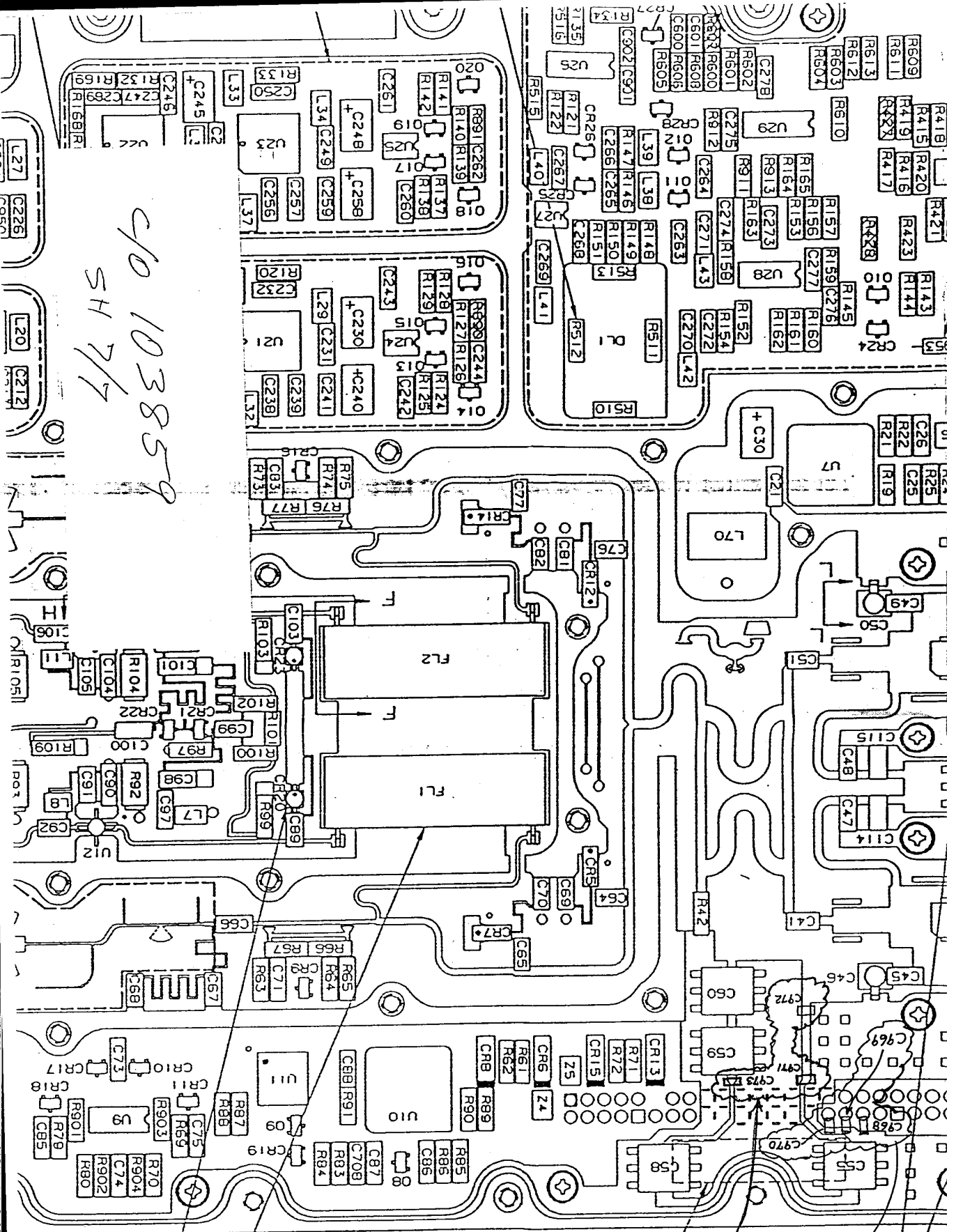
DETAIL C

SEE DETAIL

IDENTIFICATION LABEL
FAR SIDE OF ITEM 2
APPROXIMATELY
AS SHOWN

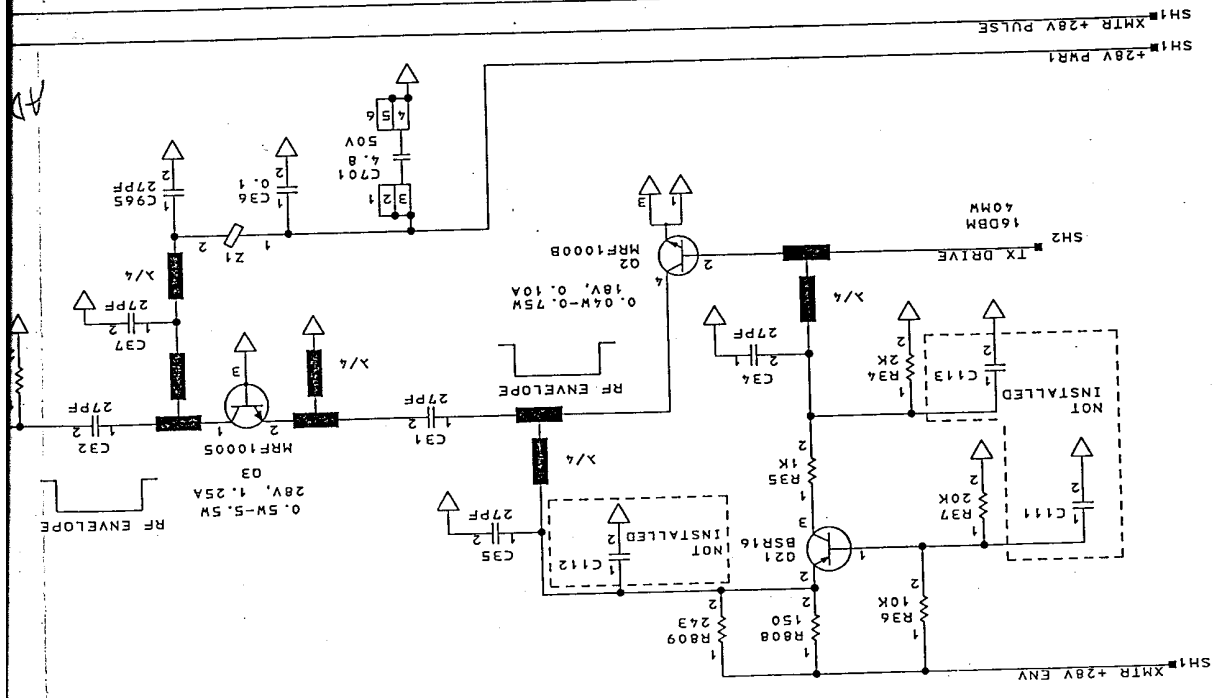
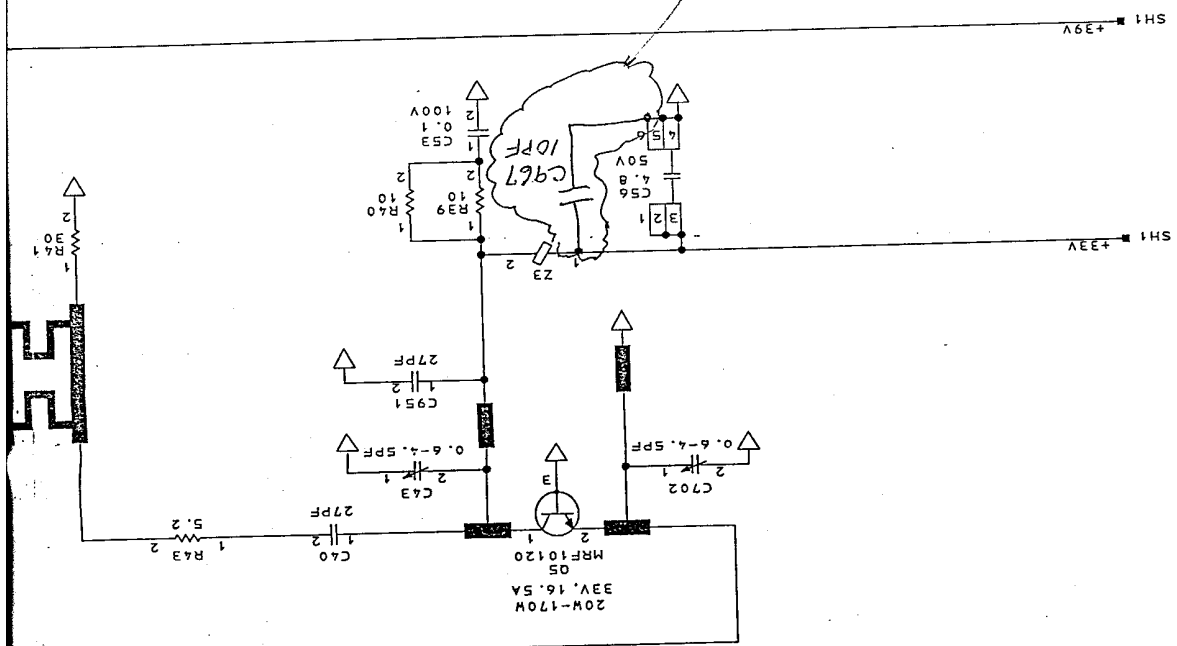
SEE DETAIL R

SEE DETAIL N



POWER AMPLIFIER

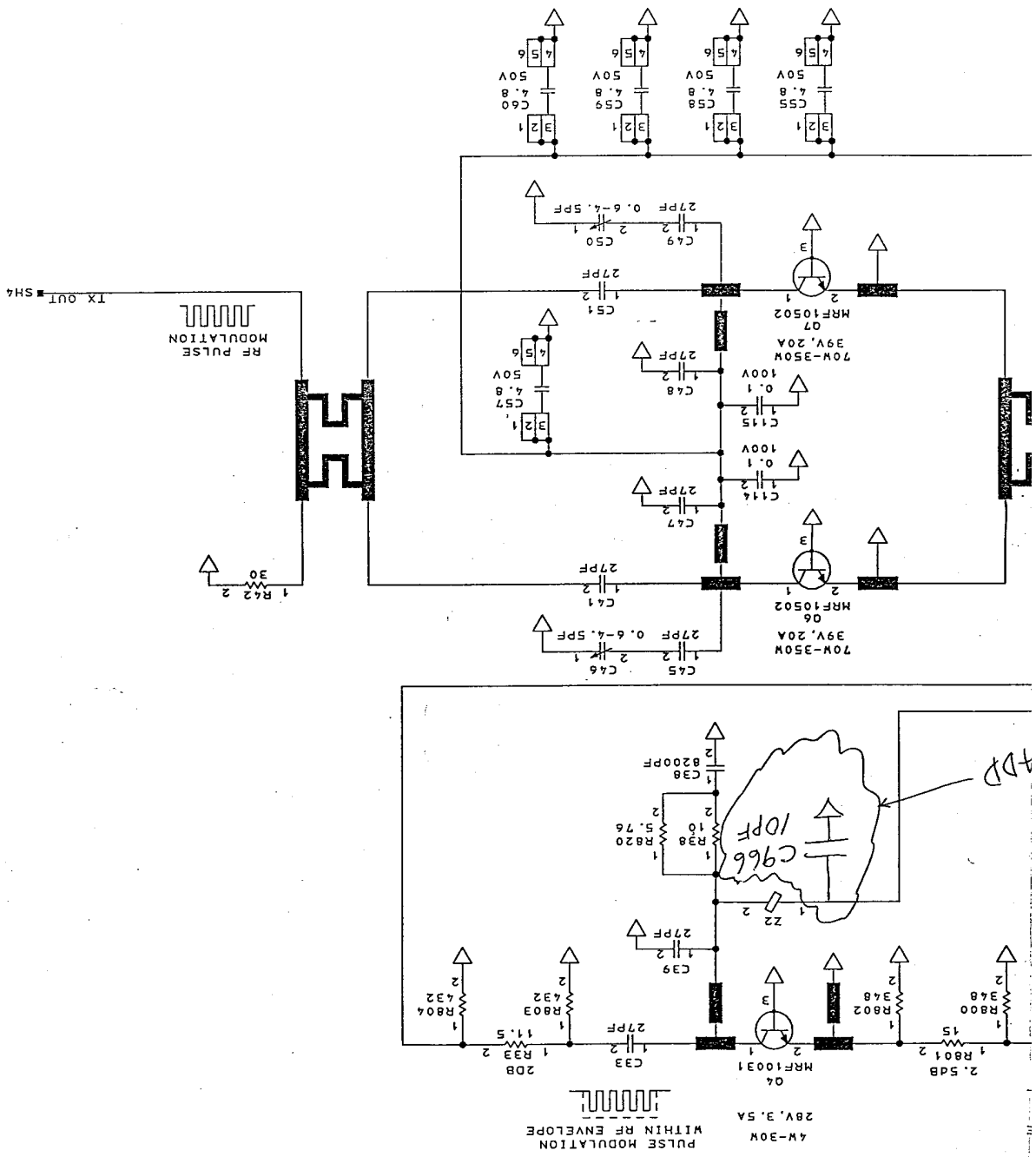
ADD



COMPUTER GENERATED

POWER AMPLIFIER
SCHEMATIC DIAGRAM SHEET NO. 3

C/O 103859
SH 6/7



[illegible]