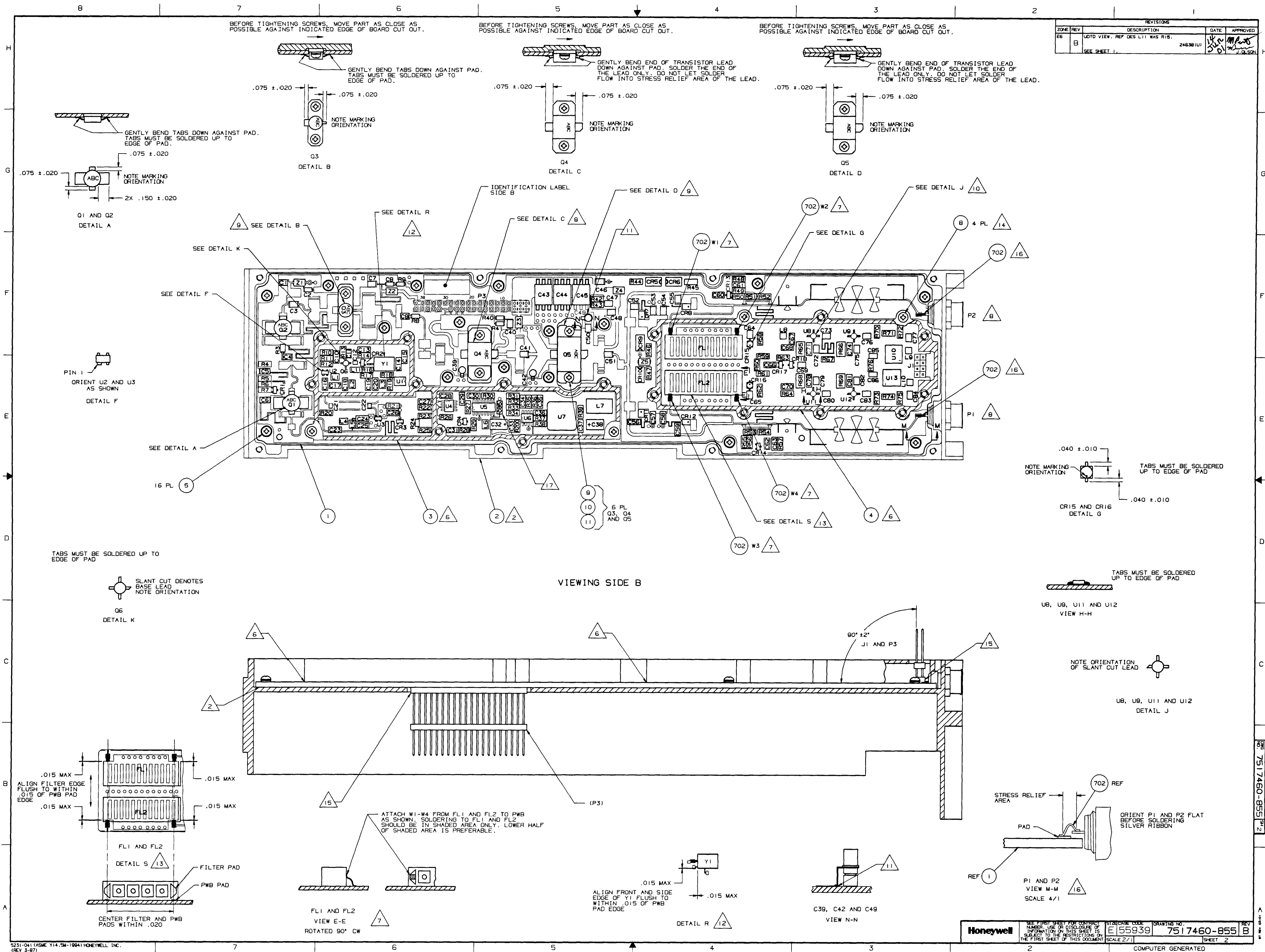
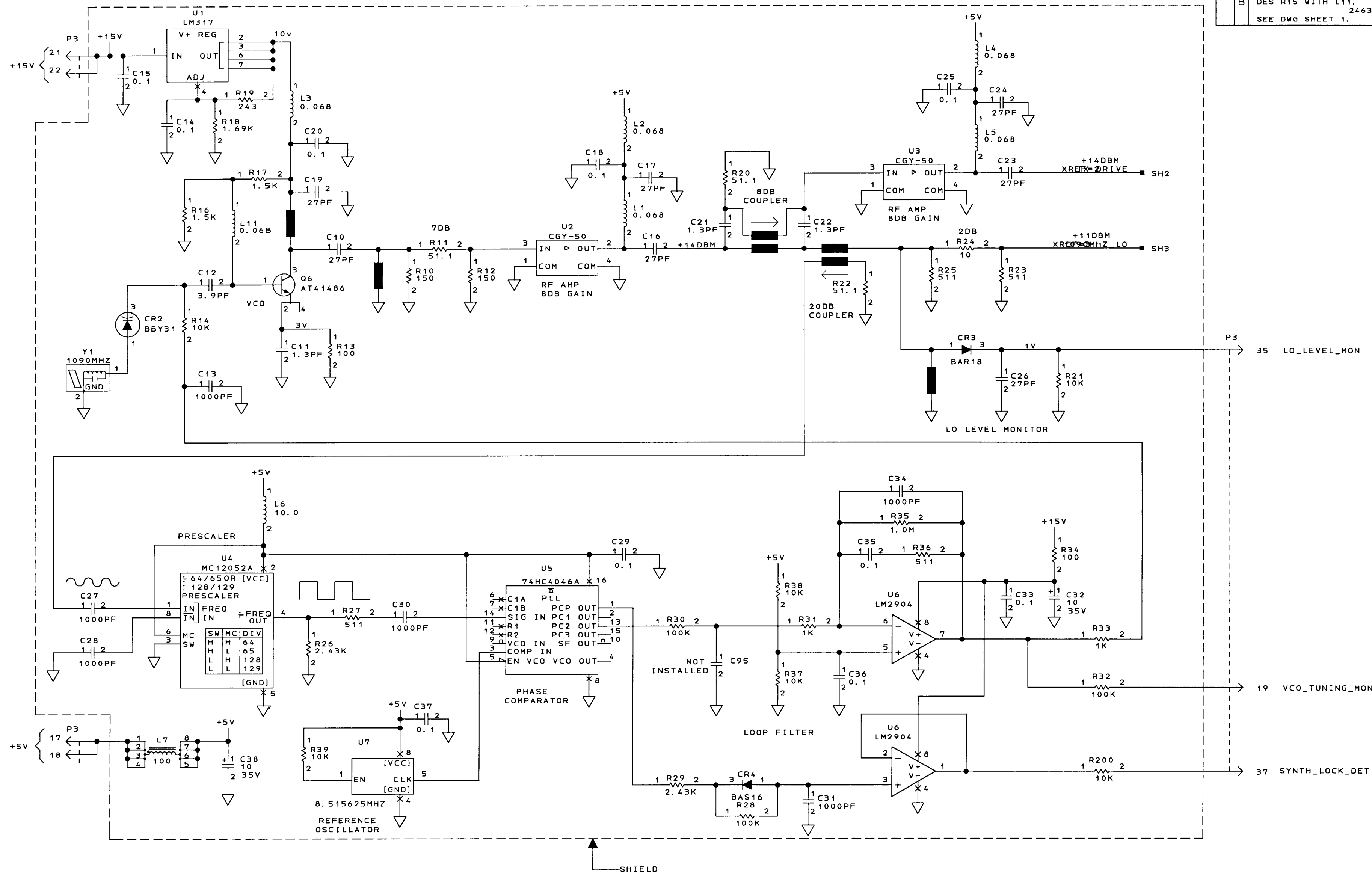




REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
D7	A	UDTD SCHEM: REF DES U4 TYPE WAS MC12022A.	10 NOV 00	M. INOUE
		SEE DWG SHEET 1.		M. BROWN
B		UDTD SCHEM: REPLACE REF DES R15 WITH L11.		M. BROWN
		SEE DWG SHEET 1.		M. RICHARDS

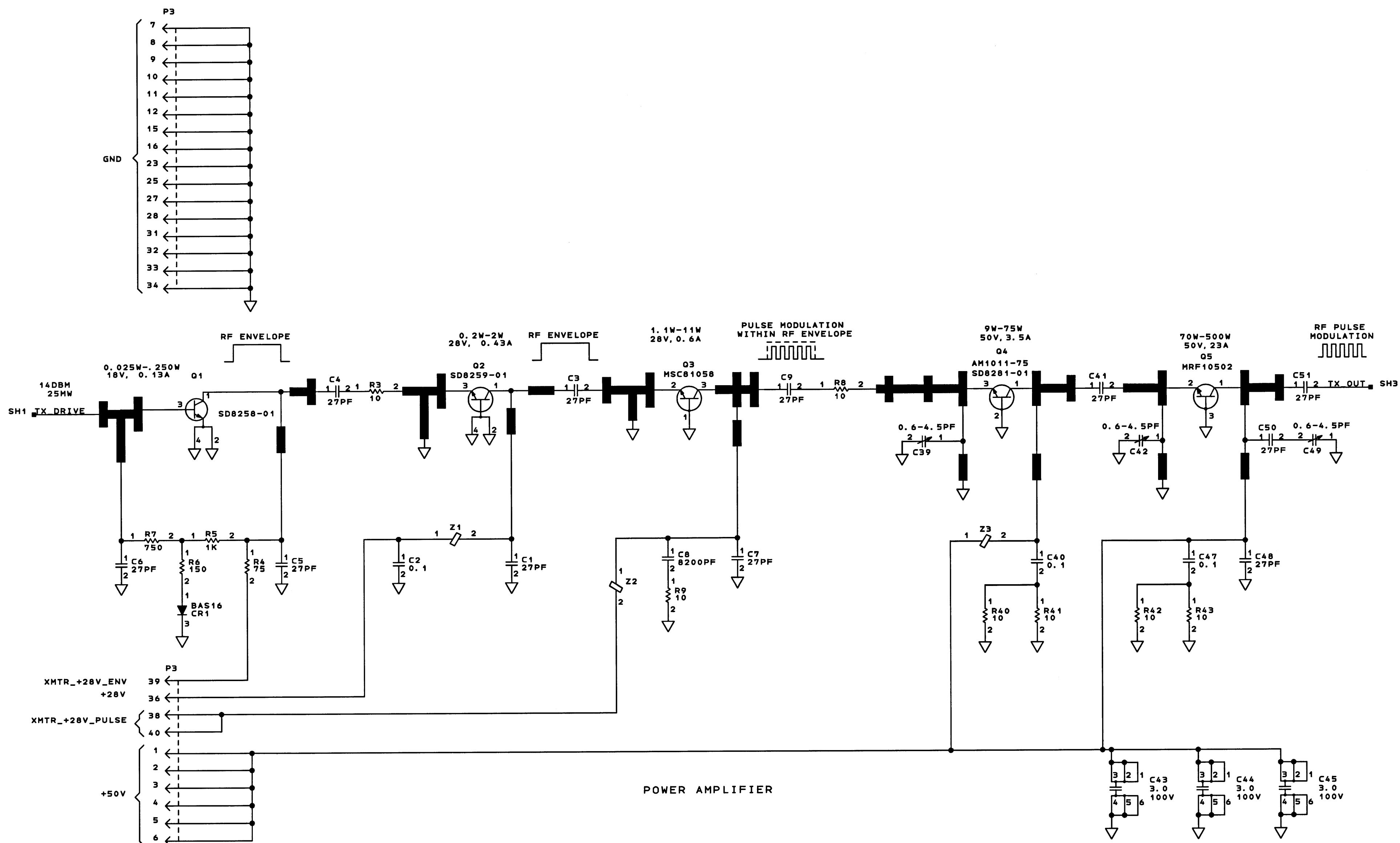


- NOTES:
UNLESS OTHERWISE SPECIFIED:
1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
2. ALL RESISTANCE VALUES ARE IN OHMS.
3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN.
5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND ANSI/IEEE STD 991-1986. LOGIC SYMBOLS CONFORM TO ANSI/IEEE STD 91-1984.
6. ALL VOLTAGES ARE DC UNLESS OTHERWISE NOTED.

1090 MHz LOCAL OSCILLATOR
SCHEMATIC DIAGRAM SHEET NO. 1 OF 5

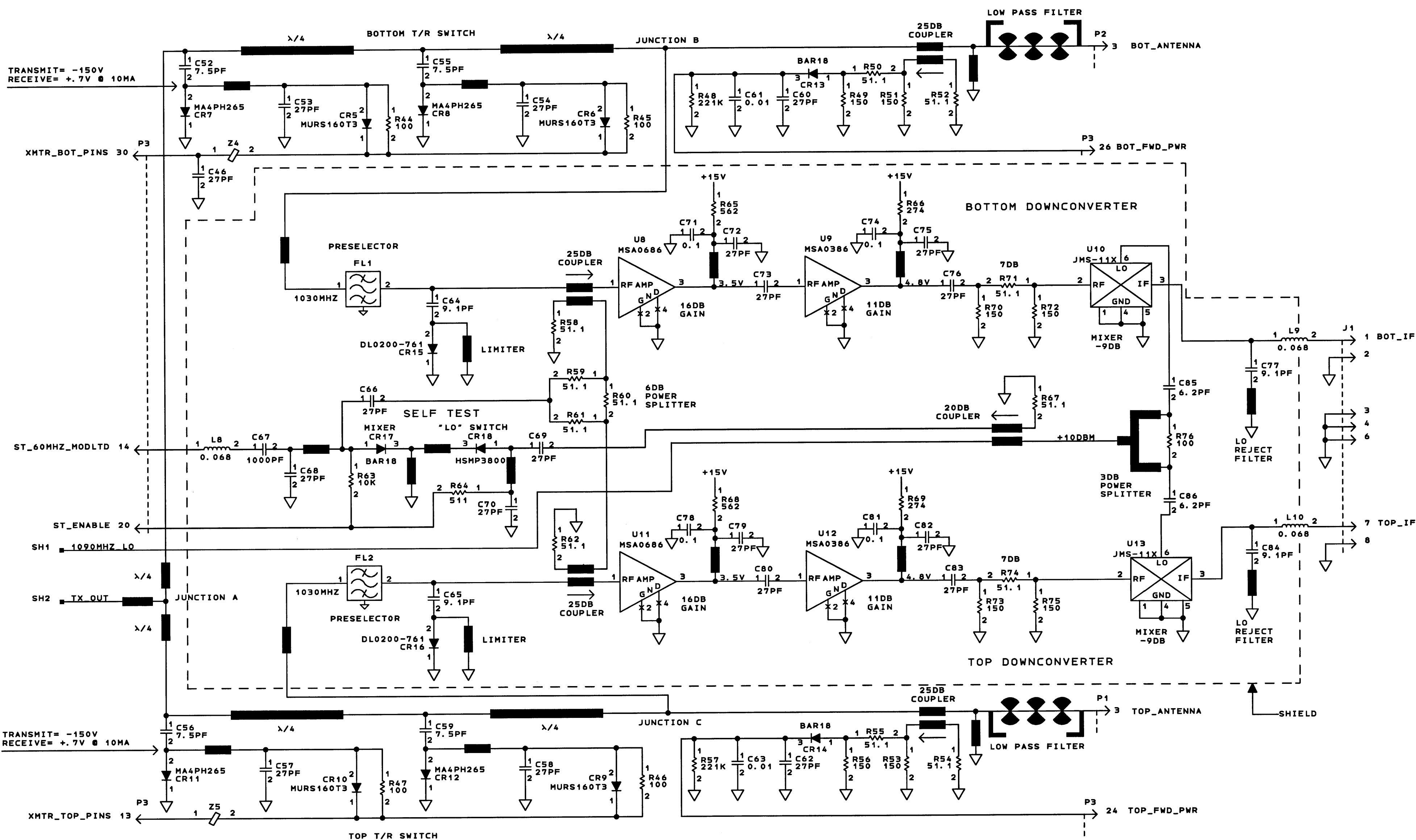
COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	7517460-855	B



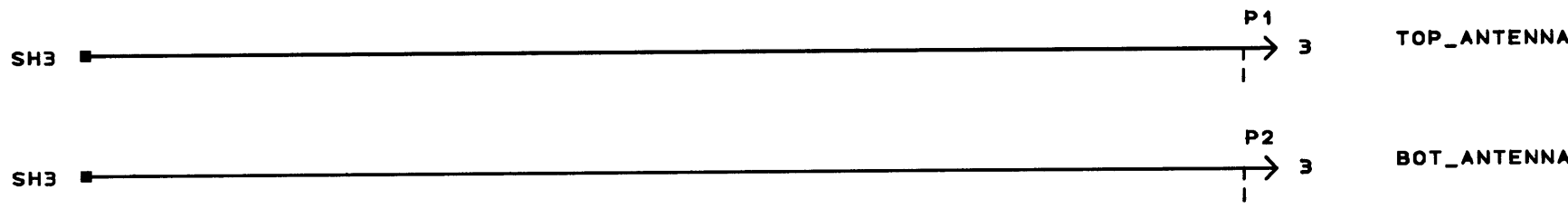
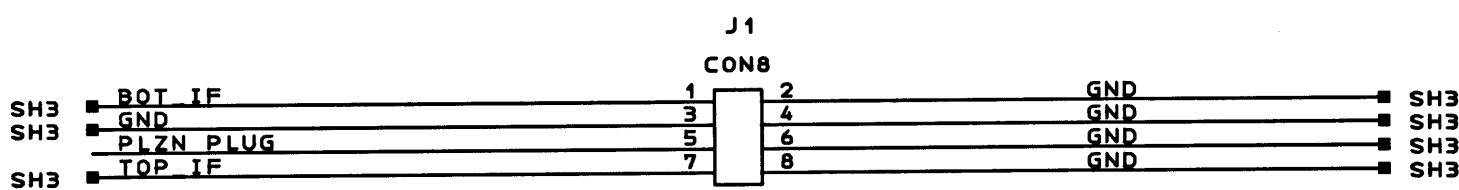
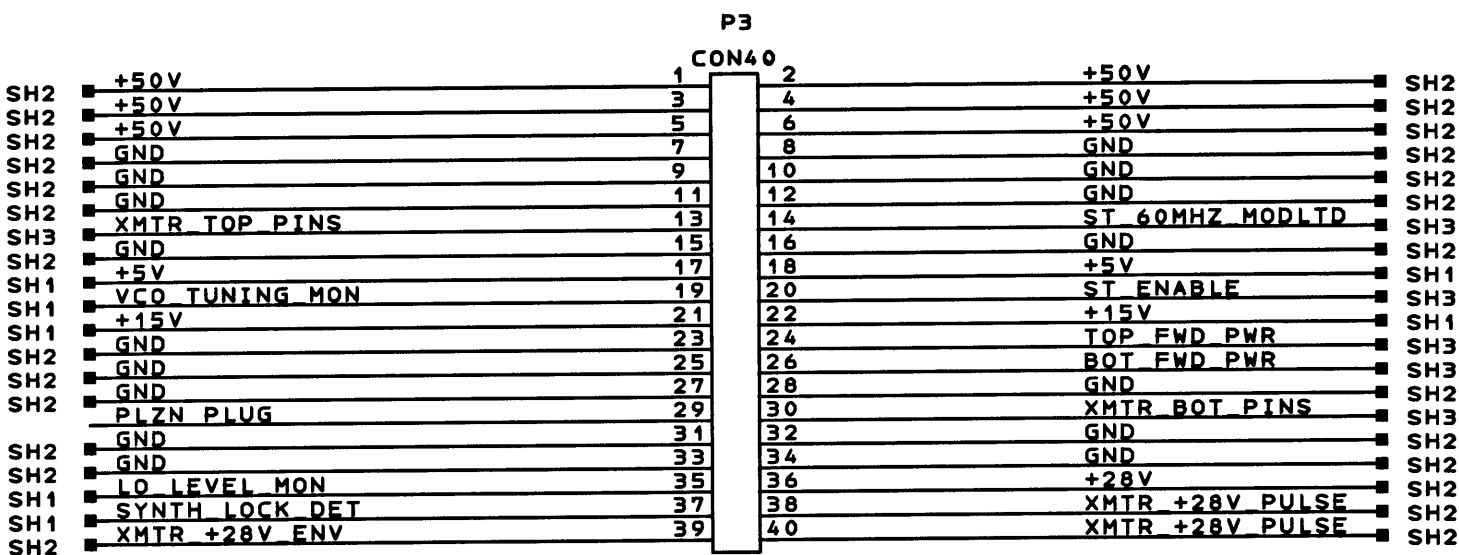
POWER AMPLIFIER
SCHEMATIC DIAGRAM SHEET NO. 2

COMPUTER GENERATED

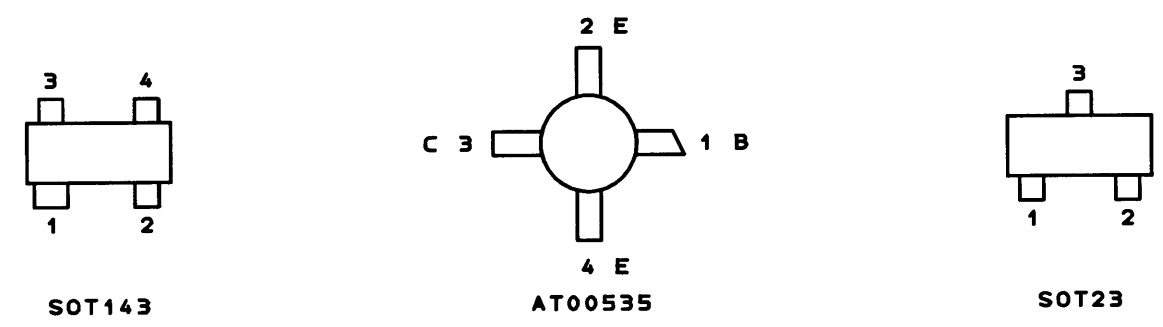


DUPLEXER/DOWNCONVERTERS/SELF-TEST
 SCHEMATIC DIAGRAM SHEET NO. 3

COMPUTER GENERATED



PINOUTS OF SMT DEVICES

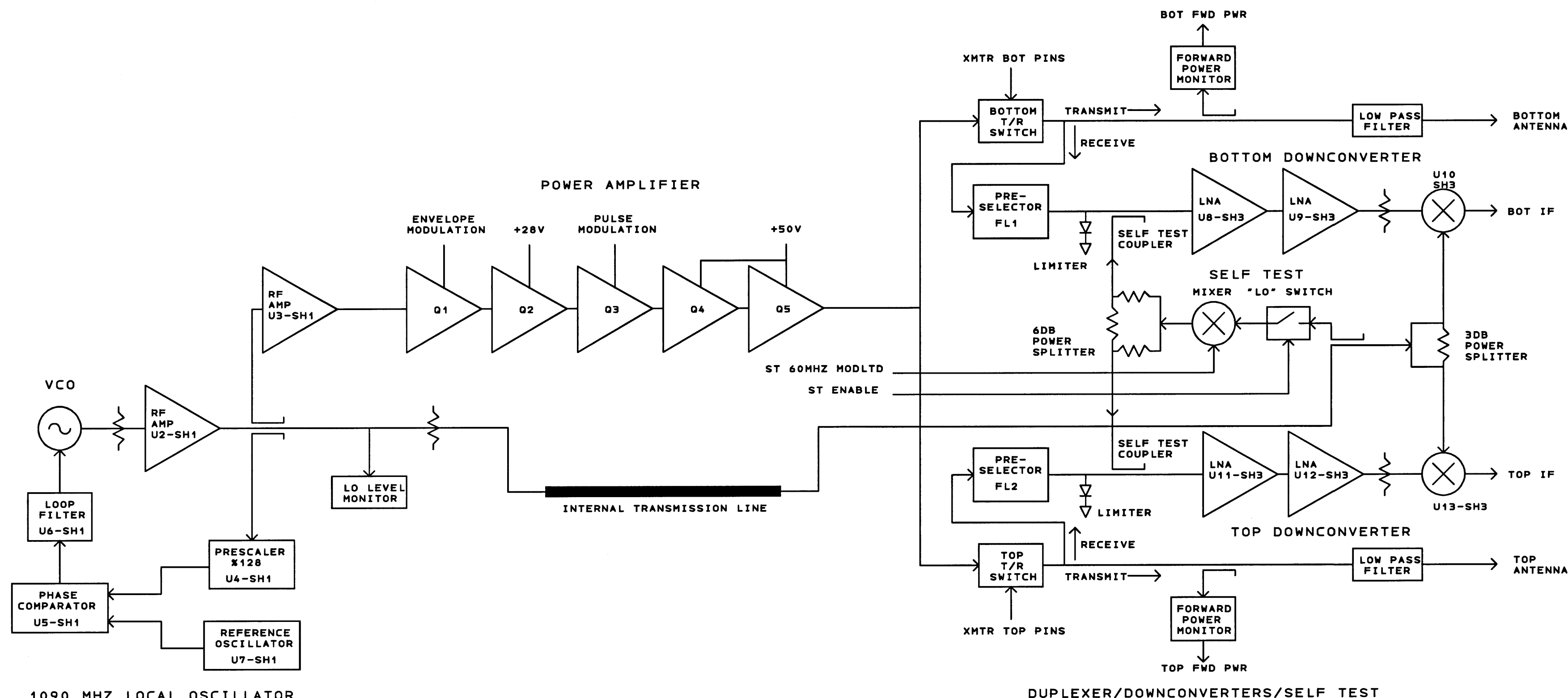


COMMON LABELS
ON TOP OF SMT DEVICES

PART NAME	LABEL
BAS16	A6
BAR18	5F, D76
BZX84-C13	Y3
BSR14	U8
MMBT5550	1F
BSR16	T8
SMBG15A	LH
SMBG5A	KE
MURS160T3	U1J
MMBT5401	2L
BZX84-C10	6Z
SMBG48A	MW
PZTA92	ZTA92
CGY50	G2
MSA0686	A06
MSA0386	A03
HSMP3800	D0
AT00535	NONE
HSMP3820	F0

CONNECTOR PIN-OUT
SCHEMATIC DIAGRAM SHEET NO. 4

COMPUTER GENERATED



BLOCK DIAGRAM
SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED