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**TELSON INFORMATION & COMMUNICATION CO., LTD.**

TEL : 82-2-887-2690 (EXT 766)

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### 3. THEORY OF OPERATION

#### **FREQUENCY GENERATION CIRCUITRY**

THE FREQUENCY GENERATION CIRCUITRY IS COMPOSED OF THE SYNTHESIZER IC(U102) AND THE VCO. THE BLOCK DIAGRAM ILLUSTRATES THE INTERCONNECT AND SUPPORT CIRCUITRY USED IN THE DESIGN. REFER TO THE SCHEMATIC FOR REFERENCE DESIGNATOR.

THE SUPPLY FOR THE SYNTHESIZER IS REGULATED 5.0 VOLTS WHICH ALSO SERVES THE REST OF THE RADIO. IN ADDITION TO THE VCO, THE SYNTHESIZER MUST INTERFACE WITH THE LOGIC AND AUDIO FILTER CIRCUITRY. PROGRAMMING FOR THE SYNTHESIZER IS ACCOMPLISHED THROUGH THE CLOCK, DATA AND ENABLE SIGNALS FROM THE MICROPROCESSOR, U200.

A SERIAL DATA IS SENT WHENEVER THE SYNTHESIZER IS PROGRAMMED. A 5.0 VOLTS DC SIGNAL FROM PIN 7 INDICATES TO THE MICROPROCESSOR THAT THE SYNTHESIZER IS LOCKED. WHILE UNLOCK IS INDICATED BY A LOW VOLTAGE ON THIS PIN. THE AUDIO SIGNAL FROM THE AUDIO FILTER IS MODULATED BY THE VARICAP DIODE(D59) OF VCO.

#### **PLL FREQUENCY SYNTHESIZER**

THE U102 PLL IC INCLUDES ALL THE FUNCTIONS SUCH AS THE PHASE COMPARATOR, THE PROGRAMMABLE DIVIDER, THE LOCK DETECTOR AND REFERENCE OSCILLATOR. THE SYNTHESIZER USES A 14.4MHz VCTCXO(U103) TO PROVIDE THE REFERENCE FREQUENCY FOR THE SYSTEM.

THE CHARGE PUMP CIRCUIT AND LOOP FILTER, COMPRISED OF Q58, Q59, R133 AND R138, R139, R140, R141, C172, C173, C174, PROVIDE THE NECESSARY DC STEERING VOLTAGE FOR THE VCO AS WELL AS FILTERING OF SPURIOUS SIGNALS FROM THE PHASE DETECTOR. THE PRESCALER FOR THE LOOP IS INTERNAL TO U102 WITH THE VALUE DETERMINED BY THE FREQUENCY BAND OF OPERATION.

THE VCTCXO IS THE TEMPERATURE COMPENSATION CIRCUIT TO MAINTAIN THE FREQUENCY WITHIN THE ALLOWABLE ERROR RANGE EVEN UNDER A LOW TEMPERATURE OF -30° C.

#### **VCO**

THE VCO IS OSCILLATED BY THE CONTROL VOLTAGE SUPPLIED FROM THE SYNTHESIZER AND CONSISTS OF THE COLPITTS OSCILATOR OF THE Q61(RX), Q62(TX).

THE OPERATION OF RX AND THX IS SELECTED BY 5V SWITCH TR(Q75,Q77) AND THE TX VCO OSCILLATES FROM 146MHz TO 174MHz BY CONTROL VOLTAGE (1.5V~5.5V) AND THE RX VCO OSCILLATES FROM 191MHz TO 219MHz BY CONTROL VOLTAGE (1.5V~5.5V).

IN THE TRANSMIT MODE, THE VCO IS OSCILLATED BY THE COLPITTS OSCILLATOR, WHICH CONSISTS OF D58, D57, C157, C158, R134, D59, C187, C188, VC2,

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IN THE RECEIVE MODE, THE VCO IS OSCILLATED BY THE COLPITTS OSCILLATOR, WHICH CONSISTS OF D55, D56, C177, VC1, L72, C178, L72, C179, C180 AND Q61. THE Q63 AND PERIPHERAL COMPONENTS AMPLIFY AN RF CARRIER OSCILLATED FROM TX AND RX VCO, MAINTAIN -10dBm OUTPUT LEVEL AND REJECT THE EFFECTS OF REVERSE POWER CONNECTED TO OUTPUT TERMINAL.

**POWER DRIVE**

TX DRIVE STAGE AMPLIFIES THE RF POWER FOR THE MODULE INPUT TO BE SATISFIED, AS WELL AS HAS THE ABILITY FOR VCO NOT TO BE OSCILLATED. IT IS COMPOSED OF THE TWO STAGES AMPLIFIER. THE FIRST AMPLIFIER (Q65) OPERATES AS CLASS A AND HAS THE FUNCTION TO BLOCK THE OSCILLATION CAUSED BY THE MISMATCHING BETWEEN VCO AND POWER MODULE. THE SECOND ONE (Q66) AMPLIFIES THE INPUT POWER FOR THE MODULE TO BE AMPLIFIED INTO 5 WATTS. RF POWER DRIVE AMPLIFIES BY 17dBm (50mW).

**TX MODULE AND APC**

THE TX MODULE (U101), WHICH FINALLY AMPLIFIES THE RF CARRIER, IS CONNECTED WITH THE APC (AUTOMATIC POWER CONTROL) CIRCUIT TO CONTROL A POWER. IT AMPLIFIES 50mW OBTAINED FROM TX DRIVE TO 7 WATTS. THE VOLTAGE OF TX MODULE IS PASSED THROUGH THE BEAD COIL BD1.

THE APC IS SENSING AN RF POWER AND CONSTANTLY MAINTAINS AN RF POWER REGARDLESS OF VOLTAGE CHANGES BY DECREASING THE CONTROL VOLTAGE OF POWER MODULE IN OVER CARRIER CASE AND INCREASING THAT OF POWER MODULE IN UNDER CARRIER CASE.

THE APC CONSISTS OF RF CARRIER SENSING PARTS. THE RF CARRIER IS TRANSFORMED TO VOLTAGE BY C226, D60 AND C227. THE VOLTAGE IS SUPPLIED INTO THE BASE OF DIFFERENTIAL AMPLIFIER, Q68. THEN LOW POWER (1 WATT) IS CONTROLLED BY THE VR13, WHEREAS HIGH POWER BY VR14 AND Q67.

THE DIFFERENTIAL AMPLIFIER, Q68 CONTROLS BY COMPARING THE BASE REFERENCE VOLTAGE FROM SENSING PART. THE CONTROL VOLTAGE OF AN POWER MODULE TO BE THE SPECIFIED RF POWER.

**ANTENNA SWITCH AND HARMONIC FILTER**

THE ANTENNA SWITCH IS COMPOSED OF SWITCHING DIODE. THE ANTENNA SWITCH SELECTS TX CARRIER OR RX SIGNAL. THE R/TX SWITCHING IS ACCOMPLISHED BY D50, D51.

IN TRANSMISSION MODE, 5.0V DC IS APPLIED TO R175, L85 THEN D50, D51 ARE ON. TX CARRIER PASSED BY D50 AND THROUGH THE HARMONIC FILTER TO ANTENNA.

IN RECEIVER MODE, 5.0V DC IS NOT APPLIED TO D50. THEN D50 IS OPENED. RX SIGNAL IS PASSED TO C107.

THE HARMONIC FILTER IS COMPOSED OF C100, C102, C103, C104, C105, C106, L50, L51, L52 AND L53. THIS FILTER REMOVES HARMONIC SPURIOUS LIKE A 2<sup>nd</sup>, 3<sup>rd</sup> CONDUCTED SPURIOUS. IN TRANSMISSION MODE, THIS FILTER HAS INSERT LOSS

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### FRONT END

FRONT END SELECTS AND AMPLIFIES THE SIGNALS OF WANTED BAND AMONG RF SIGNALS EXISTING IN THE FREE SPACE. IT CONSISTS OF THE BANDPASS FILTER (BPF) AND LOW NOISE AMPLIFIER (LNA).

THE FILTER IS COMPOSED OF 5<sup>TH</sup> BPF BEFORE THE LNA AND 7<sup>TH</sup> BPF AFTER THE LNA. IT SELECTS THE BANDS BETWEEN 146MHz AND 174MHz.

ESPECIALLY THE BILATERAL DIODE, D52 BLOCKS FOR LNA TO BE BROKEN OR SATURATED BY LIMITING THE CRITICAL RF SIGNALS FROM THE ANTENNA. THE GAIN OF THE LNA IS 20Db. THE LNA MAKES SIGNALS MAXIMUM, HAS THE MINIMUM NOISE FIGURE.

### MIXER

THE MIXER USES THE FET TO GENERATE THE FIRST IF, 45MHz BY MIXING THE RF CARRIER SIGNAL AND LOCAL SIGNAL FROM THE VCO. ITS FREQUENCY IS  $F_{IF} = F_{RF} - F_{LOCAL}$ .

### LOCAL DRIVE

THE LOCAL DRIVE SUPPLIES LOCAL CARRIER (191MHz~219MHz) GENERATED FROM THE SYNTHESIZER INTO MIXER, AND BLOCKS TO AFFECT THE UNWANTED FREQUENCY GENERATED IN MIXING TO THE VCO. IT IS COMPOSED OF Q64, PERIPHERAL CAPACITORS AND RESISTORS. THE SIGNAL TO BE BUFFERED MAXIMALLY LIMIT THE SPURIOUS BY USING LOW PASS FILTER THAT IS COMPOSED OF C204, C205 AND L80.

### CRYSTAL FILTER AND IF AMP

THE CRYSTAL FILTER CONSISTS OF A PAIR OF F2 AND F3, ITS PASS BAND WIDTH IS +/- 7.5kHz AND ITS ATTENUATION BANDWIDTH IS +/- 22kHz. ITS RIPPLE IS LESS THAN 1 dB AND INSERT LOSS IS 4 dB.

THE IF AMP (Q52) AMPLIFIES 20dB THE FREQUENCY SELECTED BY THE CRYSTAL FILTER.

### IF IC AND NOISE SQUELCH

THE IF IC (U100) CONSISTS OF THE 2<sup>ND</sup> MIXER, CERAMIC FILTER, CERAMIC DISCRIMINATOR, NOISE SQUELCH.

THE 2<sup>ND</sup> MIXER GENERATE THE 2<sup>ND</sup> IF 455kHz BY MIXING THE 1<sup>ST</sup> IF, 45MHz AND LOCAL FREQUENCY, 44.545MHz (X60). THE MIXED SIGNAL, 455kHz IS PASSED THE CERAMIC FILTER F5, F6 EACH CASE OF 12.5/25kHz. THE CERAMIC DISCRIMINATOR (X61) AND DAMPING RESISTOR (R127) OPERATE WITH A QUADRATURE CIRCUITRY, ENABLE THE RECOVERED AUDIO BY 90° PHASE SHIFTING IF CENTER FREQUENCY. THE DAMPING RESISTOR DETERMINES THE PEAK SEPARATION OF THE DETECTOR.

WHEN UNWANTED SIGNAL IS RECEIVED, THE NOISE SQUELCH IS ENABLED BY DETECTION OF NOISE LEVEL. NOISE DETECTION IS GIVEN FROM NOISE HIGH

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### MICROPROCESSOR UNIT (MPU)

THE MPU (U200) IS TH HIGH SPEED AND HIGH PERFORMANCE 8-BIT SIGNLE CHIP MICROCOMPUTER. THE MPU CONTAIN CPU CORE, ROM, RAM, INPUT/OUTPUT PORTS, A/D CONVERTER, TIMER/COUNTER.

### MPU PORT ASSIGNMENT

| PORT | PORT NAME                                    | FUNCTION                                                                     | PORT | PORT NAME                                                    | FUNCTION                                                                                           |
|------|----------------------------------------------|------------------------------------------------------------------------------|------|--------------------------------------------------------------|----------------------------------------------------------------------------------------------------|
| P0   | P0.0<br>P0.1                                 | PLL DATA<br>PLL ENABLE                                                       | P6   | P6.0<br>P6.1<br>P6.2<br>P6.3<br>P6.6                         | CH. SWITCH 1<br>CH. SWITCH 2<br>CH. SWITCH 4<br>CH. SWITCH 8<br>LOW BAT. DET.                      |
| P2   | P2.2<br>P2.3<br>P2.4<br>P2.6<br>P2.7         | PC PROGRAM<br>BEEP GEN.<br>TONE DET.<br>PLL LOCK<br>TONE DET                 | P8   | P8.0<br>P8.1<br>P8.2<br>P8.3<br>P8.4<br>P8.5<br>P8.6<br>P8.7 | TX B+<br>RX B+<br>TX ENABLE<br>VCO ENABLE<br>H/L POWER<br>AUDIO MUTE<br>MIC ENABLE<br>12.5/25 CON. |
| P4   | P4.0<br>P4.1<br>P4.2<br>P4.3<br>P4.4<br>P4.5 | RX LED<br>TX LED<br>AK2342 RSTN<br>AK2342 STB<br>AK2342 SCLK<br>AK2342 SDATA | P9   | P9.0<br>P9.1<br>P9.2                                         | SQUELCH<br>PTT KEY<br>MONITOR KEY                                                                  |
| P5   | P5.2<br>P5.3                                 | EEROM DATA<br>EEROM CLK                                                      |      |                                                              |                                                                                                    |

### AK2342B

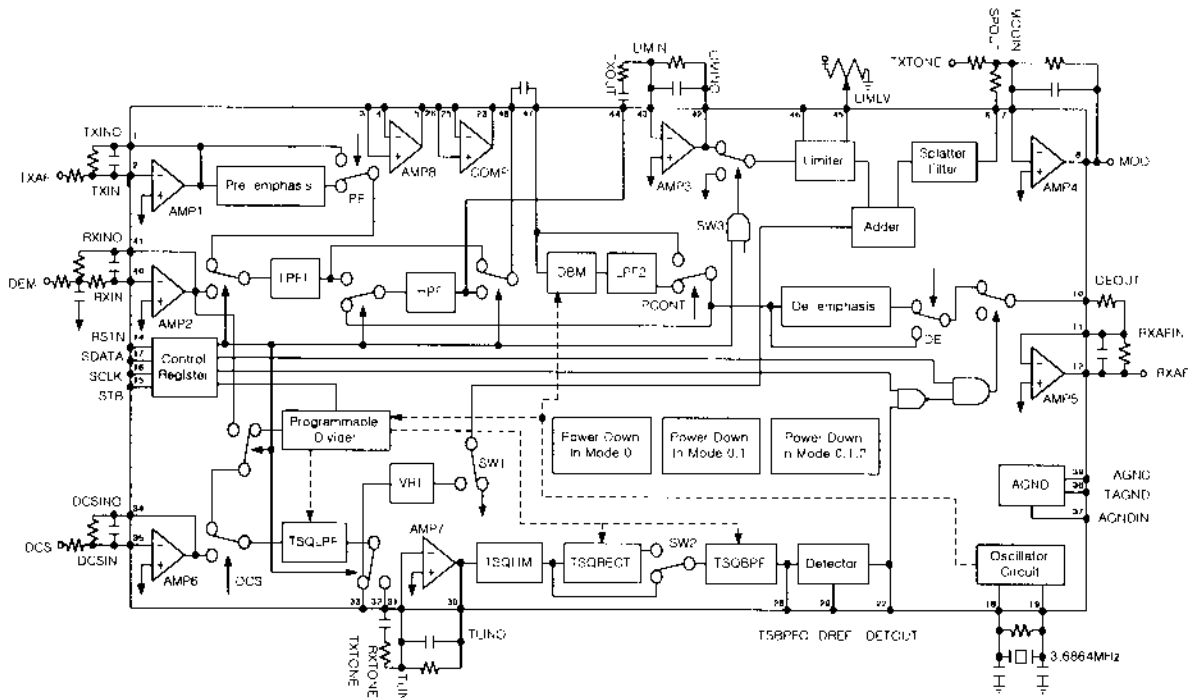
THE AK2342B IS AN IC WHICH SUPPORTS CTCSS (CONTINUOUS TONE CONTROLLED SQUELCH SYSTEM), COMPATIBLE WITH THE EIA STANDARD. ALSO VOICE SIGNAL FILTERS, A LIMITER, OP-AMP, AND OTHER CIRCUITS ARE INTEGRATED, MAKING IT POSSIBLE TO CONFIGURE A RADIO BASE BAND UNIT. THIS CHIP HAS THE VARIOUS MODES ARE SET BY WRITING DATA TO THE CONTROL REGISTER FROM THE SERIAL INTERFACE PINS (SDATA, SCLK, STB). IT MAKES POSSIBLE THAT AUDIO PASS TO FILTER, AMP, OR LIMITER AT EACH RX/TX MODE.

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## AK2342B BLOCK DIAGRAM



### AUDIO AMP.

THE AUDIO AMP AMPLIFIES AN AUDIO SIGNAL FOR A USER TO LISTEN TO THE VOICE, ALSO AMPLIFIES AN ERROR TONE OF EACH FUNCTION. IT IS COMPOSED OF THE VOLUME SWITCH, MUTE SWITCH (Q206), AND AUDIO AMP. (U205).