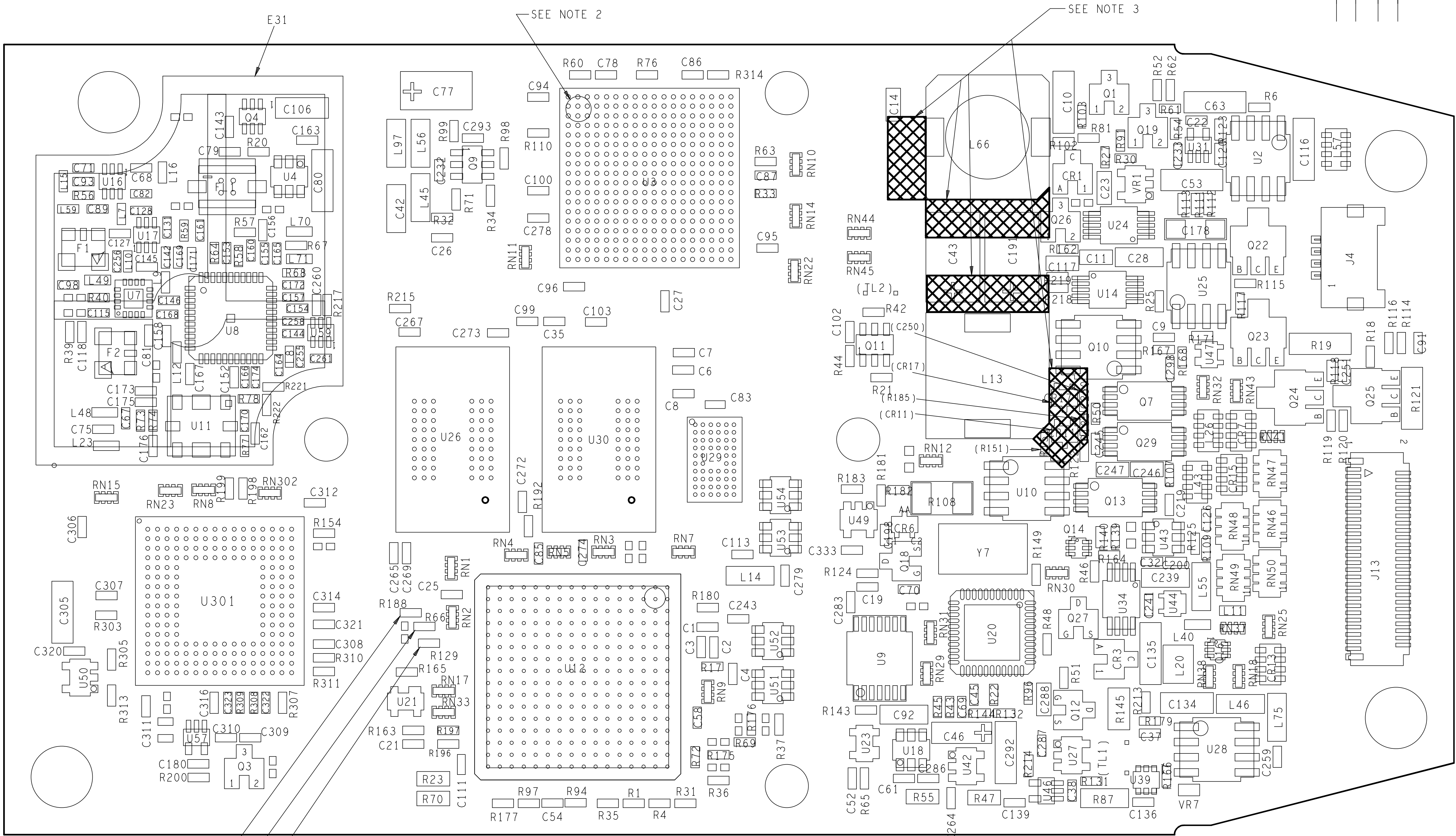


REV	ZONE	DESCRIPTION	E.C.	BY	APVD	DATE
2		RELEASE PER PPD 77251		MJ	RK	9/29/03



PRIMARY SIDE

-01, 02 & -03
ASSEMBLY

NOTES

1. LABEL BOARDS WITH APPROPRIATE PARTS LIST DASH NUMBER AND REVISION USING ITEM 2000. LABEL MUST BE PLACED IN AN AREA THAT WILL NOT AFFECT FUNCTIONALITY AND FIT OF THE BOARD. BOARDS SUPPLIED BY A THIRD PARTY MAY BE LABELED OR STAMPED AT THE DISCRETION OF THE MANUFACTURER. STAMPED MARKING MUST BE .06"-.125" HIGH USING A NON-CONDUCTIVE CONTRASTING EPOXY INK.
2. INDICATES LOCATION OF PIN NUMBER 1.

3. APPLY EPOXY, ITEM 10, AS FOLLOWS:
A) AS INDICATED AROUND L13 AND L66.
B) AS INDICATED AROUND L18.
AS INDICATED AROUND AND OVER L32.

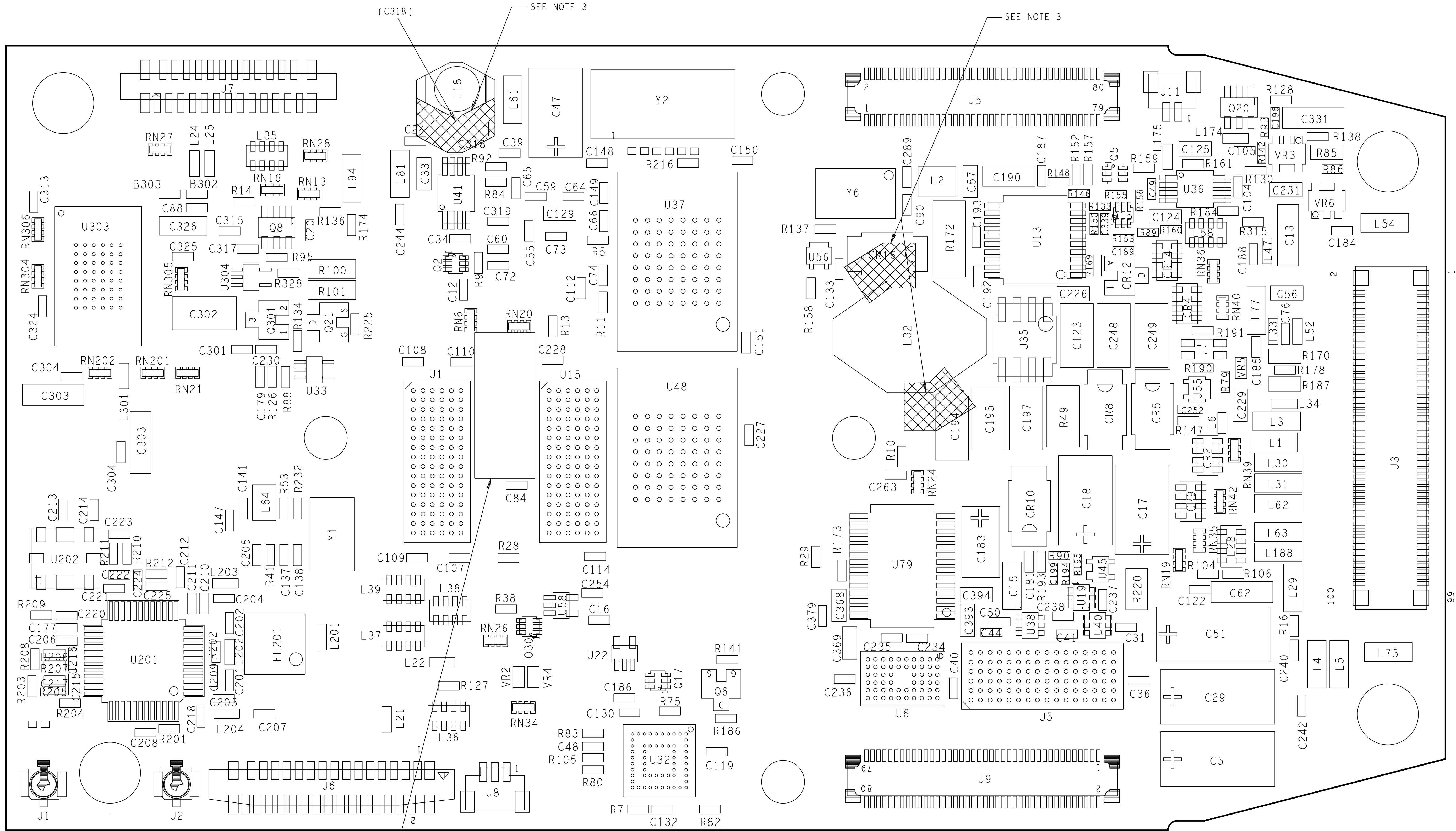
SEE ASSOCIATED P/L 24-64436-01 AND SUBSEQUENT P/L'S.

ITEM	QTY	PART NO.	DESCRIPTION	REMARKS/REF. SYMBOL
PARTS LIST				
		APPROVALS	DATE	SYMBOL TECHNOLOGIES INC. Bohemia, New York
		DRAWN M. JENSEN	27JAN03	
		CHECKED R. KOTSAY		PCB ASSEMBLY: GEMINI CPU
		ENGINEER D. DEMEO		
		MFG. ENG. P. JASSO		SIZE DWG. NO. 24-64436-XX
		PRODUCT M. HORAN		
		QUALITY X		REV. 2
		TEST ENG. D. REED		SCALE: NONE
				SHEET 1 of 4

THE DRAWING AND SPECIFICATION CONTAINED HEREON ARE PROPRIETARY AND MUST NOT BE USED, COPIED, REPRODUCED OR OTHERWISE DEALT WITH NOR ITS CONTENTS COMMUNICATED TO OTHERS EXCEPT IN ACCORDANCE WITH WRITTEN INSTRUCTIONS RECEIVED FROM SYMBOL TECHNOLOGIES INC.	
DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED	
XX +/- MM	INCH
XXX +/-	01
XXX +/-	.005
ANGLES ± FRACTIONS ±	
MATERIAL:	
FINISH:	
DO NOT SCALE DRAWING	

NEXT ASSY	USED ON
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REV	ZONE	DESCRIPTION	E.C.	BY	APVD	DATE
-		SEE SHEET 1				



SECONDARY SIDE

-01, -02 & -03
ASSEMBLY

SIZE	DWG. NO.	REV.
D	24-64436-XX	2
SCALE	ONE	SHEET 2 OF 4

