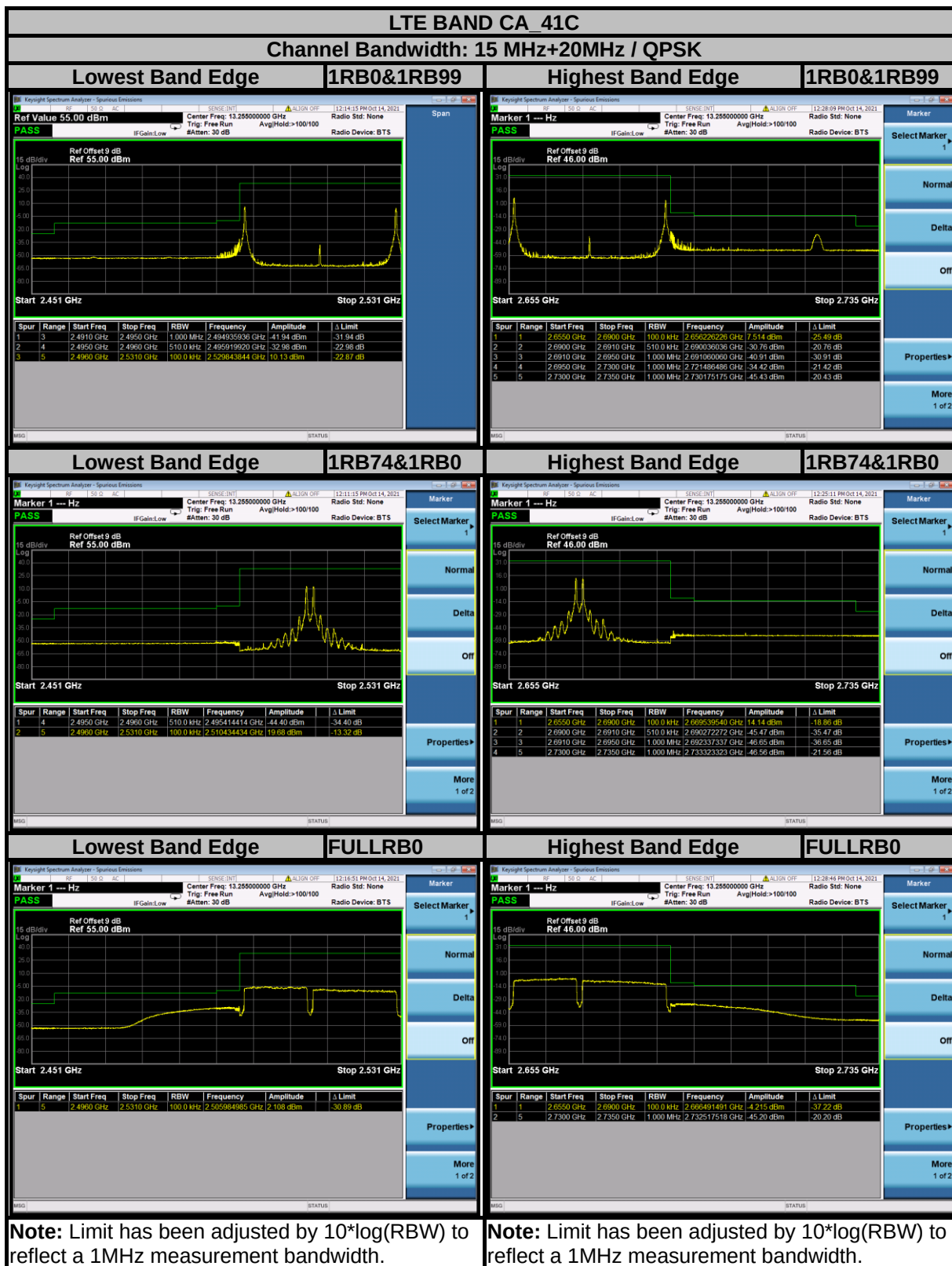
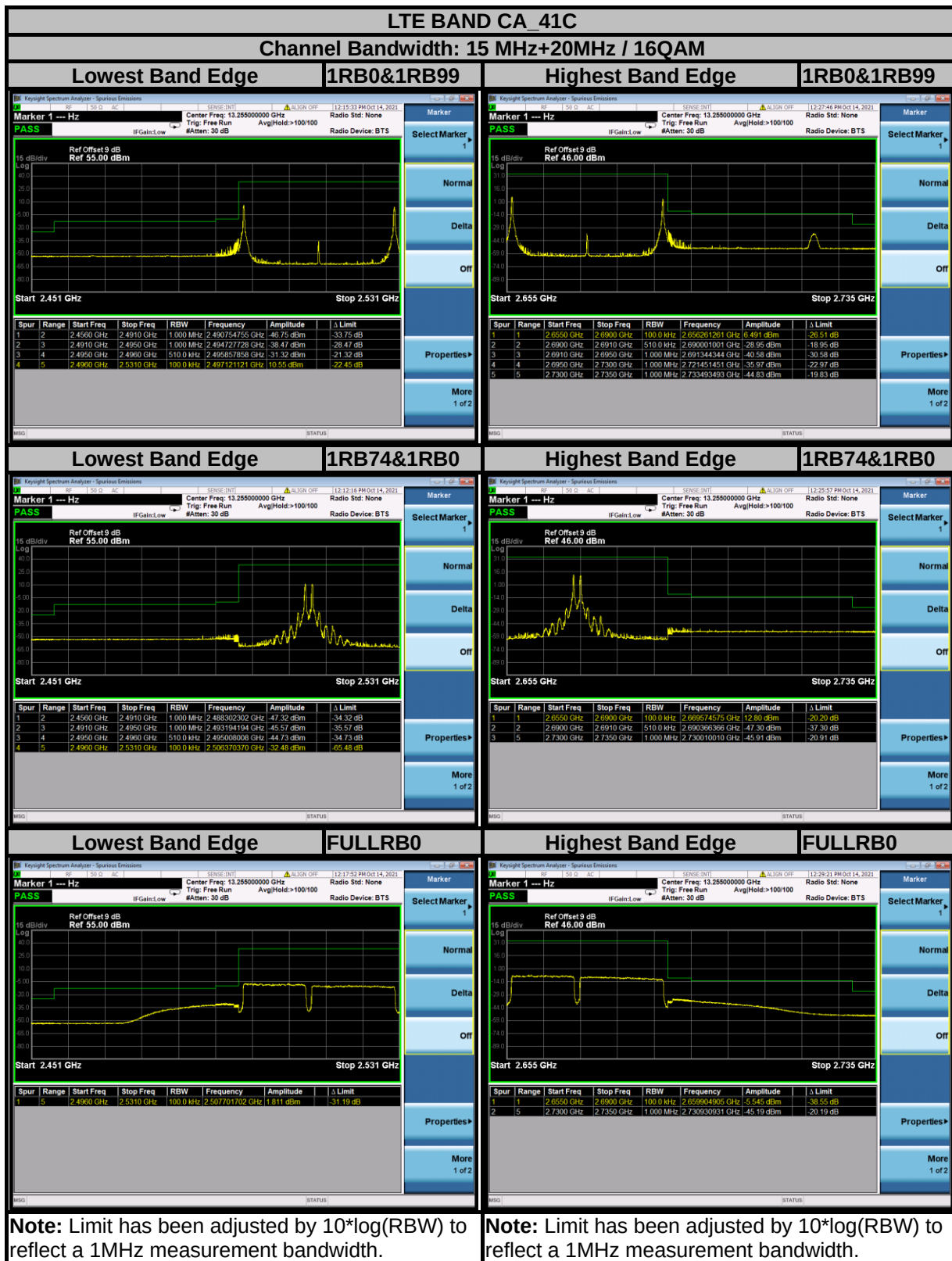
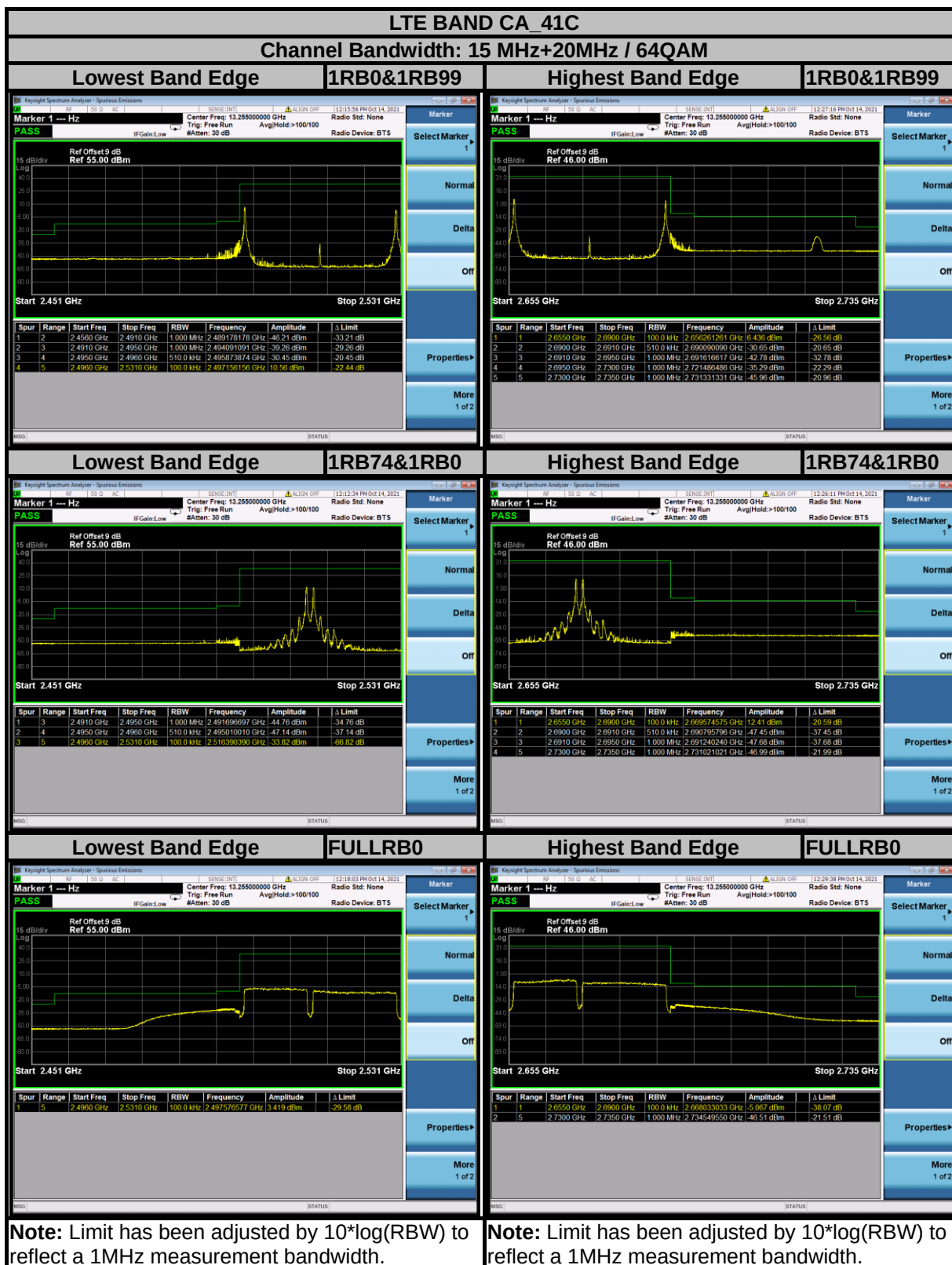
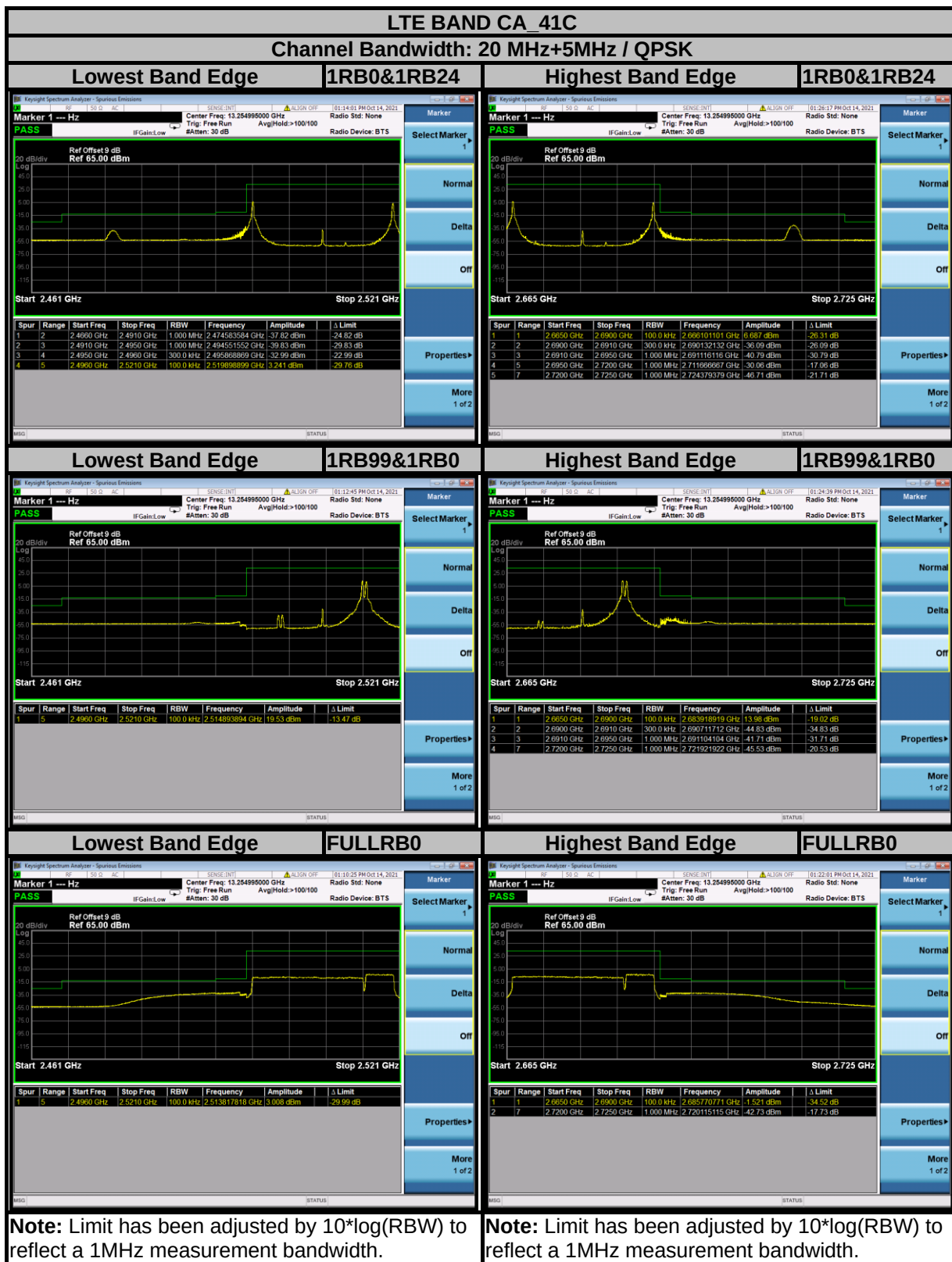
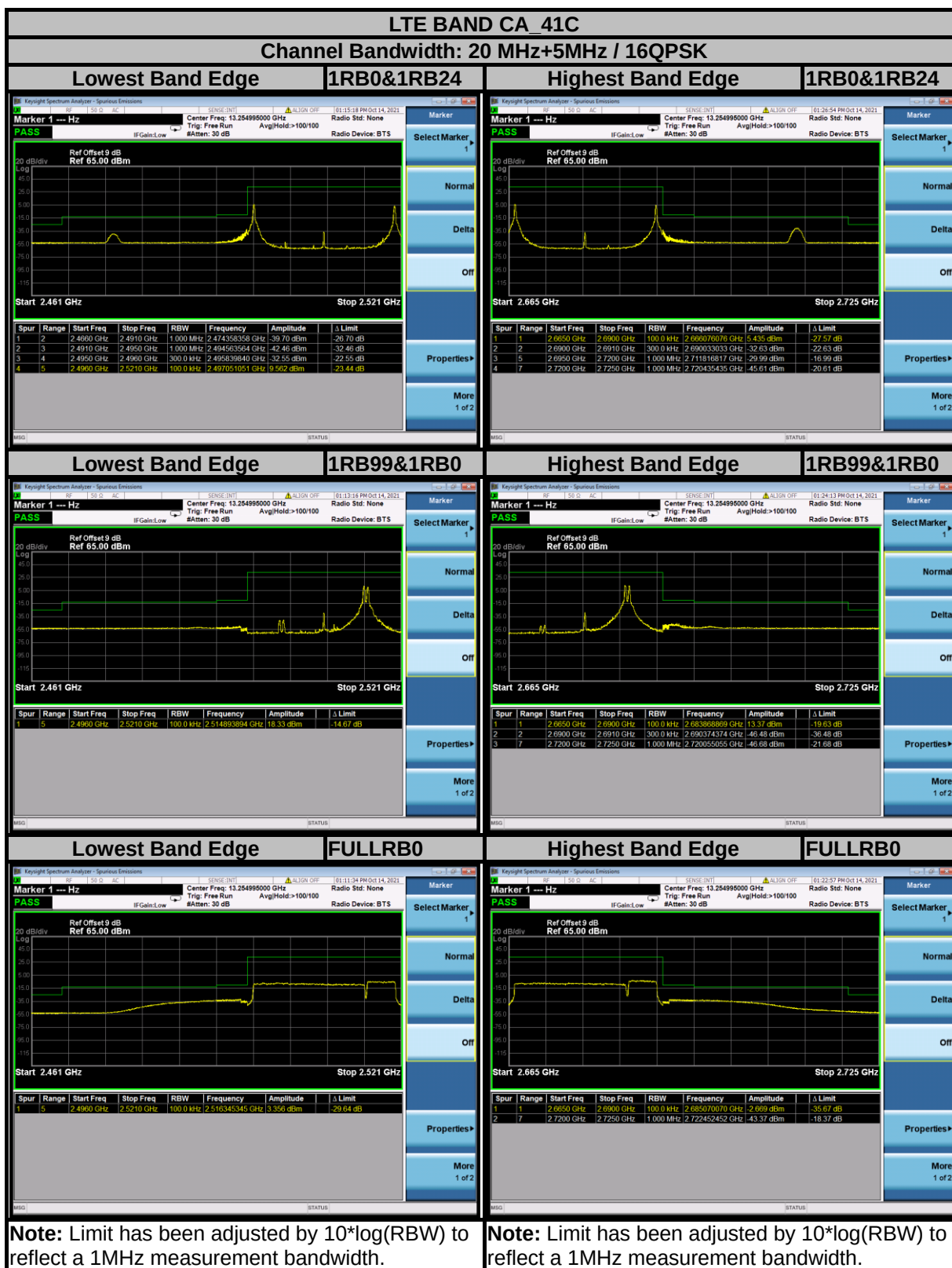


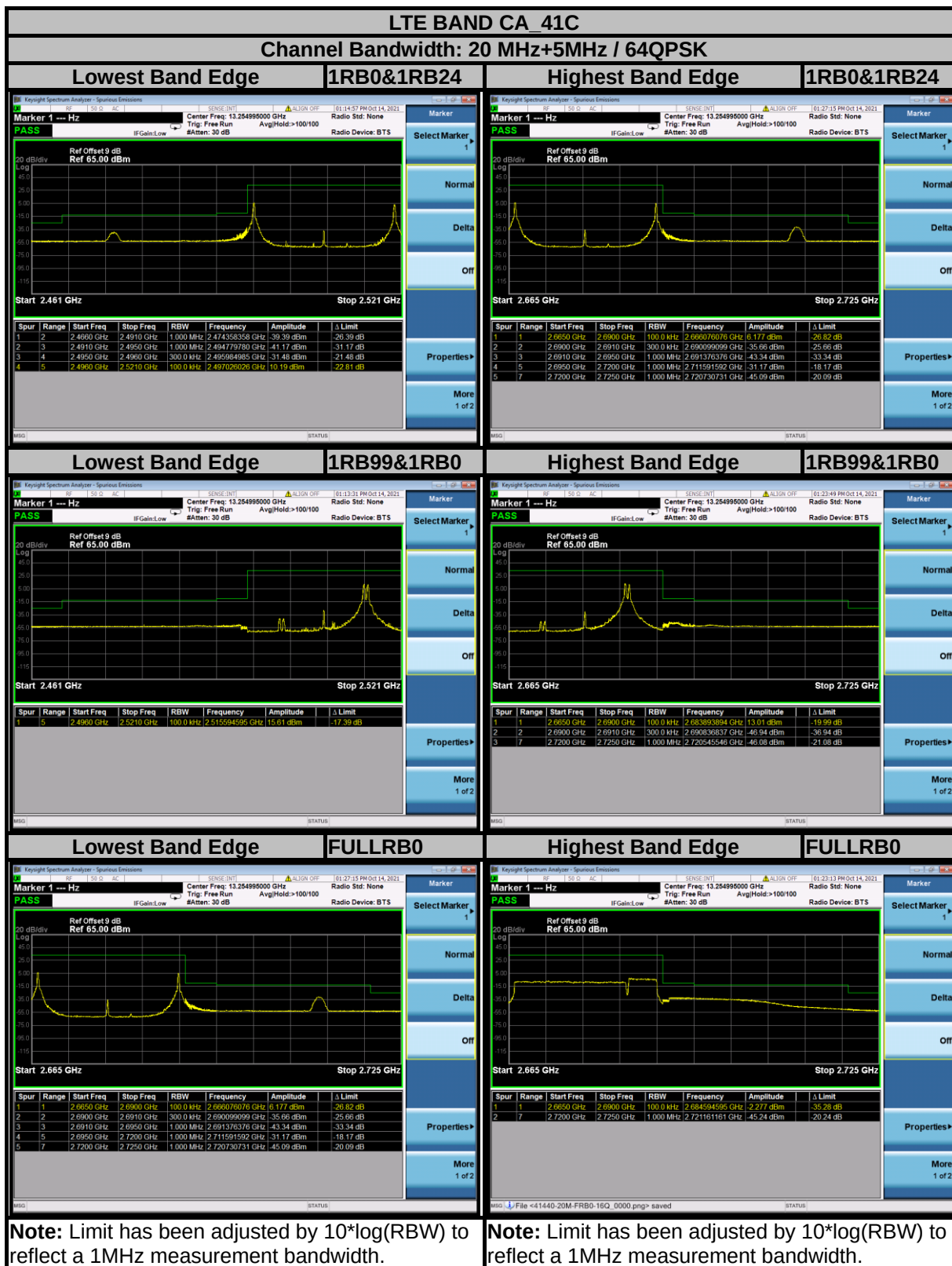


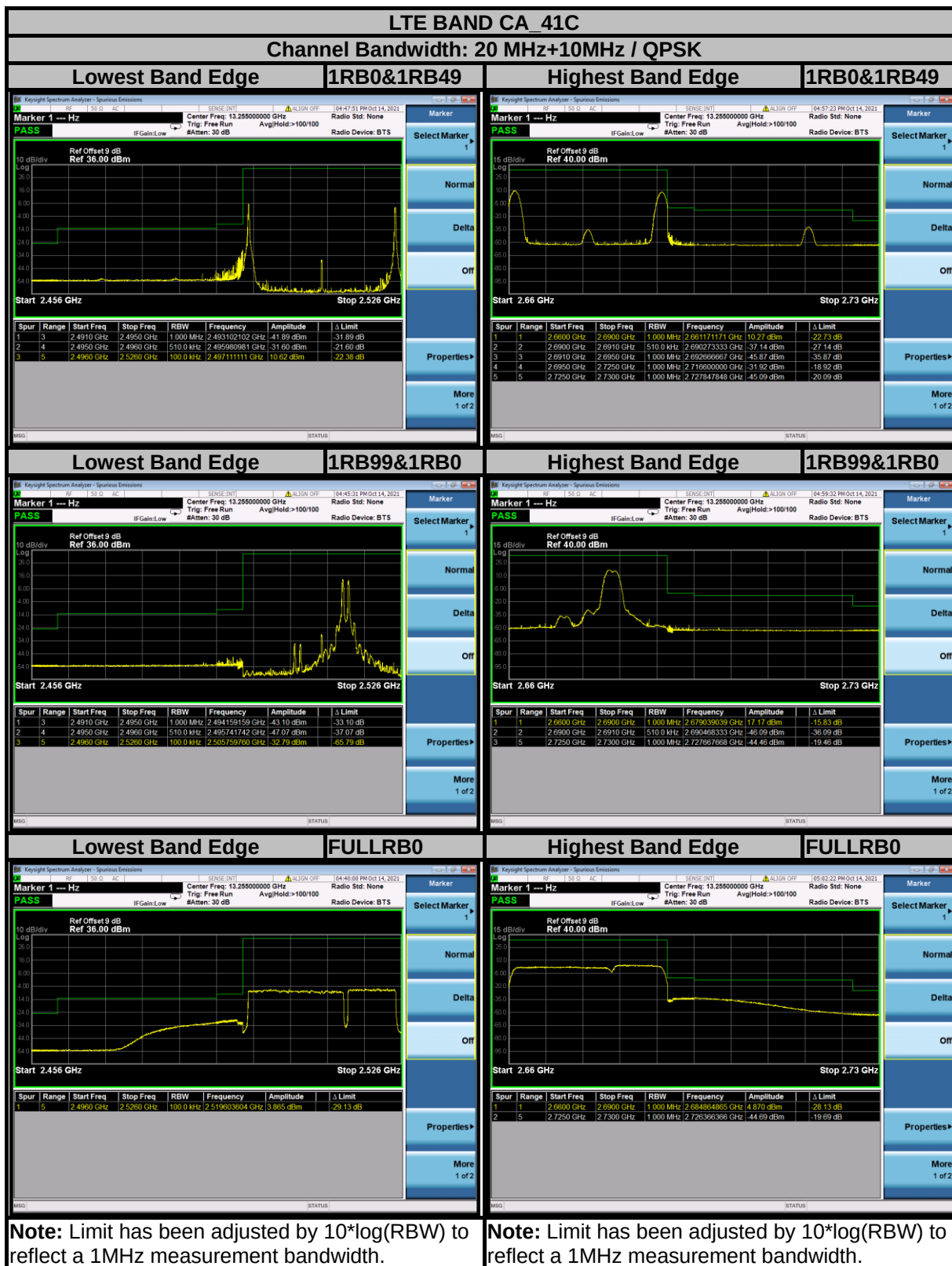








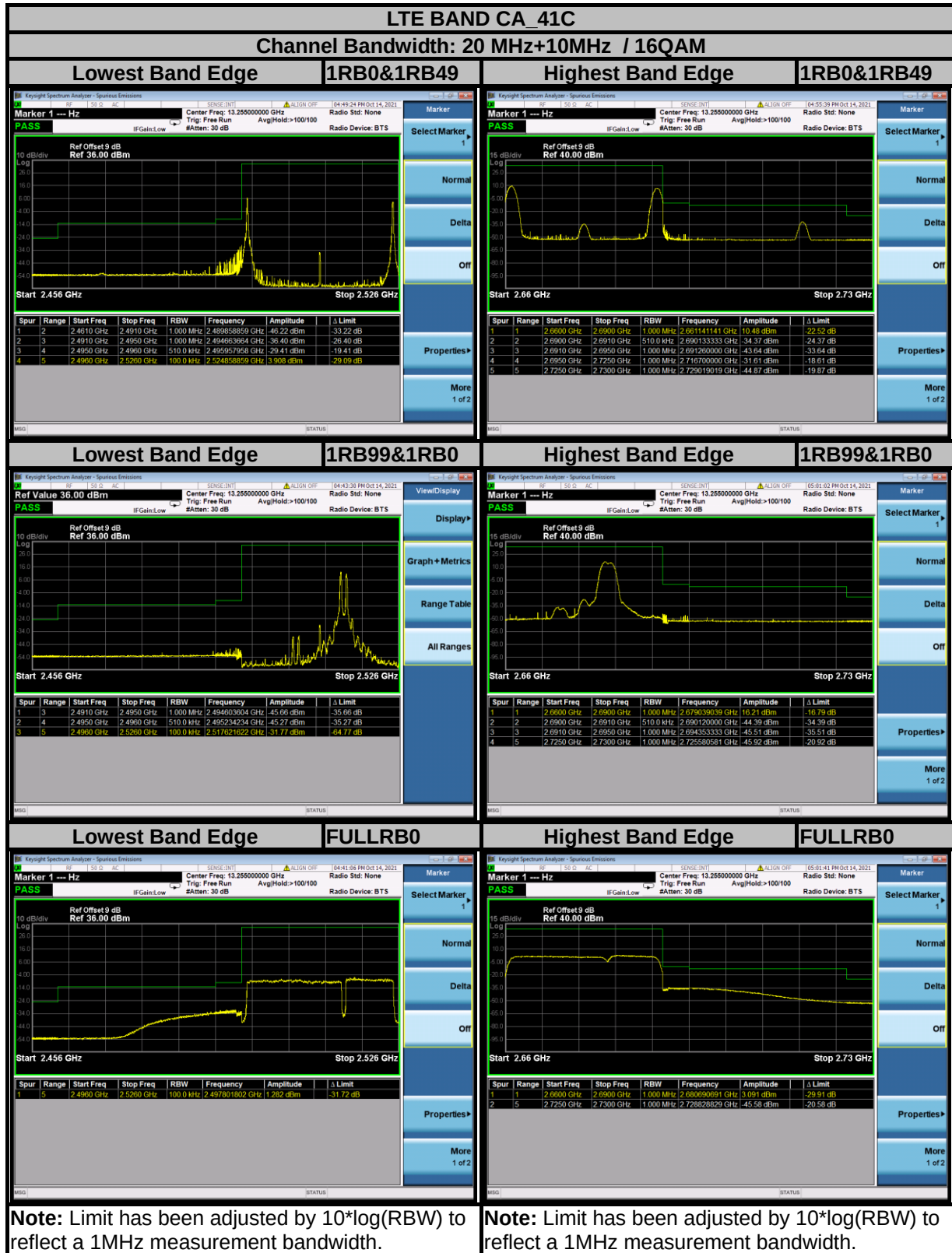


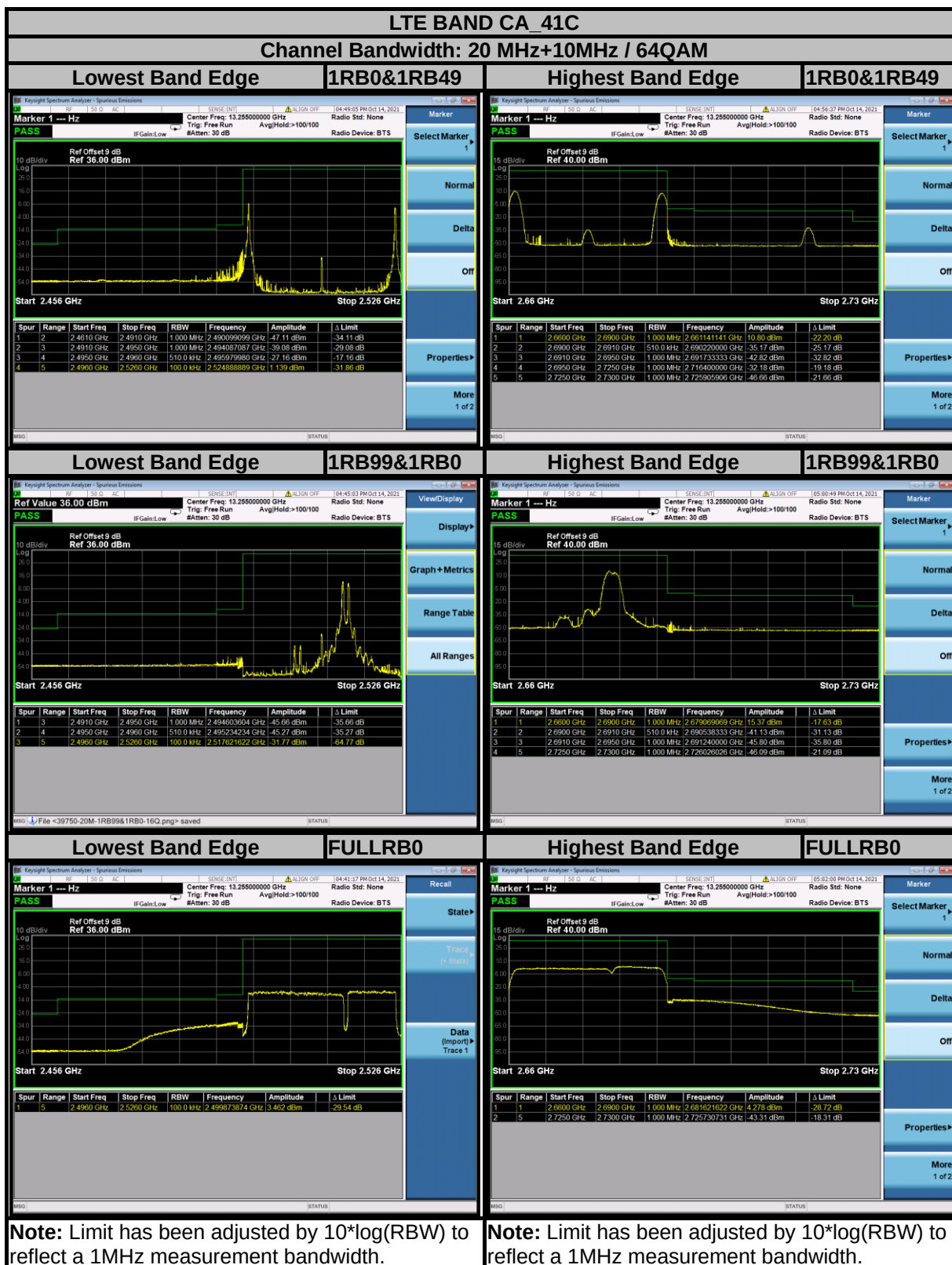


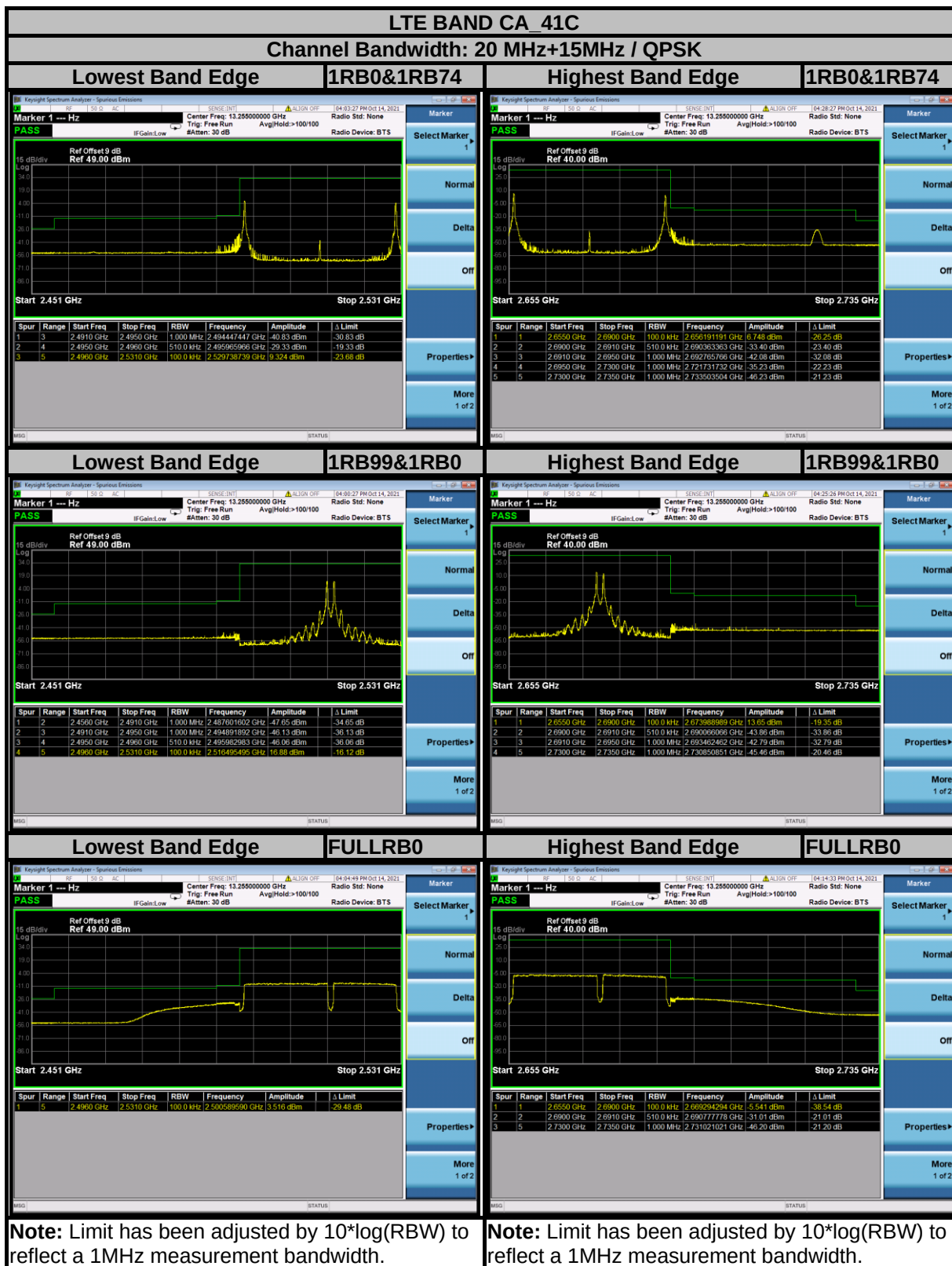


BUREAU
VERITAS

Test Report No.: W7L-P21110008RF17







Note: Limit has been adjusted by $10 \cdot \log(\text{RBW})$ to reflect a 1MHz measurement bandwidth.

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