

Federal Communications Commission
Authorization and Evaluation Division
Laboratory Division
7435 Oakland Mills Rd.
Columbia, MD 21046

RE: Letter defining design variants for Class II Permissive Change
ADTRAN, Inc. (FCC ID: **HDCTRACERT1L7**)

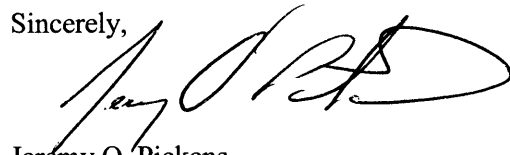
Dear Sir or Madam:

Please find below a description submitted by ADTRAN, Inc. outlining the design changes to the initial Tracer system, which required the submittal of a Class II Permissive Change.

The Tracer 10BaseT is a design variant of the Adtran Tracer 2xT1 baseband processor. The 2xT1 design features dual T1 interfaces with Dallas Semiconductor DS2151 framers, while the 10BaseT design features one Dallas DS2152 and a Dallas DS2141. The DS2152 is the interface to the T1 port on the 10BaseT unit, while the DS2141 interfaces to the router module. The router module is a design variant of the Adtran Express L1.5 HDSL router. Data from the DS2141 framer is sent to a Motorola 68EN360 processor and associated support hardware (ram, rom, nonvolatile timekeeper, etc.), where the routing functions are performed in software. Data is converted to Ethernet format and sent out the 10BaseT interface via a Level One LXT905 universal 10BaseT transceiver.

Please feel free to contact me with any questions.

Sincerely,



Jeremy O. Pickens
Senior Project Engineer