

Smart link Modem/Intersil 802.11 Combo Mini PCI Reference Board

P02: MINI\_PCI & ISL3874

ID0B  
AC\_RESET#  
SYNC  
SDATA\_OUT  
BIT\_CLK  
SDATA\_INA  
ID1B  
MONO\_PHONE  
  
TIP  
RING

LED1\_GRNN  
LED1  
LED2  
LED2\_OUT  
LED3  
GRESET  
SCLK  
SDATA  
MA[17..0]  
MD[15..0]  
MEM\_CTRL[5..0]

LED1\_GRNN  
LED1  
LED2  
LED2\_OUT  
LED3  
GRESET  
SCLK  
SDATA  
MA[17..0]  
MD[15..0]  
MEM\_CTRL[5..0]

P04: MEMORY

LED1\_GRNN  
LED1  
LED2  
LED2\_OUT  
LED3  
GRESET  
SCLK  
SDATA  
MA[17..0]  
MD[15..0]  
MEM\_CTRL[5..0]

P05: HFA3783

IF\_DET  
CAL\_EN  
RADIO\_PE  
TX\_IF\_AGC  
RX\_IF\_AGC  
LE\_IF  
44MHZ\_REF  
RXI+  
RXI-  
RXQ+  
RXQ-  
TXI+  
TXI-  
VREF  
TXQ+  
TXQ-  
SCLK\_RF\_IF  
SD\_RF\_IF  
PE1  
PE2

IF\_DET  
CAL\_EN  
RADIO\_PE  
TX\_IF\_AGC  
RX\_IF\_AGC  
LE\_IF  
44MHZ\_REF  
RXI+  
RXI-  
RXQ+  
RXQ-  
TXI+  
TXI-  
VREF  
TXQ+  
TXQ-  
SCLK\_RF\_IF  
SD\_RF\_IF  
PE1  
PE2

IF\_DET  
CAL\_EN  
RADIO\_PE  
TX\_IF\_AGC  
RX\_IF\_AGC  
LE\_IF  
44MHZ\_REF  
RXI+  
RXI-  
RXQ+  
RXQ-  
TXI+  
TXI-  
VREF  
TXQ+  
TXQ-  
SCLK\_RF\_IF  
SD\_RF\_IF  
PE1  
PE2

P06: ISL3685 & ISL3984

RX\_RF\_AGC  
LE\_RF  
PA\_PE  
TX\_DET  
T/R\_SW  
T/R\_SW#  
ANTSEL  
ANTSEL#

RX\_RF\_AGC  
LE\_RF  
PA\_PE  
TX\_DET  
T/R\_SW  
T/R\_SW#  
ANTSEL  
ANTSEL#

PE2  
PE1  
SD\_RF\_IF  
SCLK\_RF\_IF  
44MHZ\_CW  
IF+  
IF-

44MHZ\_CW  
IF+  
IF-

44MHZ\_CW  
IF+  
IF-

Revision History

| REV | Description     | Date    |
|-----|-----------------|---------|
| 0A  | Initial Release | 2/1/02  |
| 0B  | ADD MCDR        | 2/28/02 |
| 01  | Release         | 3/1/02  |
|     |                 |         |

|         |                           |              |
|---------|---------------------------|--------------|
| MINIPC1 |                           |              |
| Size C  | Document Number WN3101A   | Rev 01A      |
| Date    | Friday, December 06, 2002 | Sheet 1 of 6 |







