

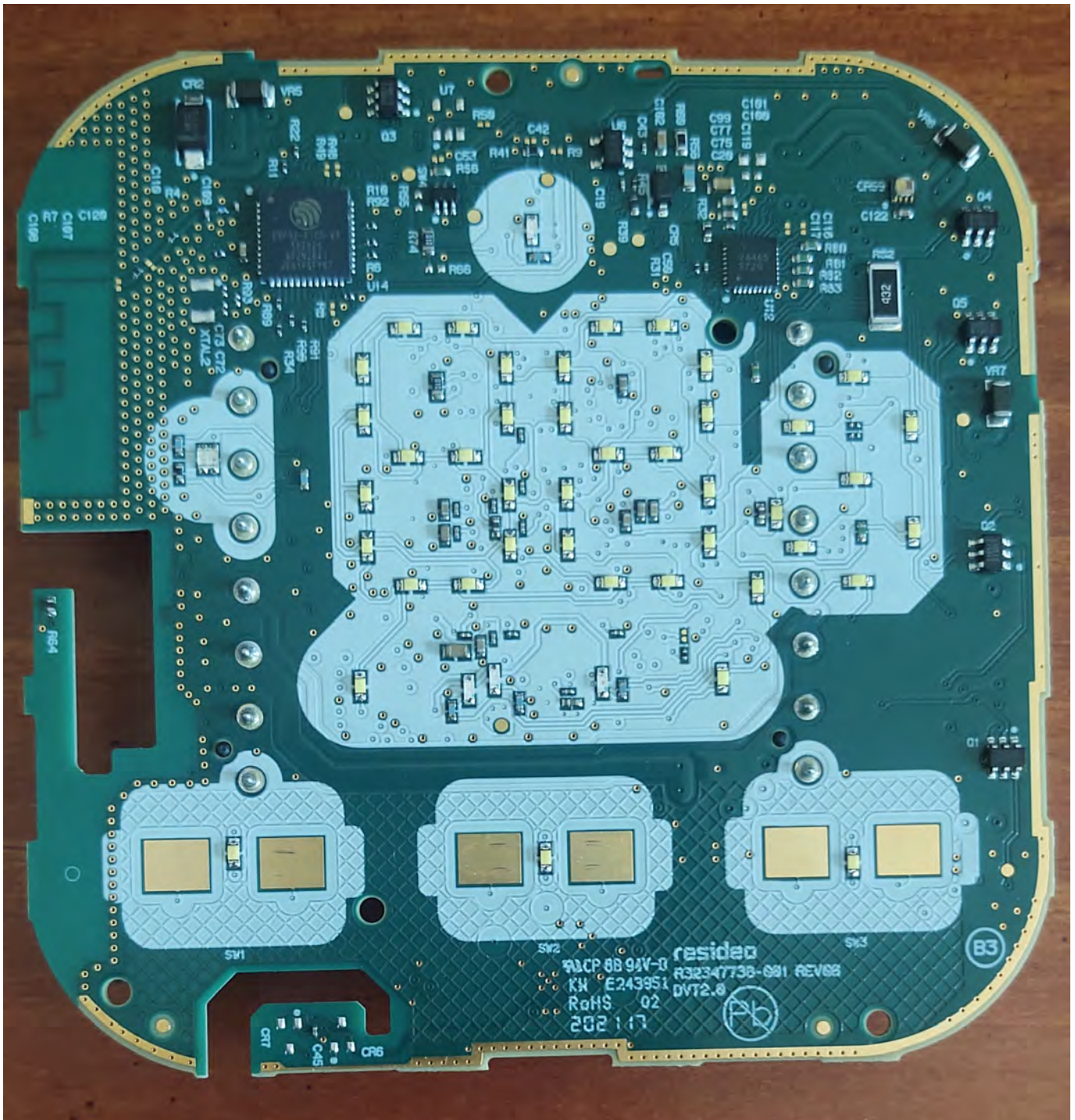


Figure 1. Top Layer.

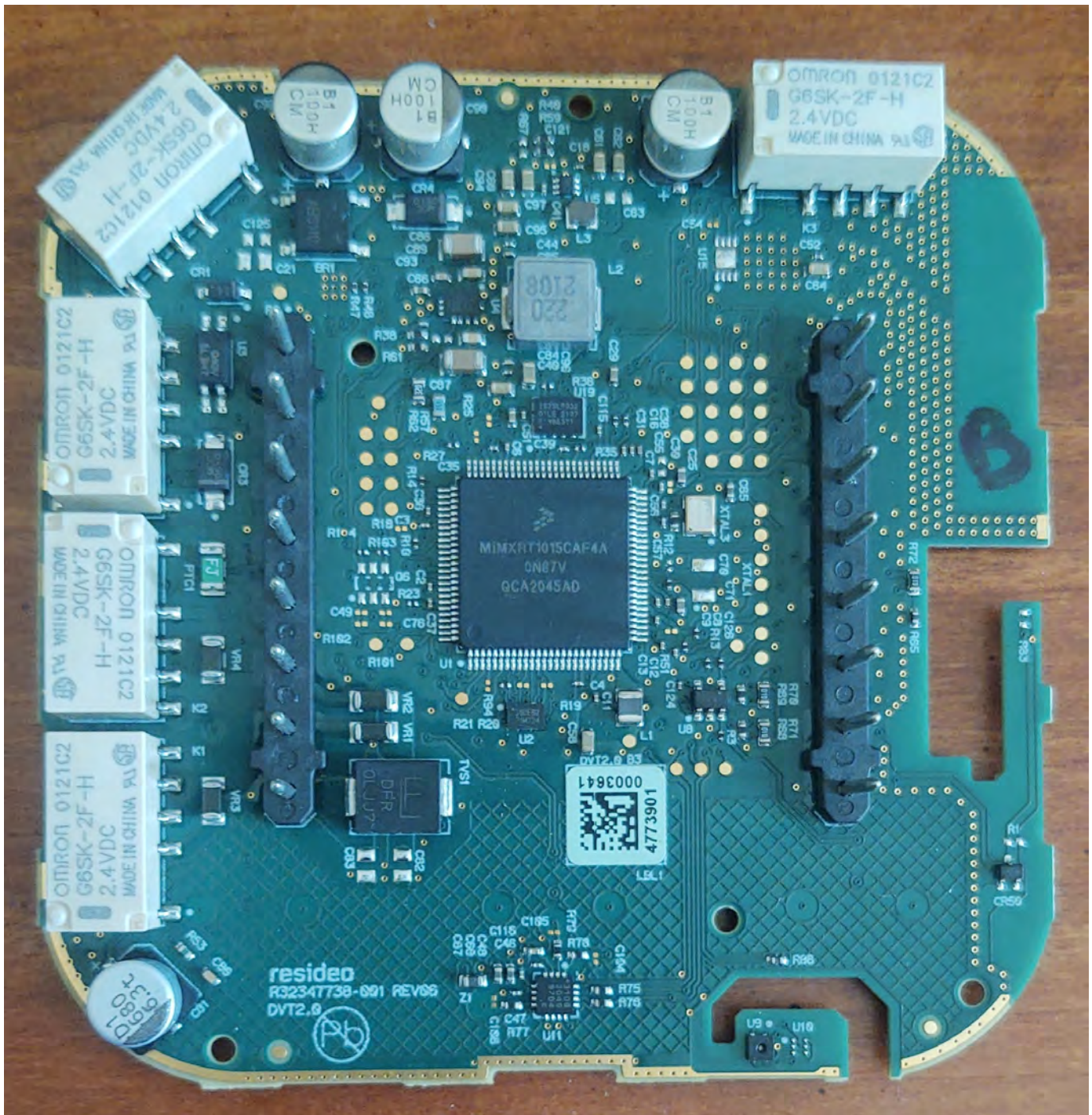


Figure 2. Bottom Layer.

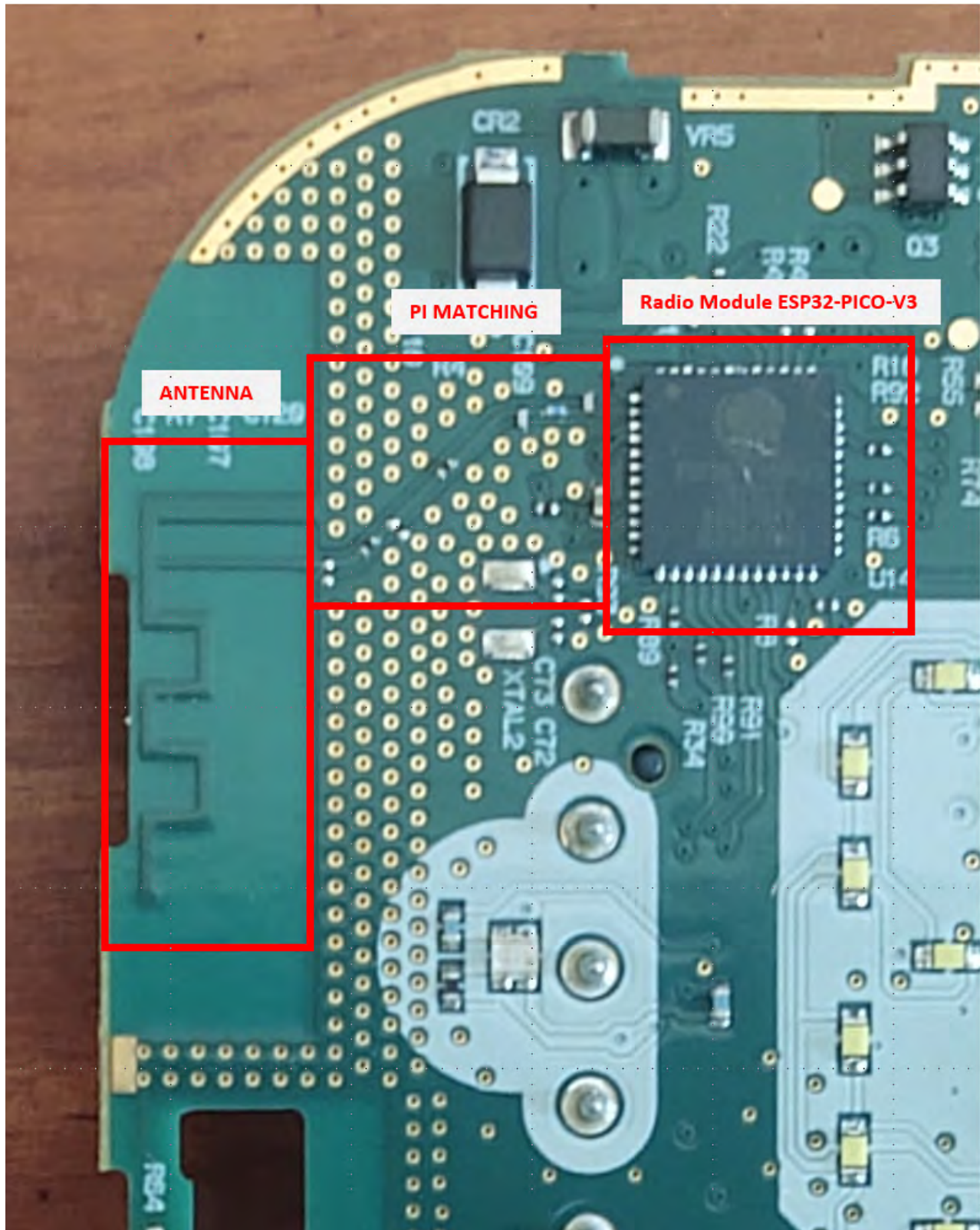


Figure 3. RF Section.

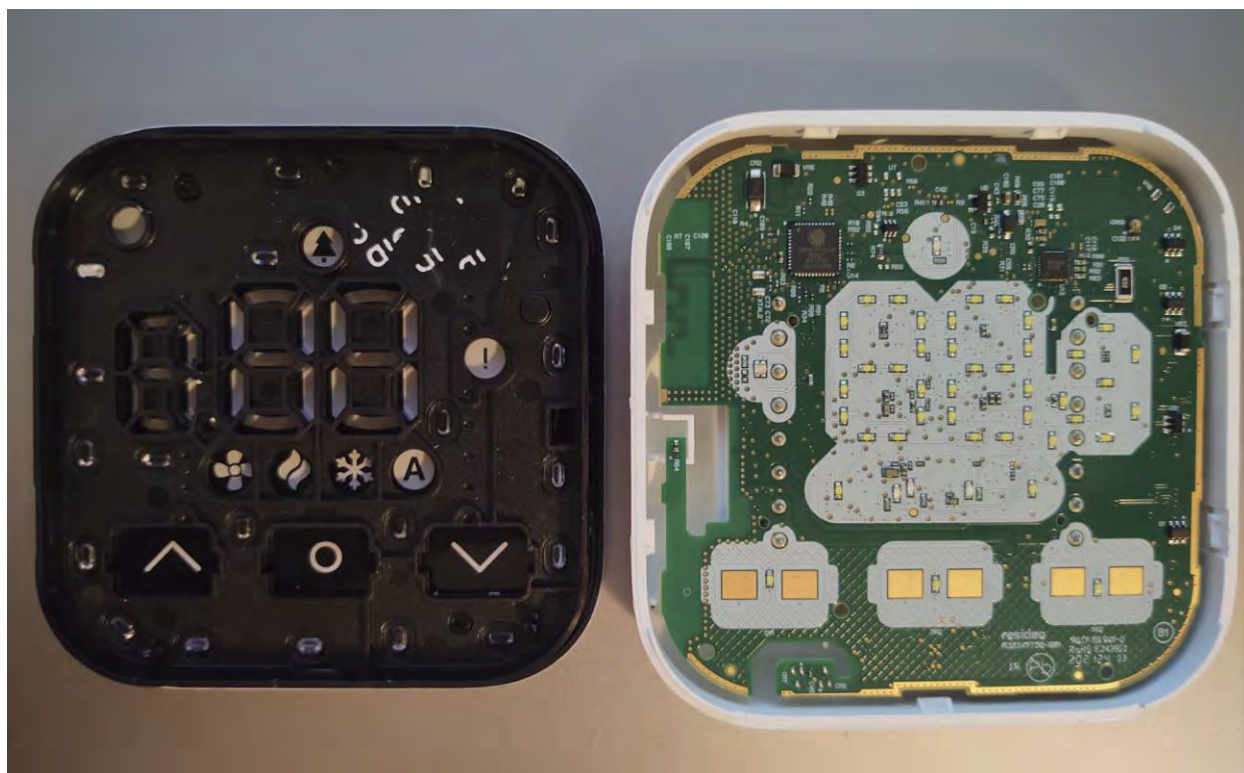


Figure 4. Internal.



Figure 5. Back.

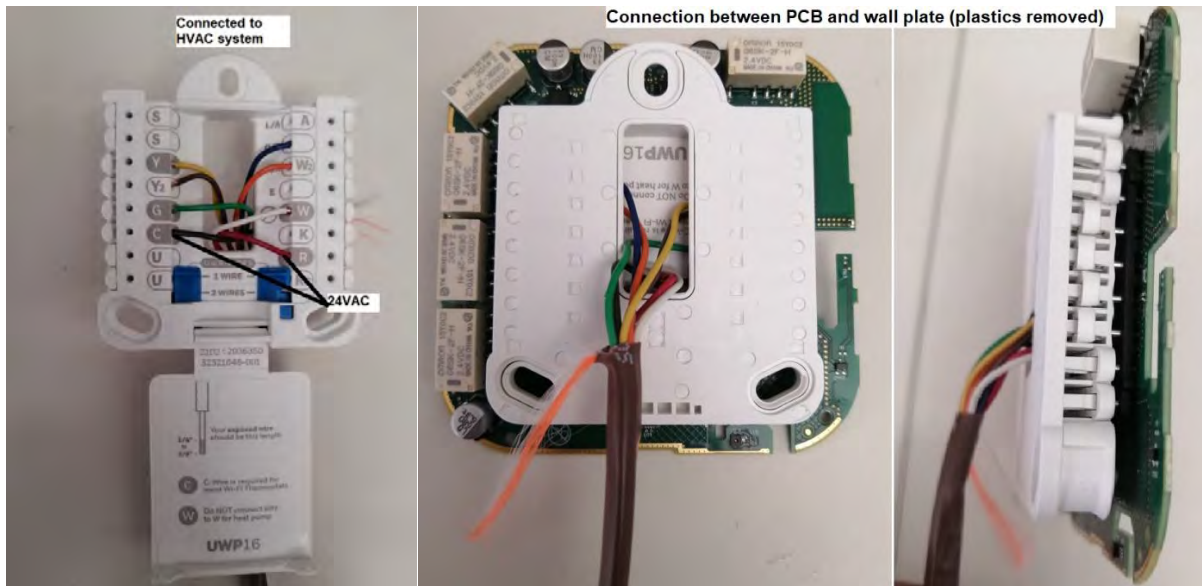


Figure 6. Wall plate and PCB connection.



Figure 7. Full construction.