Exhibit D Hardware description and Block Diagram

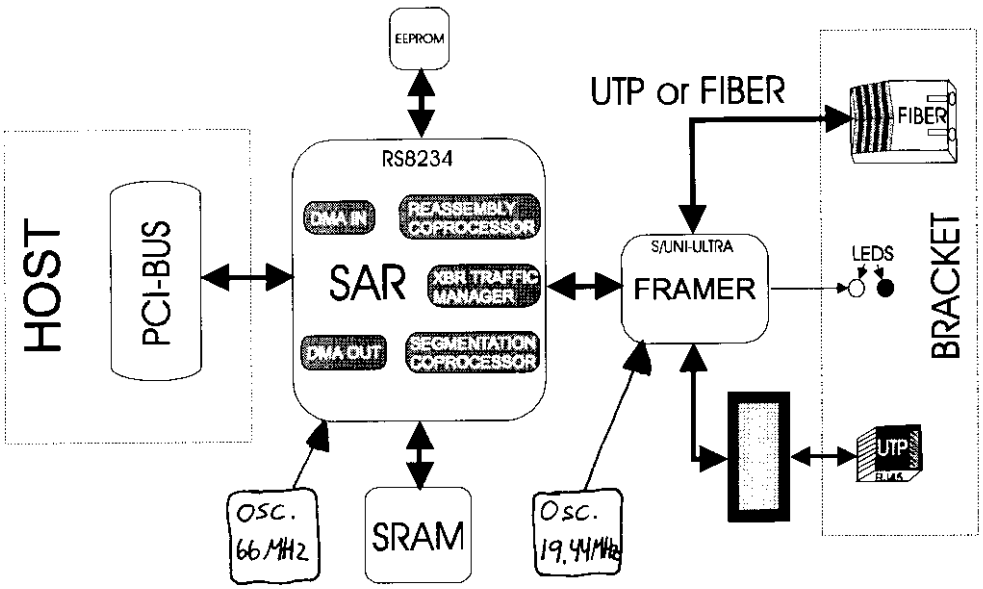

Figure 1

The following text describes the functionality of RF6161 and RF6162.

The SAR contains five coprocessors and is designed to control host traffic.

The Framer is the transmitting part that provides an interface to one of the two plugs, UTP or PECL. In the UTP direction a coupling transformer is used towards the RJ45 plug. The FRAMER receives ATM frames via a bit serial interface, recovers clock and data, and processes section, line and path overhead and convert the serial bit stream to a byte wide parallel bit stream which is transmitted to the SAR.

Two hardware pins are connected with 2 LED's, which are placed on the bracket.
One LED is indicating "Loss of signal", and the other "lock detect", when the receiver has synchronizes with the transmitter.