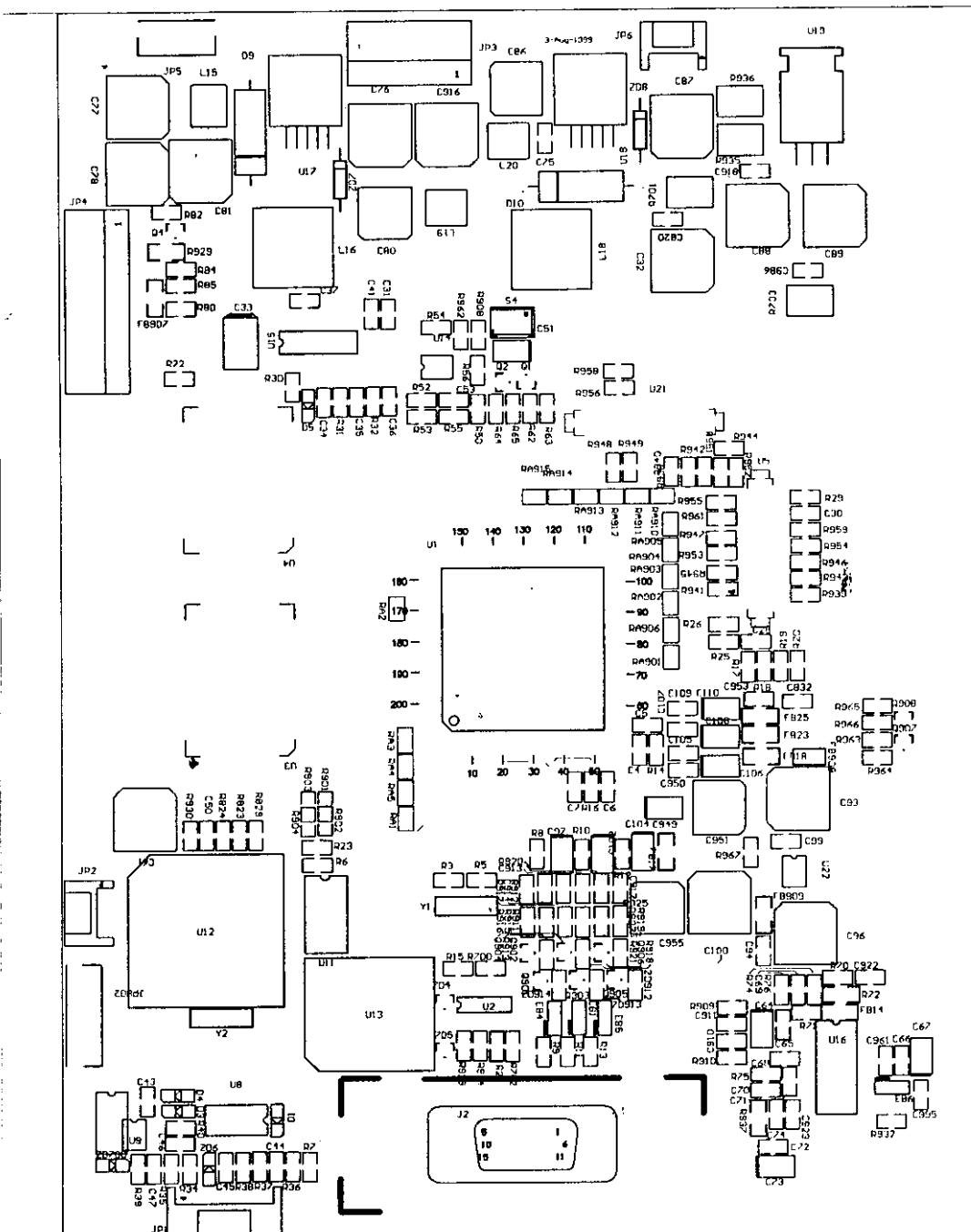


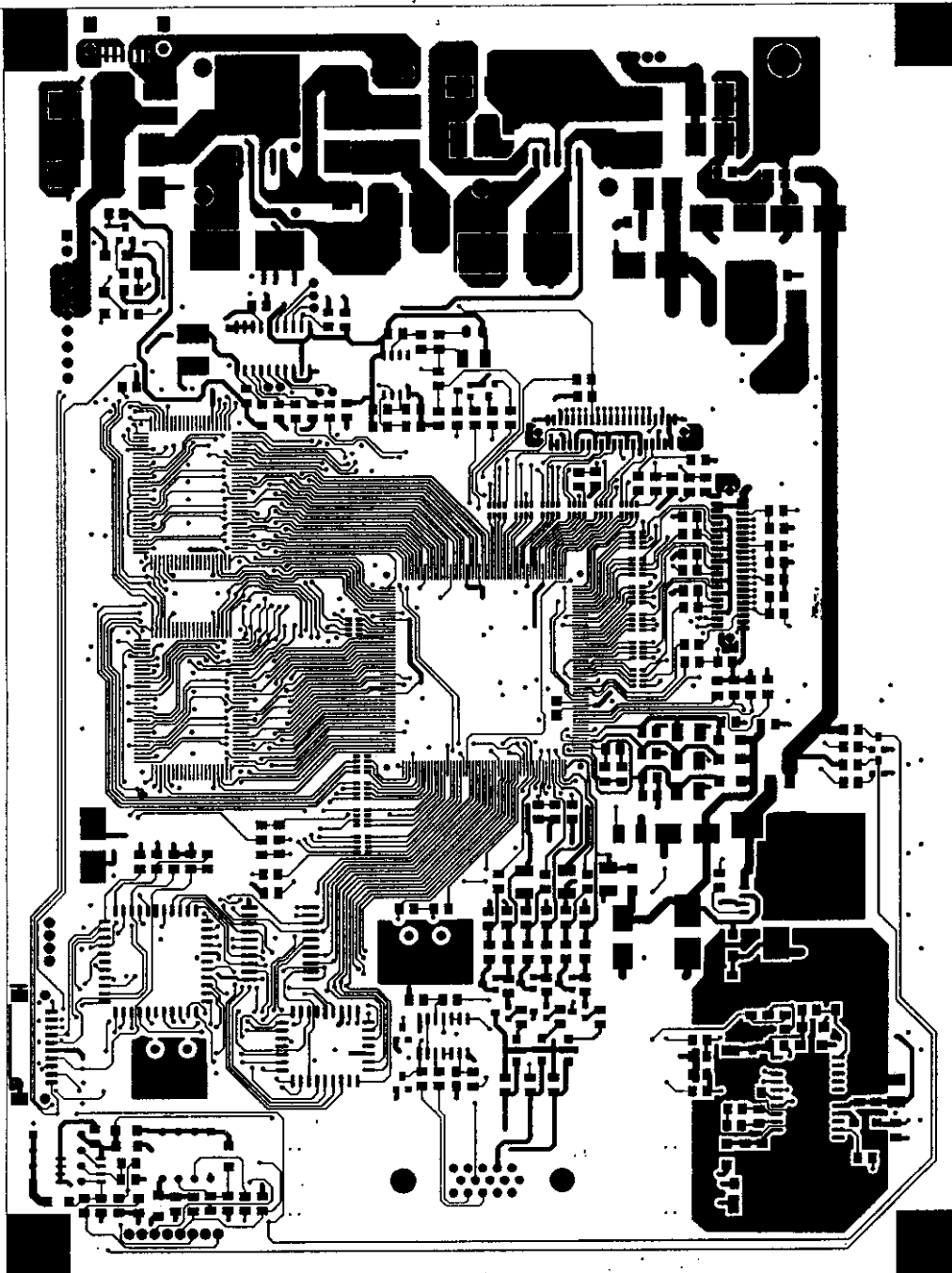


3-Aug-1999

U-CUT

MAY 17, 1999  
R-5 MAIN BOARD REV:1.1

FCC ID: HSUTRLR-5

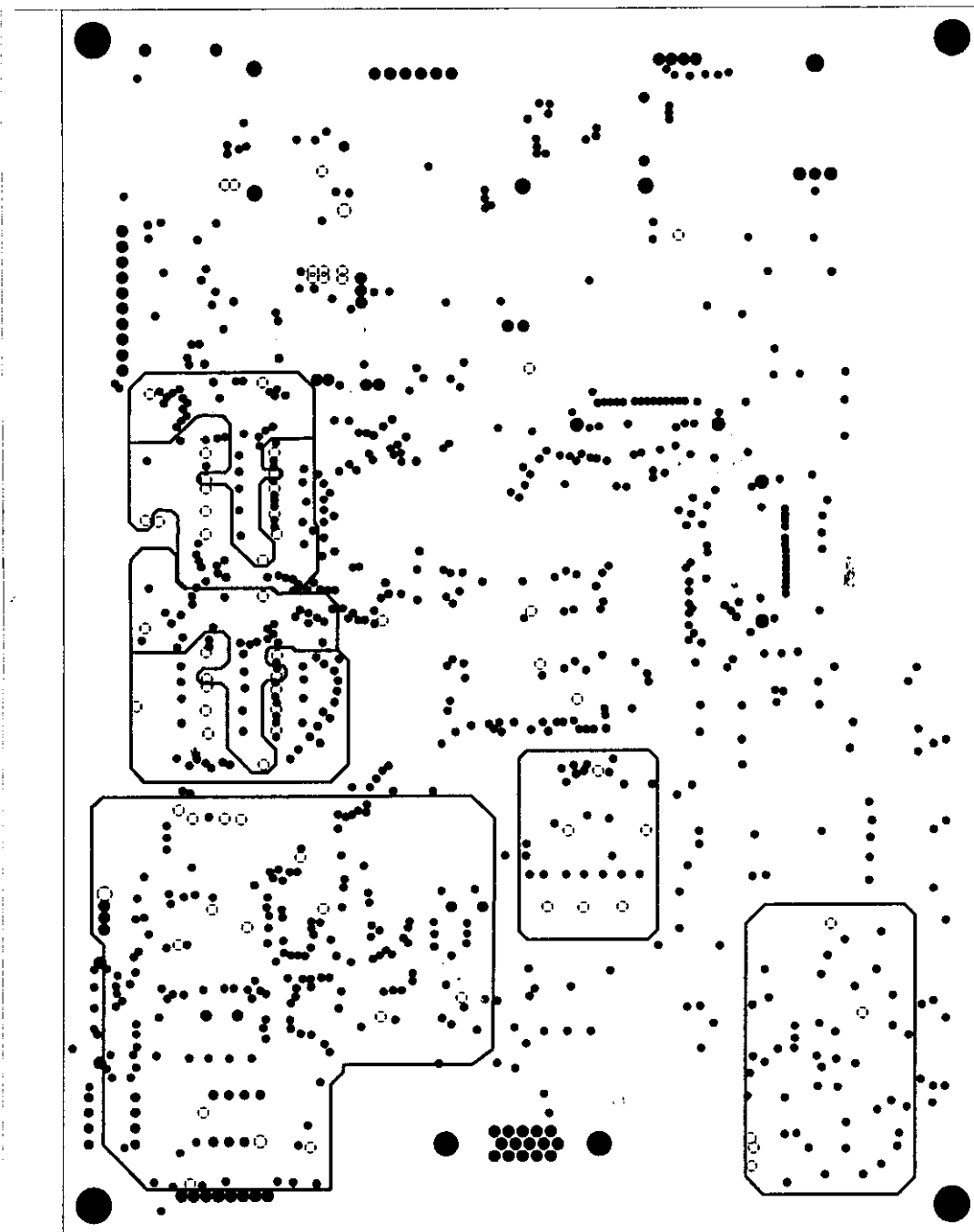


U-CUT

Top Layer

1

3-Aug-1993

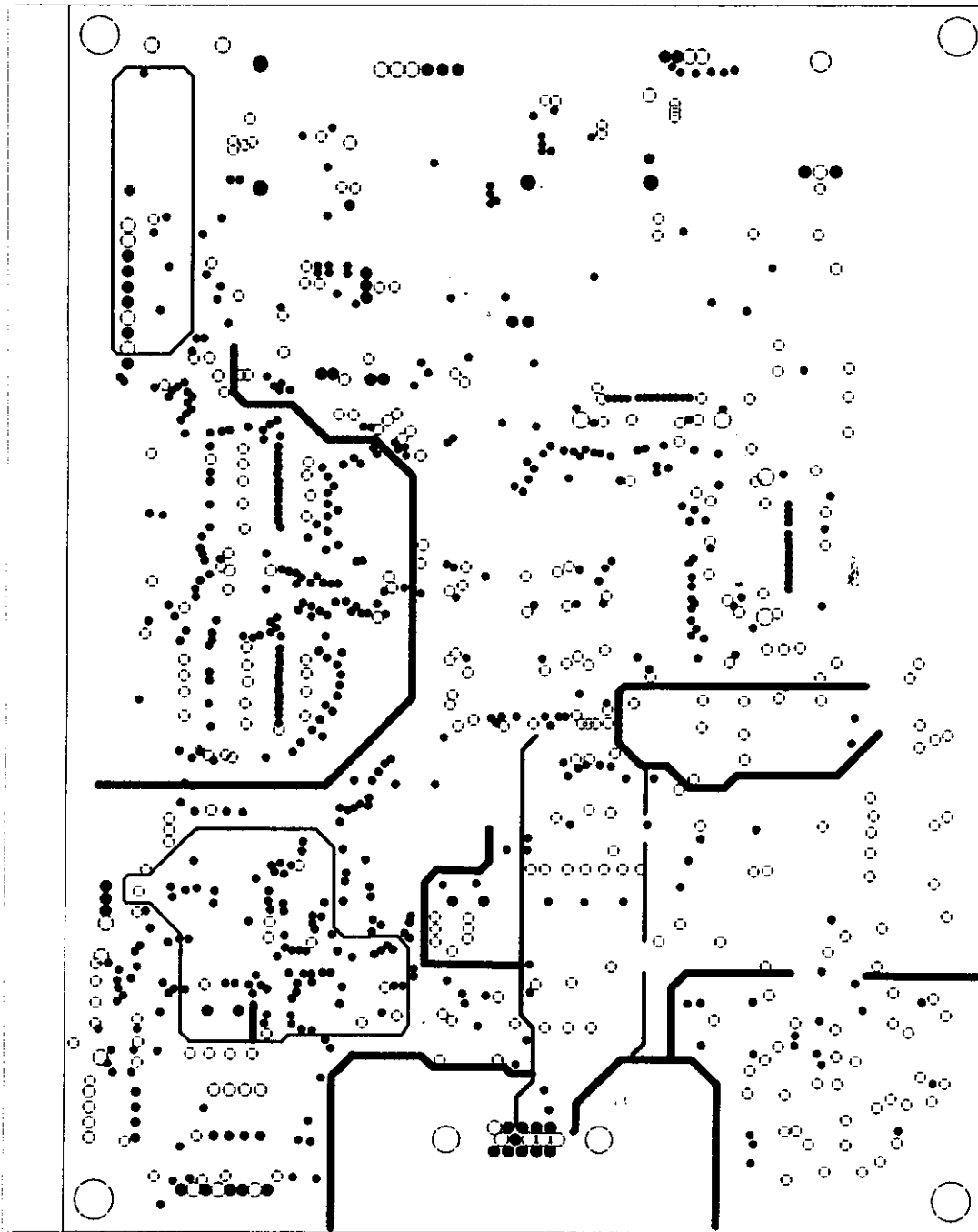


V-CUT

Internal Plane 2

3

3-Aug-1999

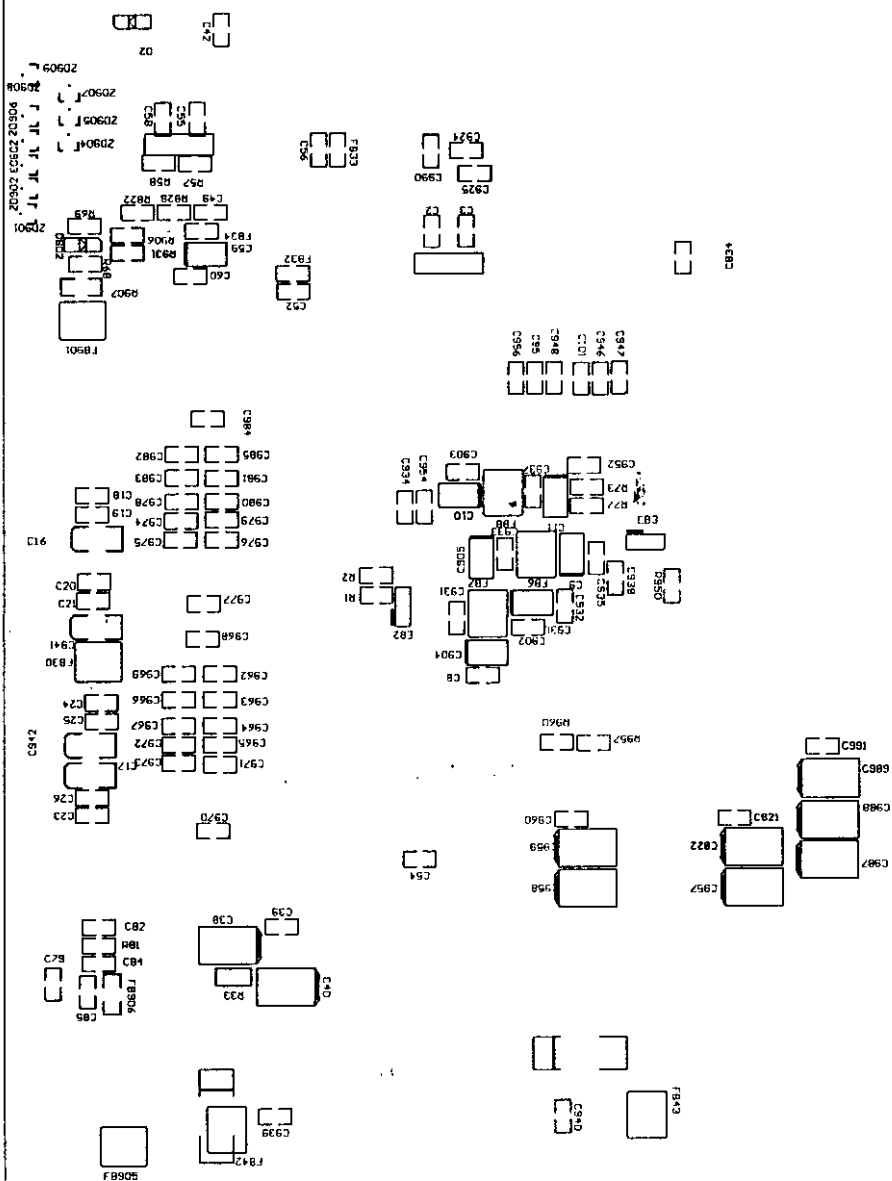


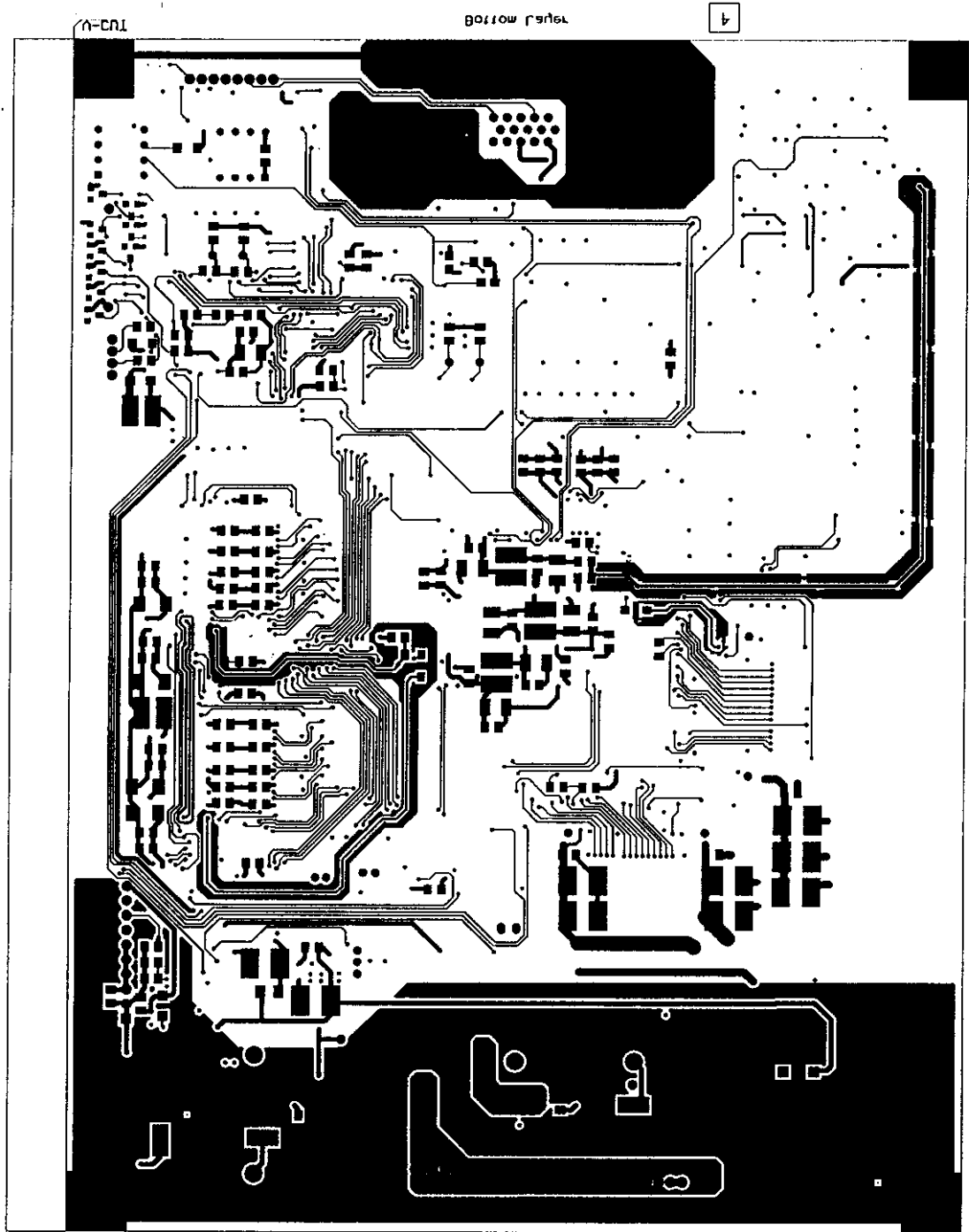
V-CUT

Internal Plane 1

2

3-Aug-1999





# Royal Information Electronics Co, Ltd.

## VERSION 0.1 REVERSION 0.7

### History:

4/1/99 - 1st Created.  
DESIGN BY BYRON  
4/5/99 - Added RGB Input Buffer,  
Corrected Schematic errors, added MCU  
ground filter and add support for 256K  
& 512K x 32 memory address 8 & 9  
select. Tony Chen

Royal Information Electronics Co., Ltd.

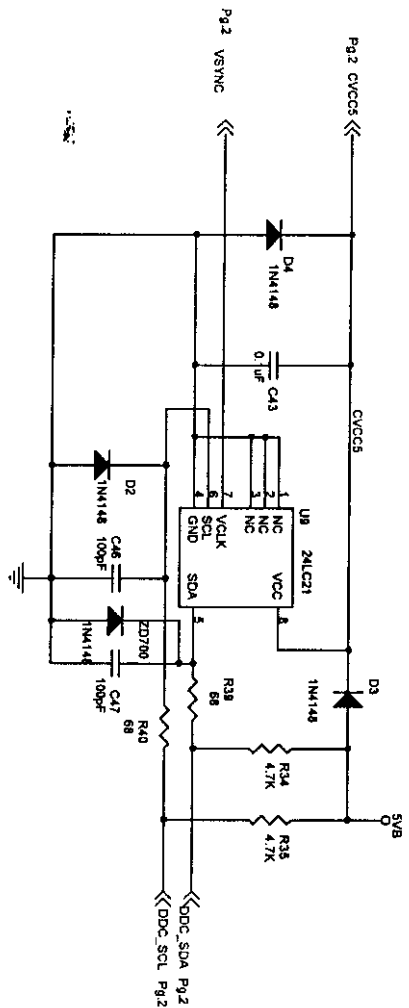
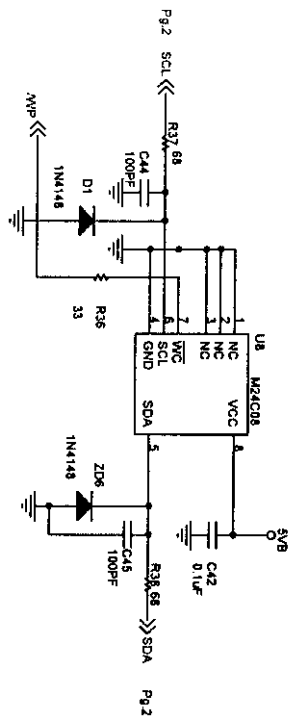
Size Document Number

R-5 Cover Page

Date Friday, July 16, 1999

Sheet 2 of 8





# R-5 EEPROM

Size Document Number

Change(Doel)

Friday, July 23, 1993

Print

3

of

8

