

Circuit Diagram

Model No. VHR-H518

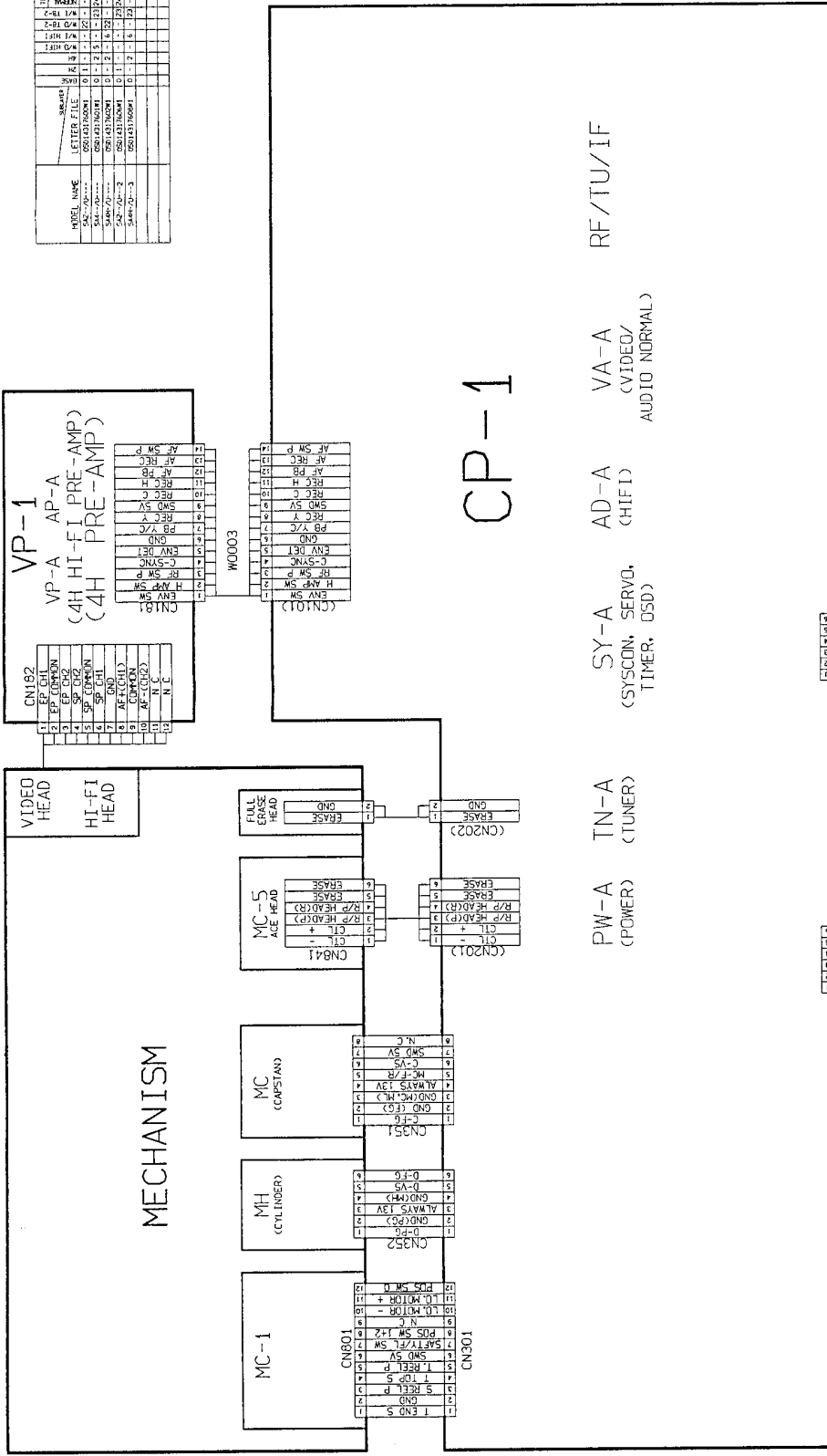
ILL. No.	Circuit Name	PWB Name	Function
ILL. 1			SD, WIRING
ILL. 2	VP-1	VP-A	PRE-AMP
ILL. 3		PW-A	POWER Circuit
ILL. 4	CP-1	AD-A	AUDIO (HiFi)
ILL. 5		VA-A	VIDEO/NORMAL AUDIO
ILL. 6		SY-A	SYSCON/SERVO/TIMER/OSD
ILL. 7		TN-A	TUNER
ILL. 8			
ILL. 9			
ILL. 10		RF/TU/IF	

The Number 8 and 9 are blank on the ILL No.

NOTE: DO NOT SCALE

発行
1997/12/11
技術部
最終変更
1998/03/06

APPROVALS	
CHKD	
CHKD	
DESIGN	ARIS H
DRAWN	ARIS H
DATE	REV NO
REVISION	SUBSEC



MODEL NAME	LETTER FILE	NUMBER	DATE	REVISION
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Model 2	Model 2	Model 2	Model 2	Model 2
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Model 98	Model 98	Model 98	Model 98	Model 98
Model 99	Model 99	Model 99	Model 99	Model 99
Model 100	Model 100	Model 100	Model 100	Model 100

CP-1

RF/TU/IF
VA-A (VIDEO/AUDIO NORMAL)
AD-A (CHIFT)
SY-A (SYSCON, SERVO, TIMER, OSD)
TN-A (TUNER)
PW-A (POWER)

ILL 1

BASIC MODEL	SA4H-/U----	CIRCUIT NAME	SD, WIRING
COMMON MODEL		PCB NAME	SCH CODE
			OSD14317602W1
		FINISH DATE	
		DRAW ID	
		PLOT DATE	

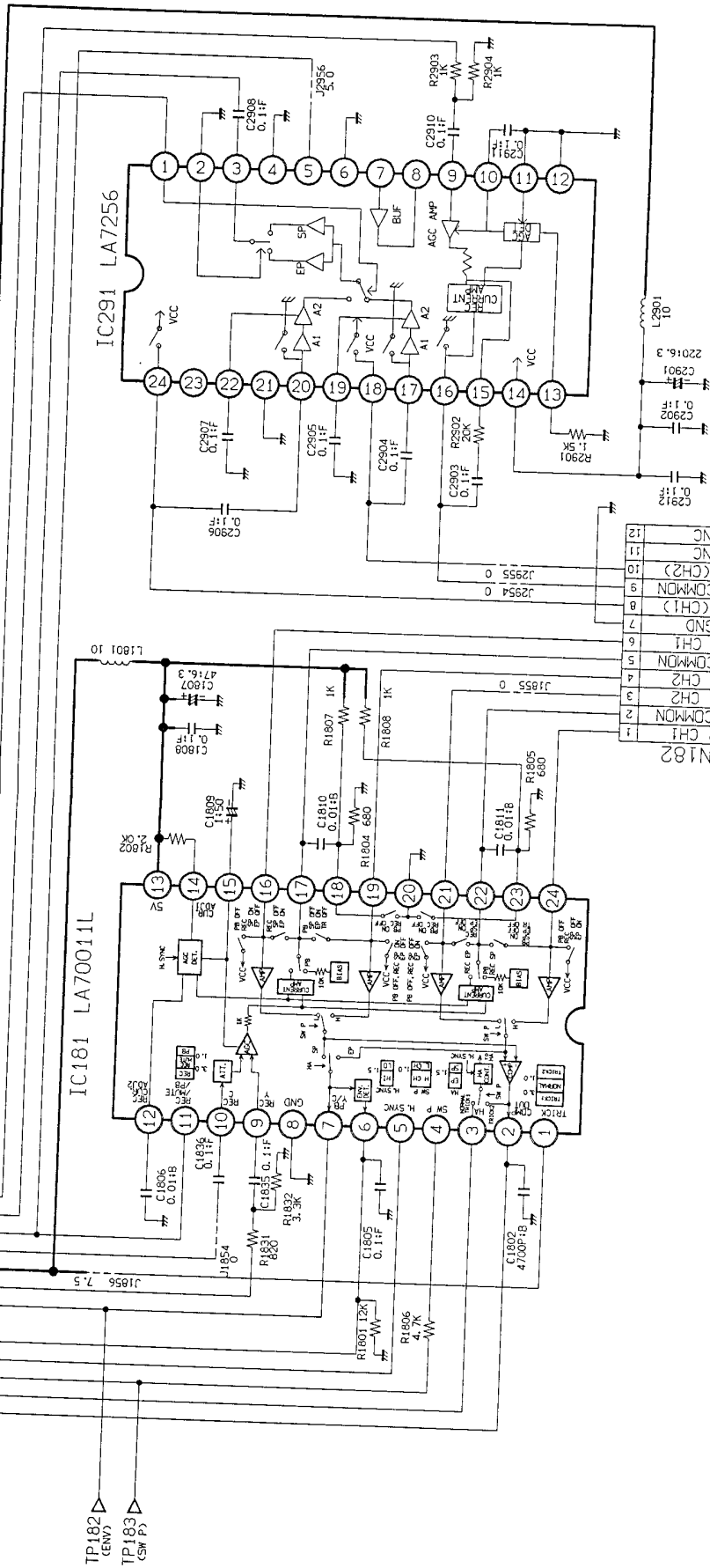


SUB LAYER			
0	COMMON	10	REC ADJ(NORMAL)
1	2HEAD	11	REC ADJ(XHIF1)
2	4HEAD	12	REC ADJ(2HEAD)
3	4HEAD(NORMAL)	13	REC ADJ(4HEAD)
4	4HEAD(XHIF1)	14	HIF1 (REC ADJ _{MAX})
5	NORMAL	15	
6	HIF1	99	OPTION
		100	SA2--/J----
		101	SA4--/J----
		102	
		103	SAHH-/J----
		104	
		105	
		106	

14	AF SW P
13	AF REC
12	AF PB
11	REC H
10	REC C
9	SMD SV
8	REC Y
7	PB Y/C
6	GND
5	ENV DET
4	C-SYNC
3	RF SW P
2	H AMP SW
1	ENV SW

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MODEL NAME	SUB-MINOR		COMMON	REC AD.	OPTION
	LETTER FILE	COPY CODE			
SAC - /U-----	05D1431760V2	0 1 5	-	-	-
SAD- /U-----	05D1431760V2	0 2 3	-	-	100
SADH-/U-----	05D1431760V2	0 2 4 6	-	-	101
					103



(TO MECHANISM)

ILL 2

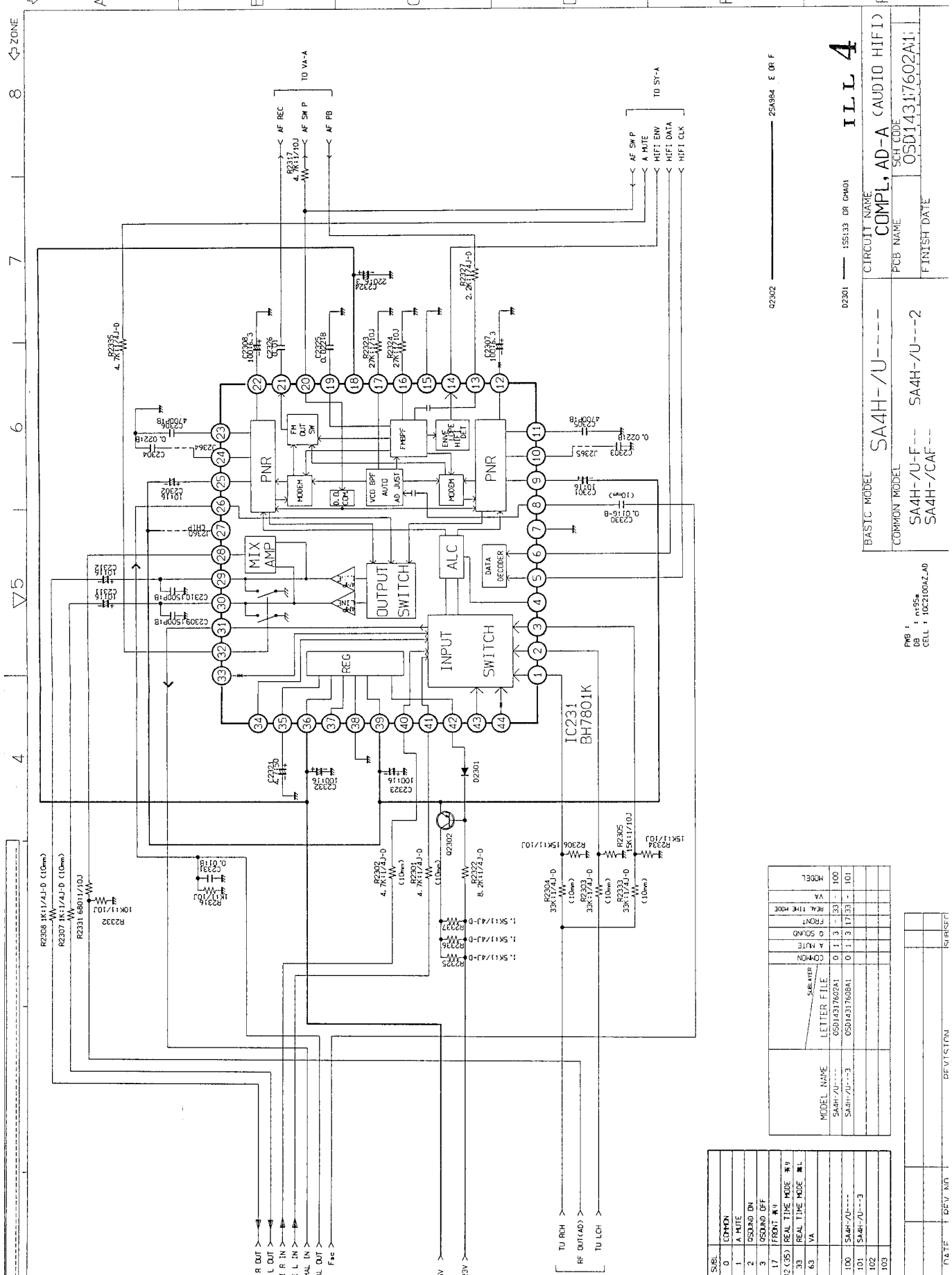
PWB :

DB : 01953

CELL : 1AC4B10C2100EZ

BASIC MODEL	SA4H-/U- - - -	CIRCUIT NAME	COMPL, VP-1 (VIDEO)	
COMMON MODEL	SA4H-/U- - - -	PCB NAME	SC# CODE	
	SA4H-/U-F-2	VP-1	OSD14317602V2	
	SA4H-/U-F-2	FINISH DATE		
	SA4H-/CAF-			
	SA4H-/U- - - -			

[illegible]



NOT FOUND

NOTE: DO NOT SCALE

MODEL NAME

LETTER FILE

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001

002

003

004

005

006

007

008

009

010

011

012

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014

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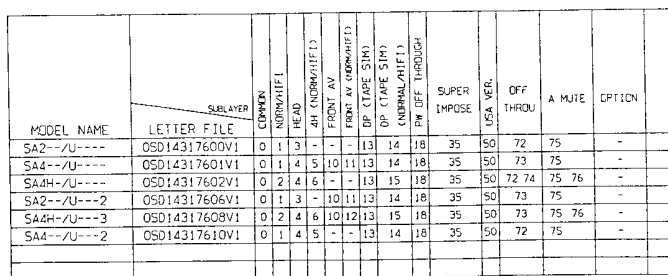
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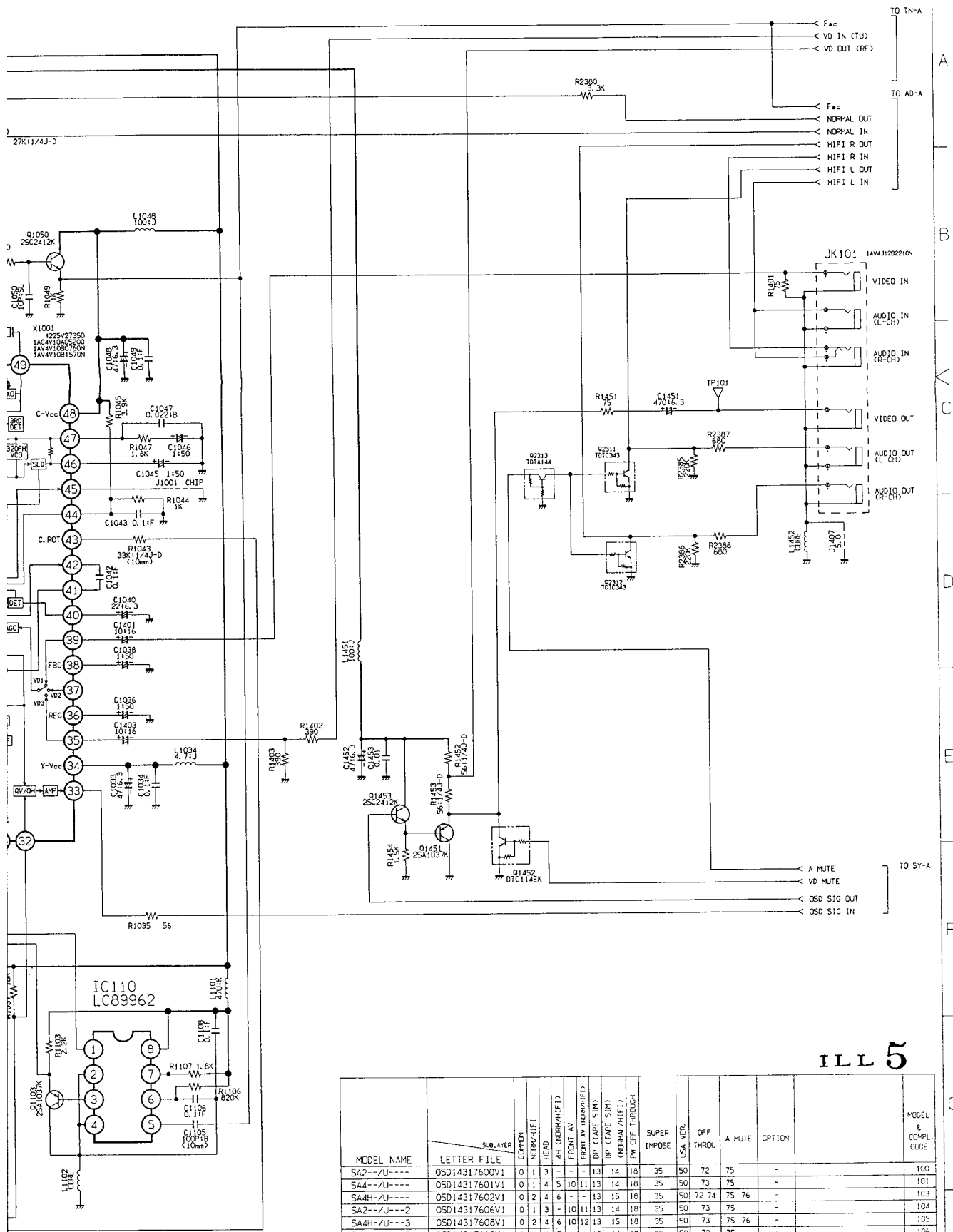
101

102

103







ILL 5

MODEL NAME	LETTER FILE	SUB-LAYER	COMMON	HEAD	4H (NORMAL/HIFI)	FRONT AV	DP (TAPE SIM)	DP (TAPE SIM) (NORMAL/HIFI)	PM OFF THROUGH	SUPER IMPOSE	USA VER.	OFF THRU	A MUTE	OPTION	MODEL & COMPL. CODE
SA2--/U----	OSD14317600V1	0	1	3	-	-	13	14	18	35	50	72	75	-	100
SA4--/U----	OSD14317601V1	0	1	4	5	10	11	13	14	18	35	50	73	75	101
SA4H-/U----	OSD14317602V1	0	2	4	6	-	-	13	15	18	35	50	72	74	103
SA2--/U---2	OSD14317606V1	0	1	3	-	10	11	13	14	18	35	50	73	75	104
SA4H-/U---3	OSD14317608V1	0	2	4	6	10	12	13	15	18	35	50	73	75	105
SA4--/U---2	OSD14317610V1	0	1	4	5	-	-	13	14	18	35	50	72	75	106

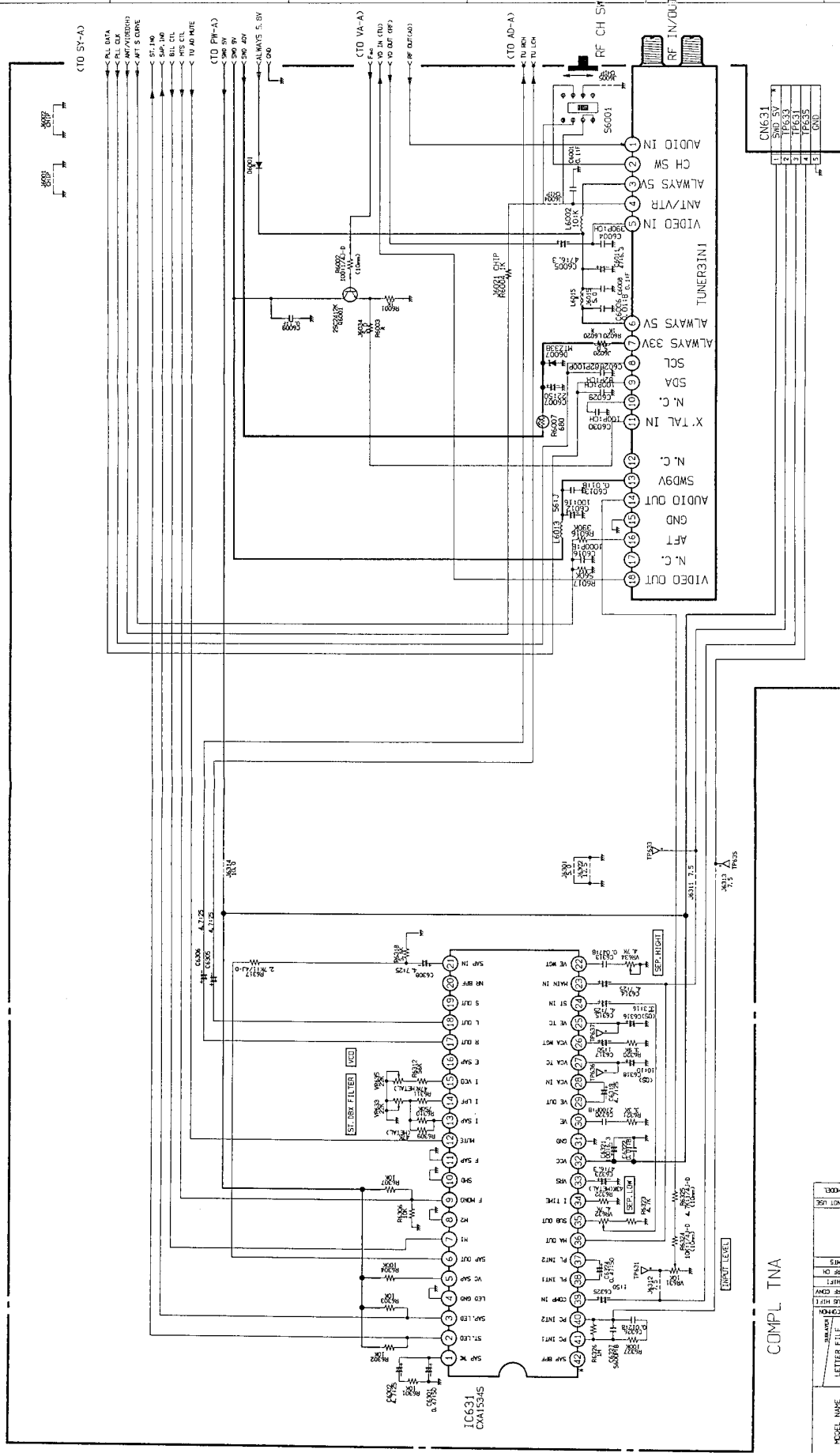
PWB :
DB : nt95a
CELL : 10C2100A.VA

BASIC MODEL SA4H-/U-----
COMMON MODEL SA4H-/U-F-- SA4H-/CAF--

CIRCUIT NAME COMPL. VA-A (VIDEO/NORMAL AUDIO)
PCB NAME CP-1 SCH CODE OSD14317602V1
FINISH

DRAW ID DB=KA34131 CELL=SA4_VAA PLOT DATE 98.03.10

NOTE: DO NOT SCALE



COMPL TNA

MODEL NAME	LETTER FILE	REV	REV NO	REV DATE
SA4H-7U	05014317602U	0	1	90.03.11
SA4H-7U	05014317602U	0	1	90.03.11
SA4H-7U	05014317602U	0	1	90.03.11

APPROVALS	DESIGN	CHKD	CHKD	DATE	REV NO	REV DATE
ARTIS H						
TRACE						

ILL 7

BASIC MODEL	SA4H-7U	CIRCUIT NAME	COMPL, TN-A
COMMON MODEL	SA4H-7U-3 SA4H-7U-2 SA4H-7U-1 SA4H-7U-0	PCB NAME	OSD14317602U
		SCH CODE	OSD14317602U
		FINISH DATE	
		DRAW ID	DB-KA34131
		CELL	SA4-TNA
		PLOT DATE	90.03.11

SANYO ELECTRIC CO., LTD. VIDEO DIVISION.

SANYO

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