

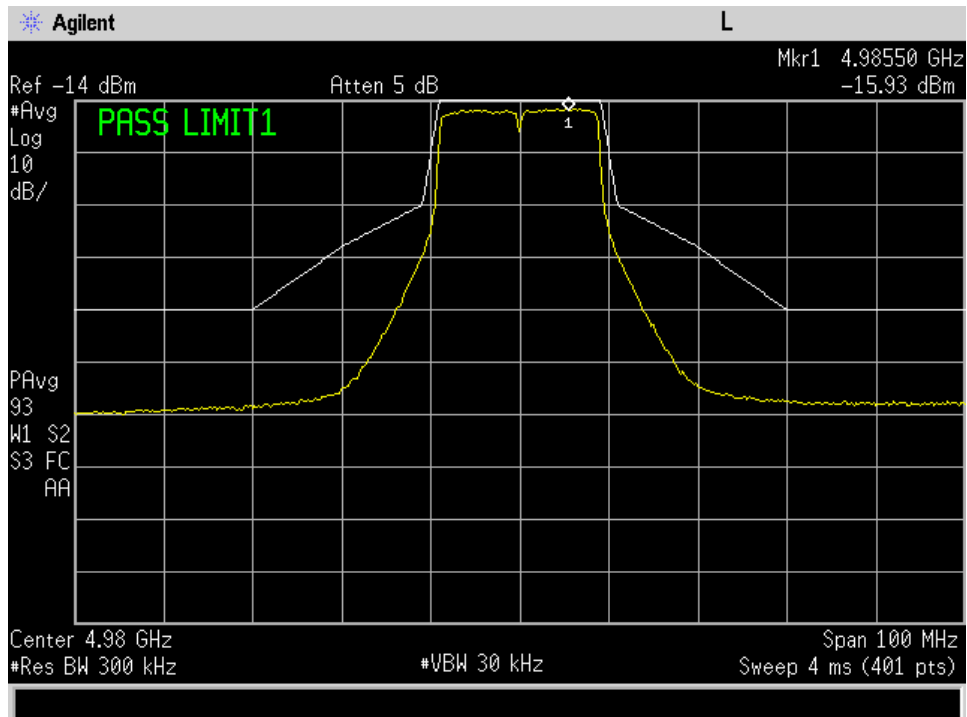


Annex D

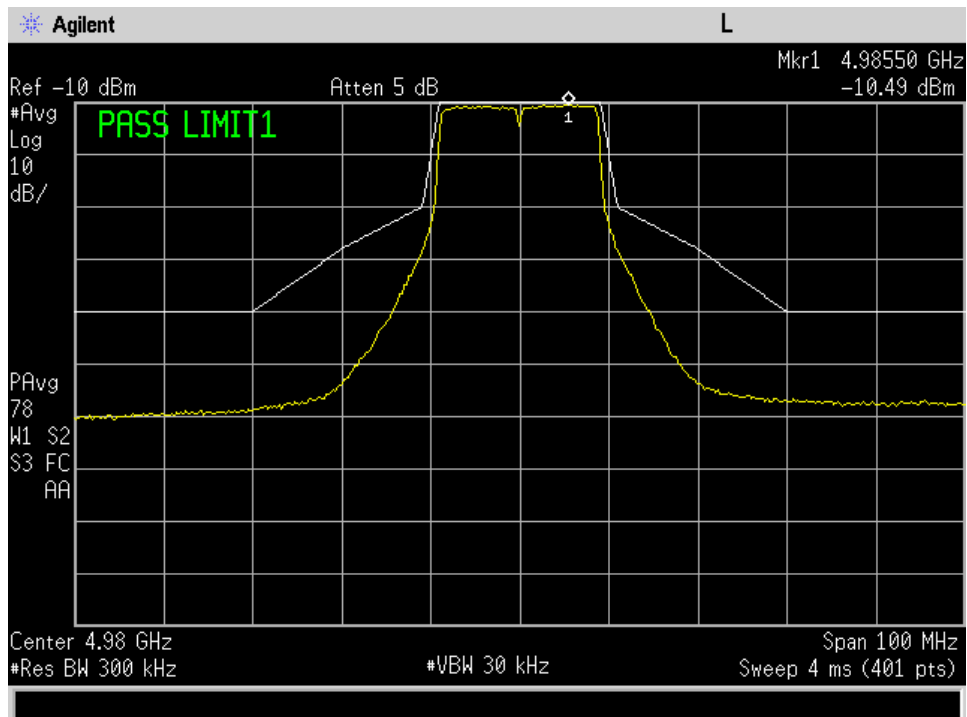
Emission Masks



High Channel Port 1 ptm

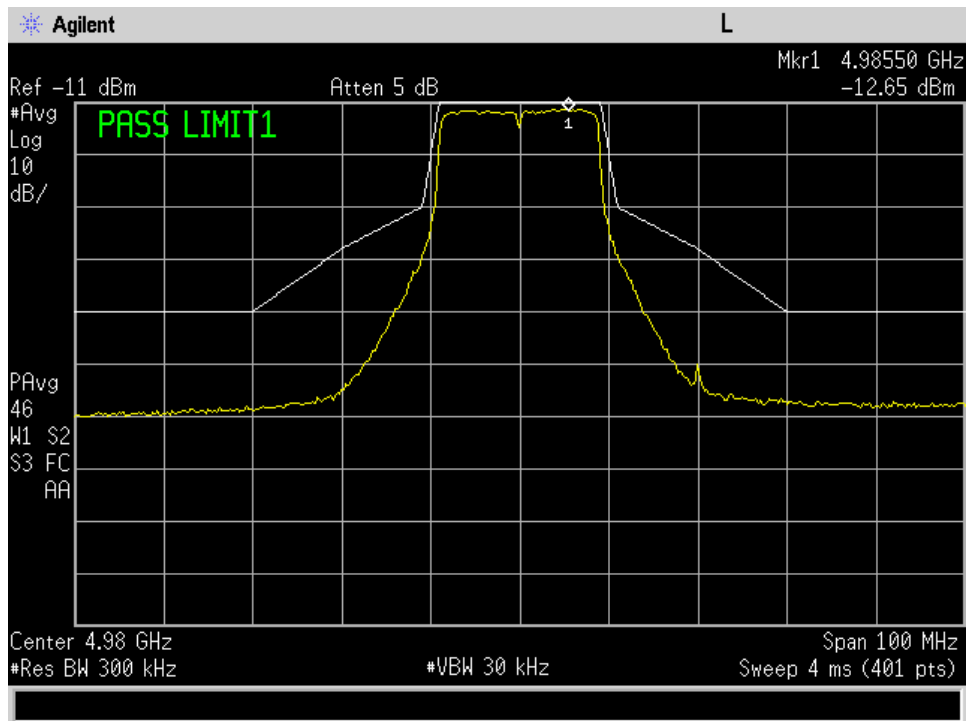


High Channel Port 1

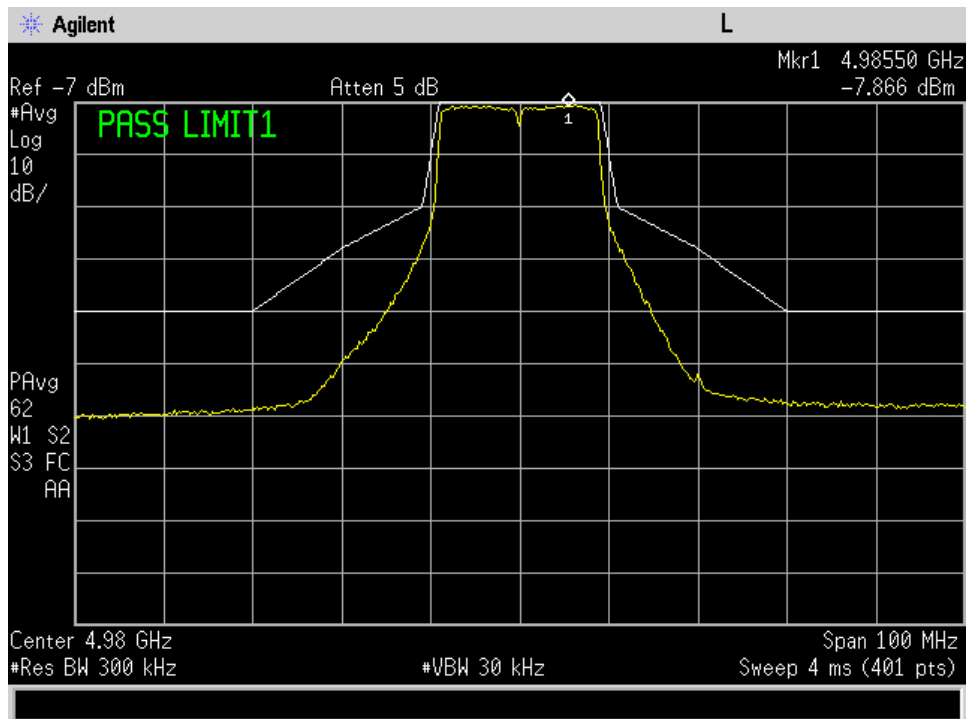




High Channel Port 2 ptm

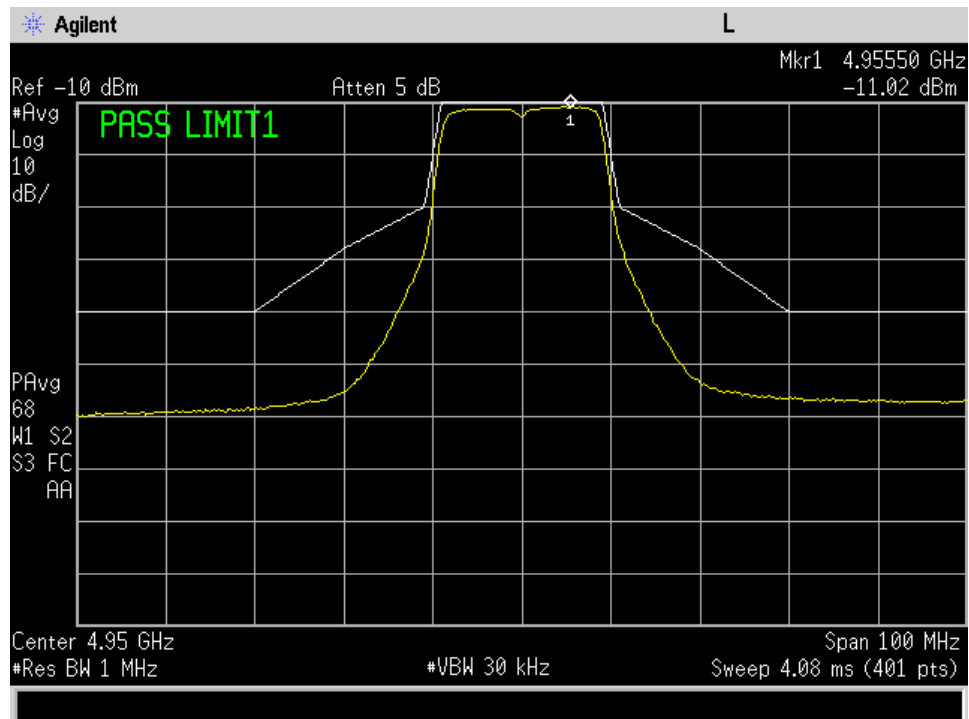


High Channel Port 2

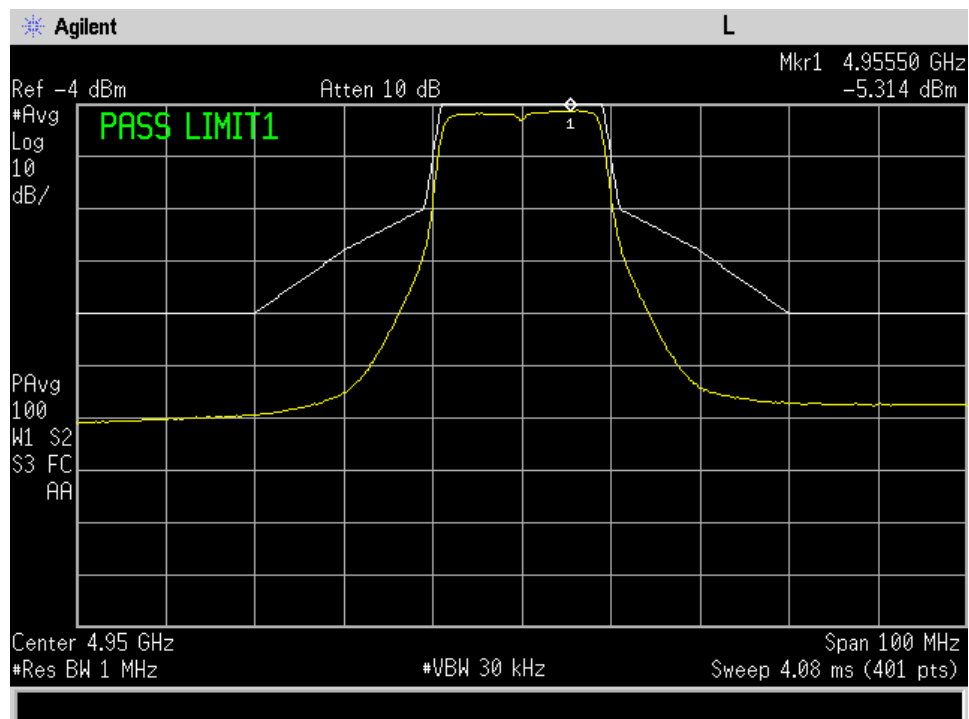




Low Channel Port 1 ptm

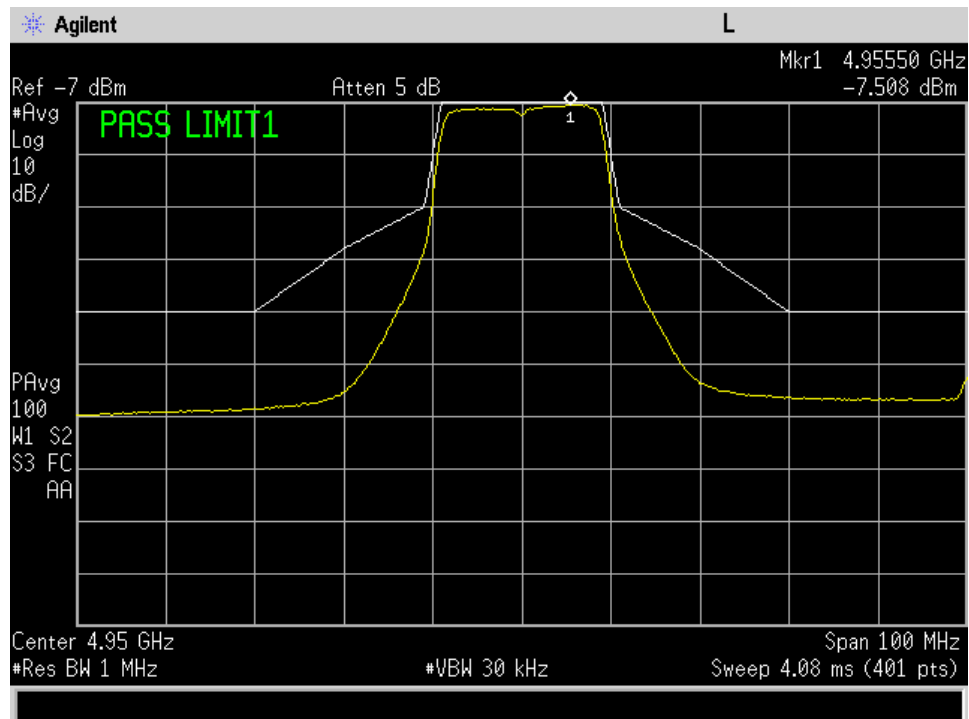


Low Channel Port 1

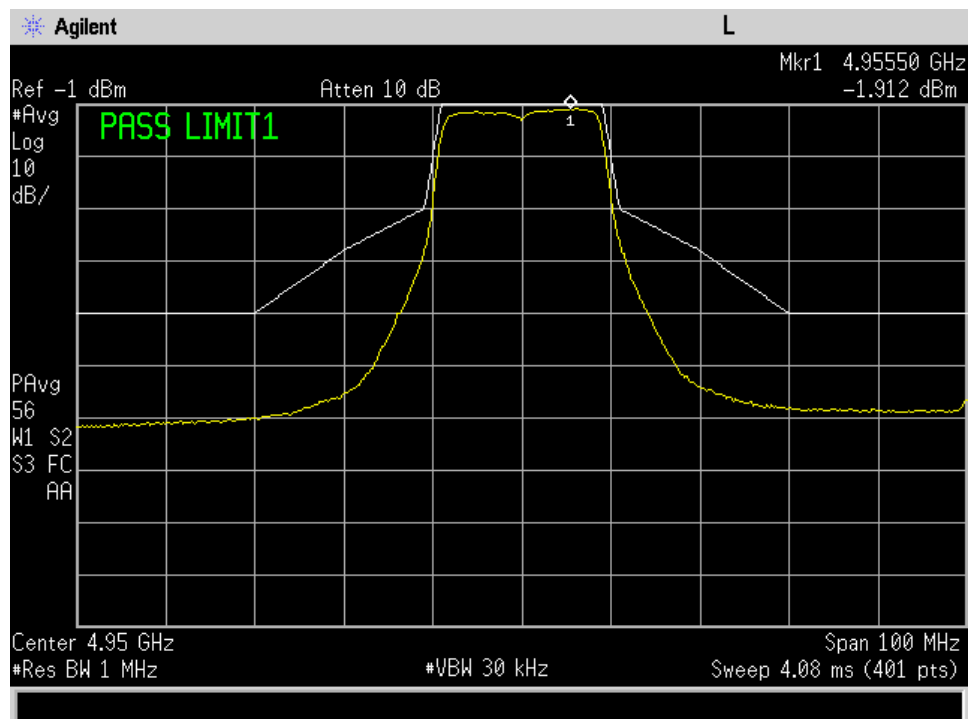




Low Channel Port 2 ptm

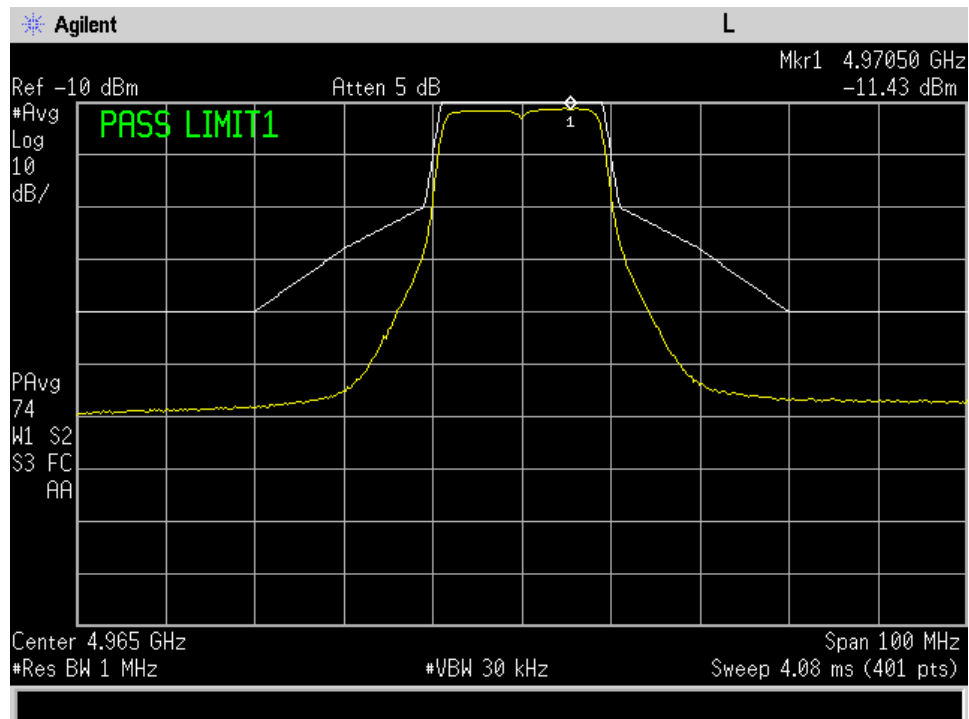


Low Channel Port 2

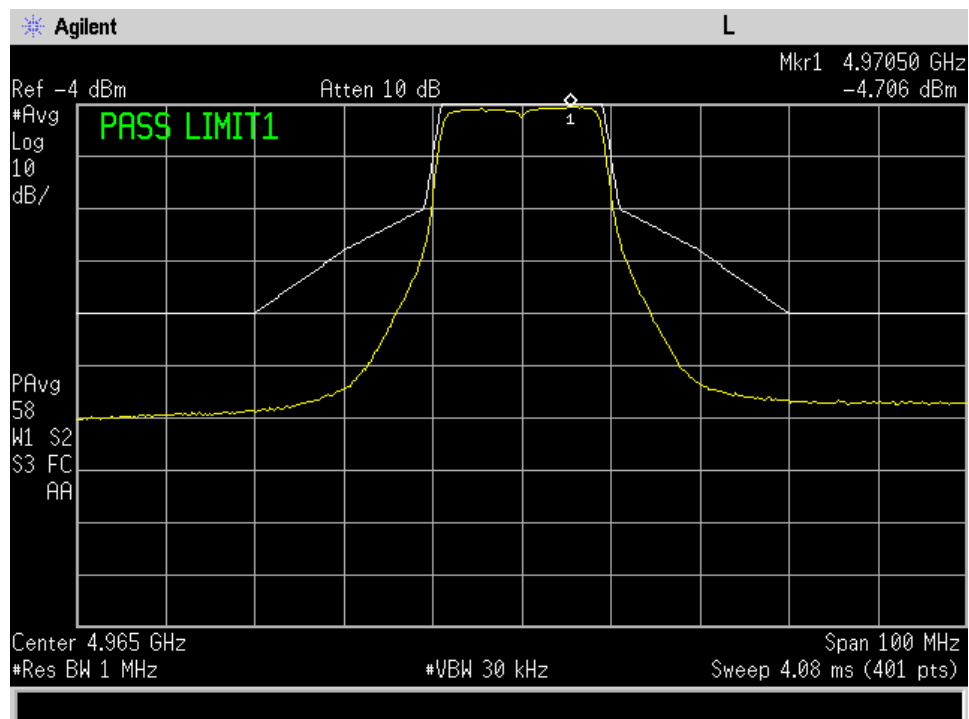




Mid Channel Port 1 ptm

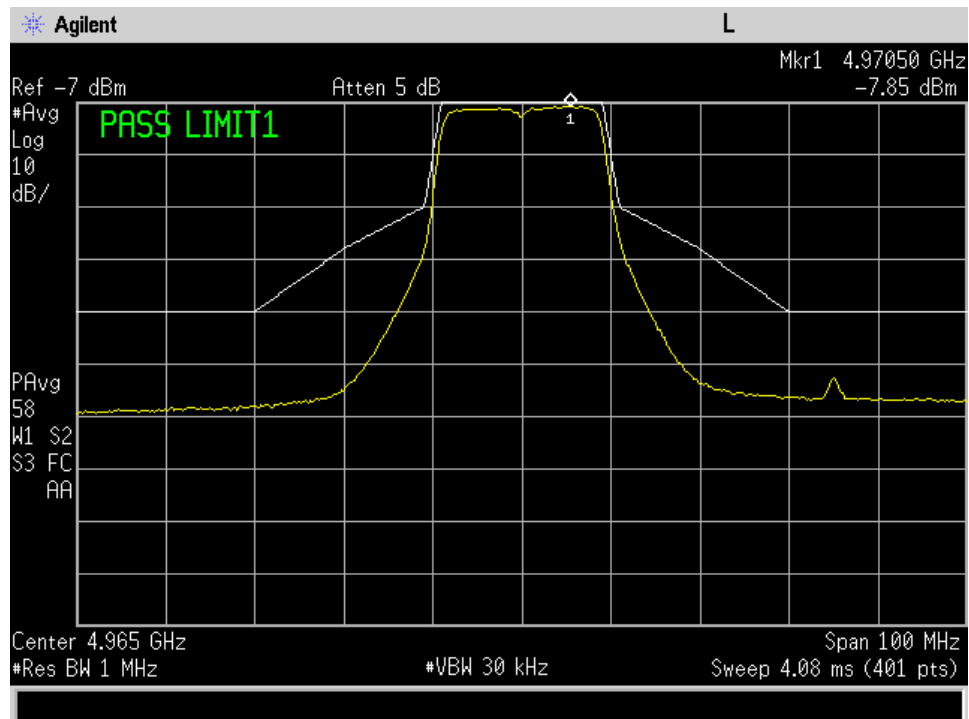


Mid Channel Port 1

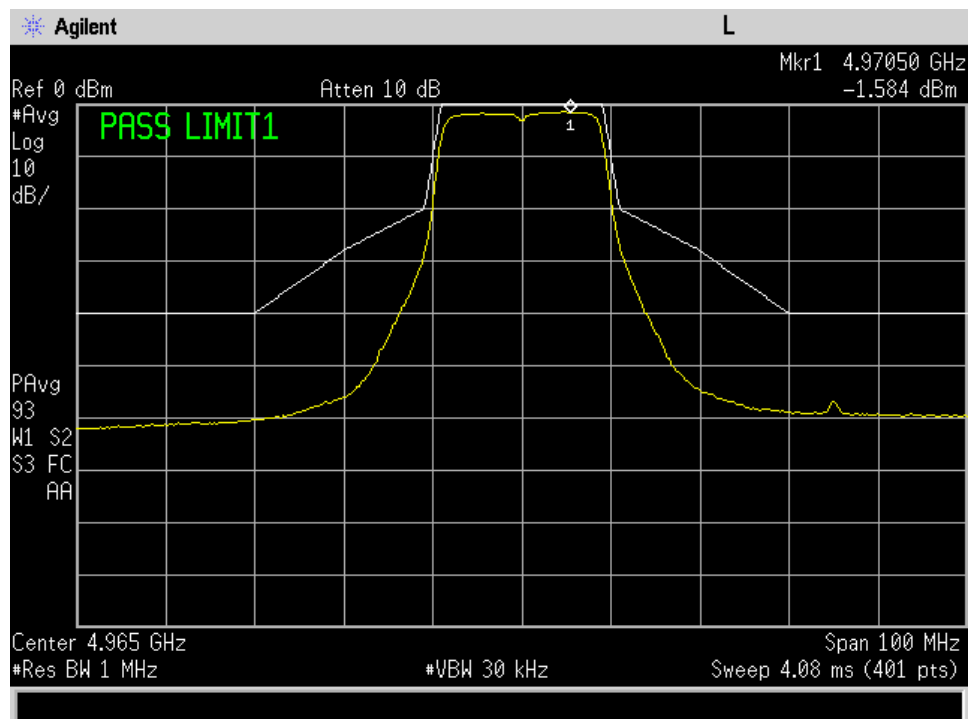




Mid Channel Port 2 ptm

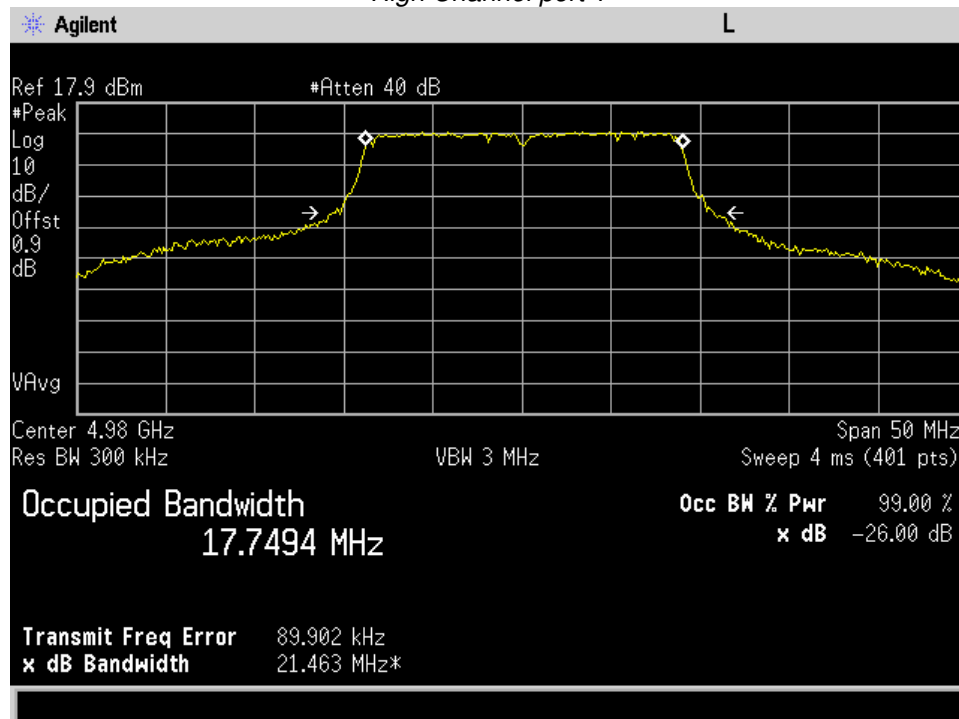


Mid Channel Port 2

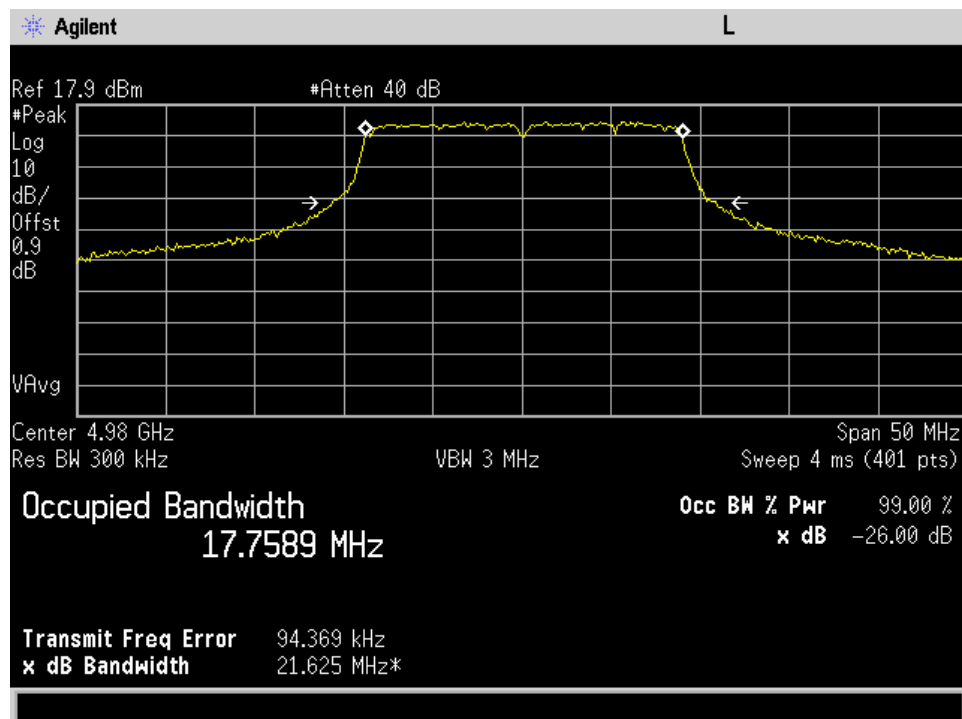




High Channel port 1

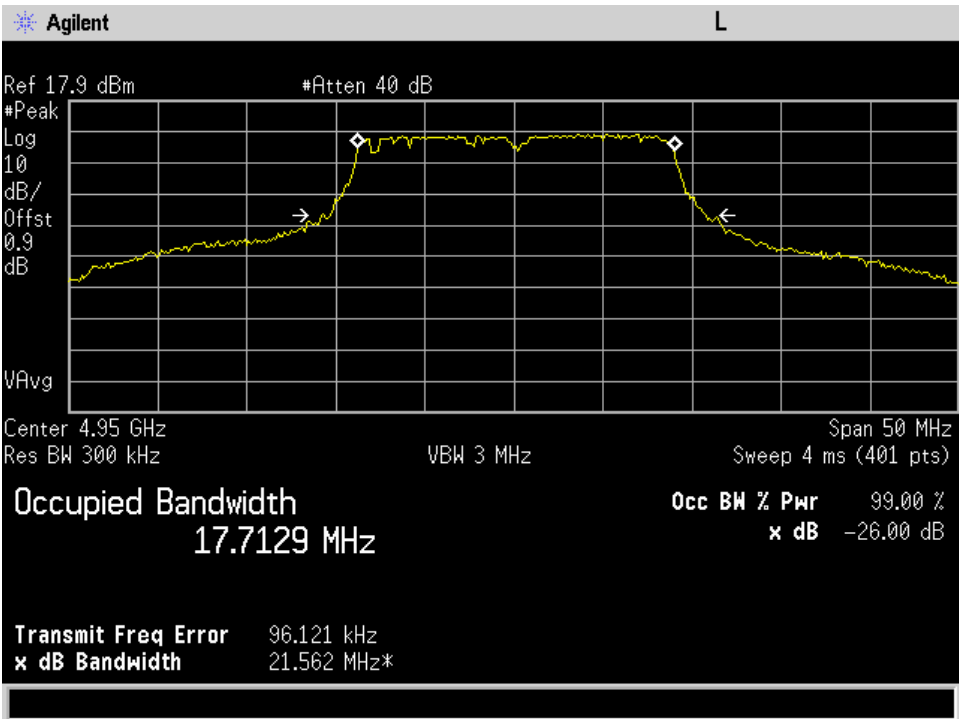


High Channel port 2

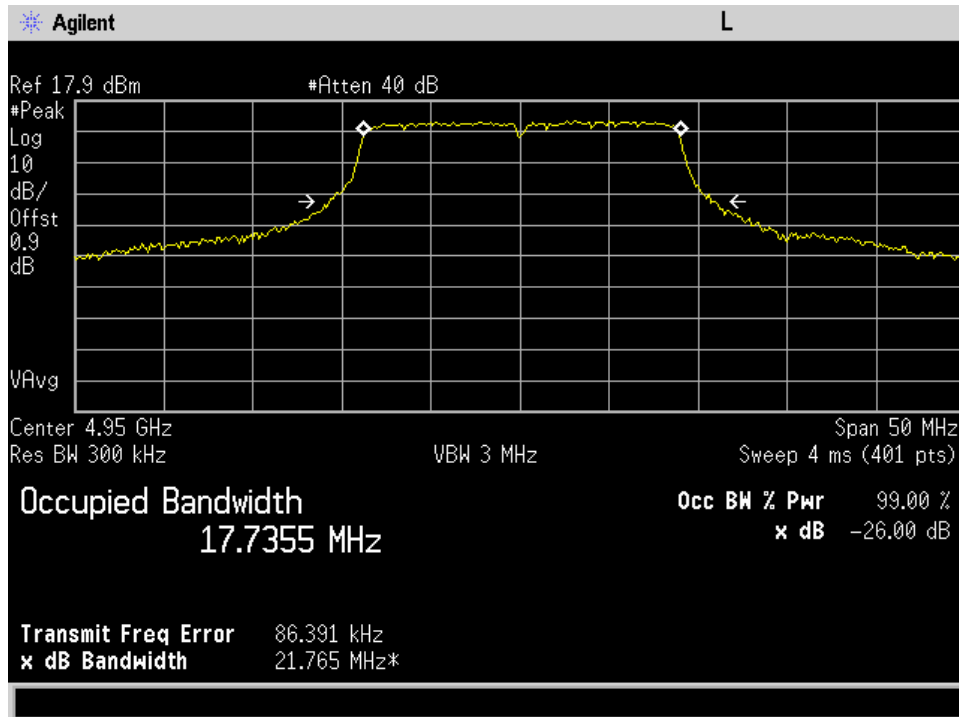




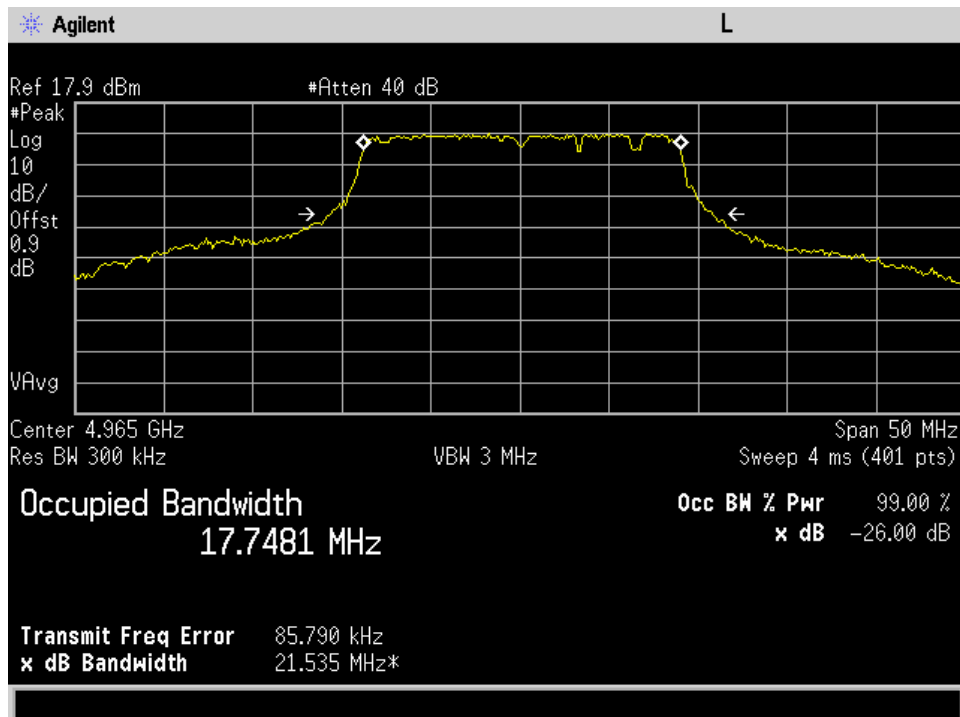
Low Channel port 1



Low Channel port 2



Mid Channel port 1



Mid Channel port 2

