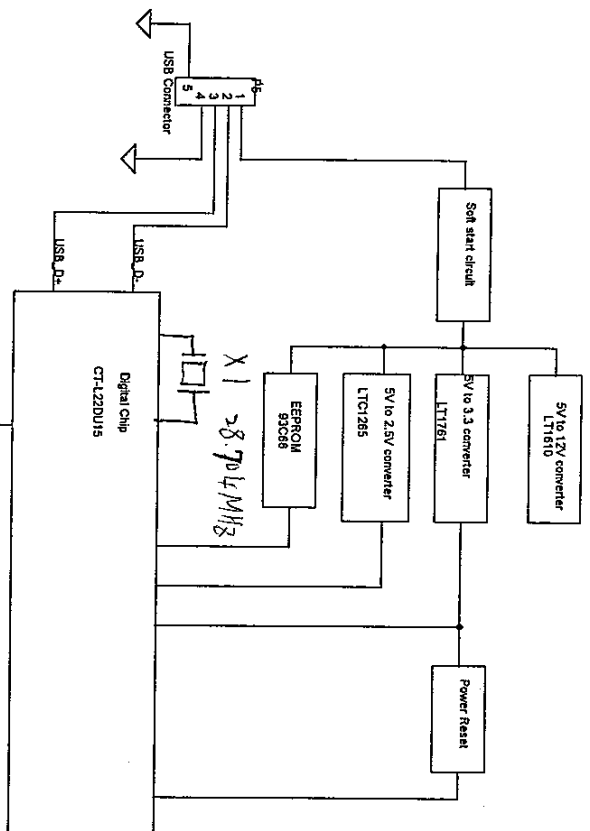
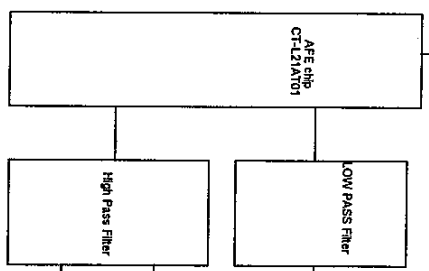
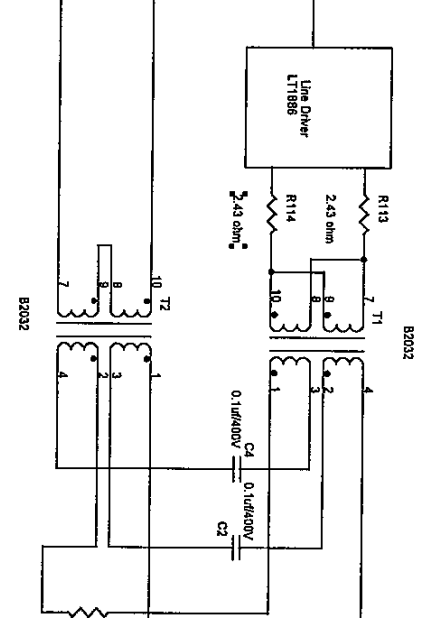
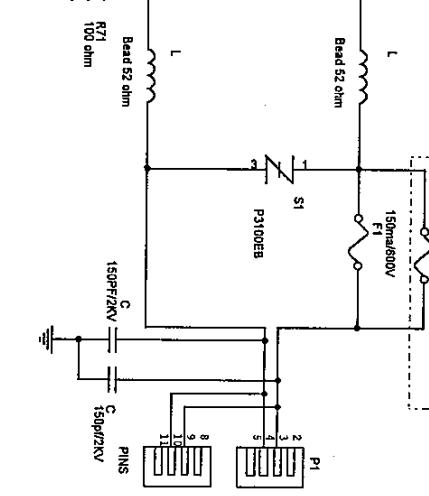


EXHIBIT E

Block Diagram



Rev D : for PA2150001001
 Rev E : PCB PA2150001001 with EEPROM rework (see page 5)
 Rev F : PCB PA2150001003 with EEPROM ECN (see page 5)
 Rev G : PA2150001005 (EMI modification) ----
 Page2 -- add I19~23 , C169,C172
 Pages -- add , L25,L26,C170,C171
 C42,C43,C139,C140,C79 -- 22uF/16V -> 33uF/16V

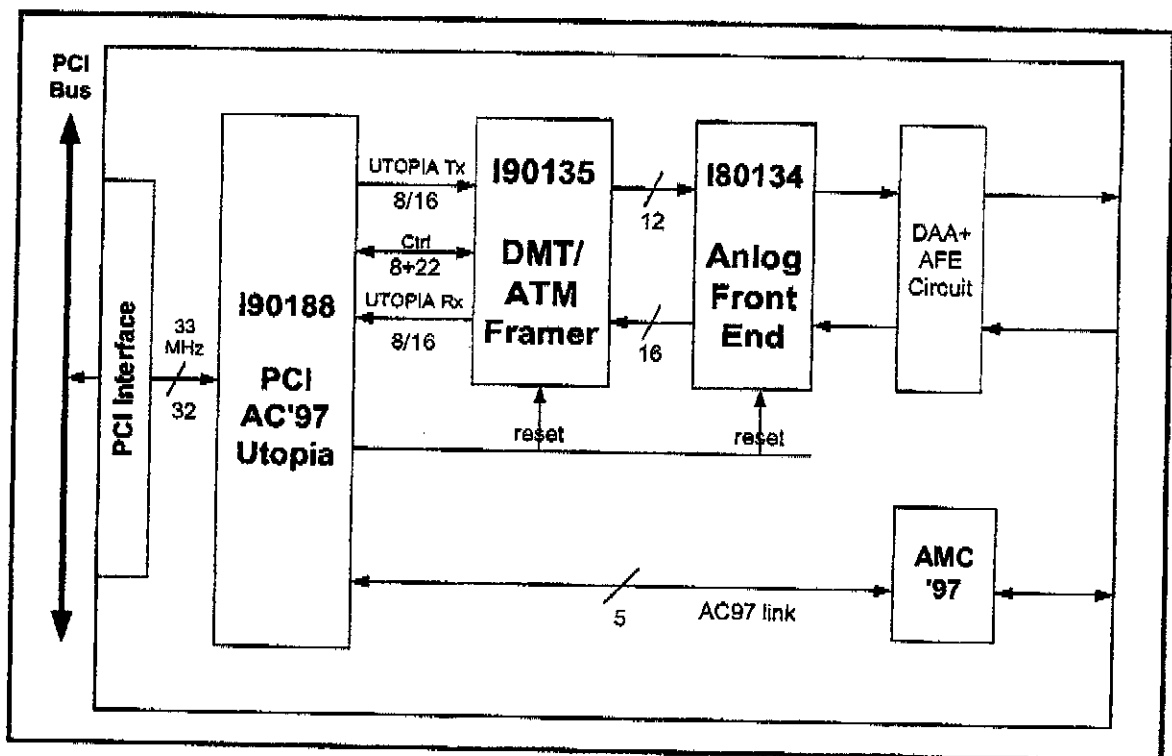
Title		Optimiser USB ADSL Modem
Document Number		B
Block diagram		B
Date	Thursday, February 17, 2000	Sheet 1 of 9
Rev	G	

To: Pan

From: Aceex

Apollo 2 Chipset
Product Data Sheet
Version 1.1 (January 1999)

System Block Diagram



The ITeX ADSL Modem Chipset

The Apollo 2 Chipset solution consists of a DMT transceiver and ATM framer (I90135), an analog front end (I80134), a proprietary PCI Utopia interface (I90188) and NDIS 5.0 miniport driver.

The Chipset Functions

The chip functions are depicted in the system block diagram.

The functions included in each IC are as follows:

- **Analog Front End (I80134)**

This CMOS IC contains the analog functions required in the transceiver. In order to cope with the high attenuation of the line and in order to keep acceptable noise level of the signal, automatic gain

control amplifiers have been implemented at the analog front of the transmission and reception paths. Then, the signal is passed through low pass filters to eliminate the echo signal and out-of-band interferences.

The AD and DA converters provide 12 bit resolution at 8.8 MHz sampling rate. In the transmission path, the control of the external hybrid drivers is done by an integrated highly linear line pre-driver.

- **DMT Transceiver and ATM Framer (I90135)**

This CMOS IC contains all the digital functional blocks required for the following functions.