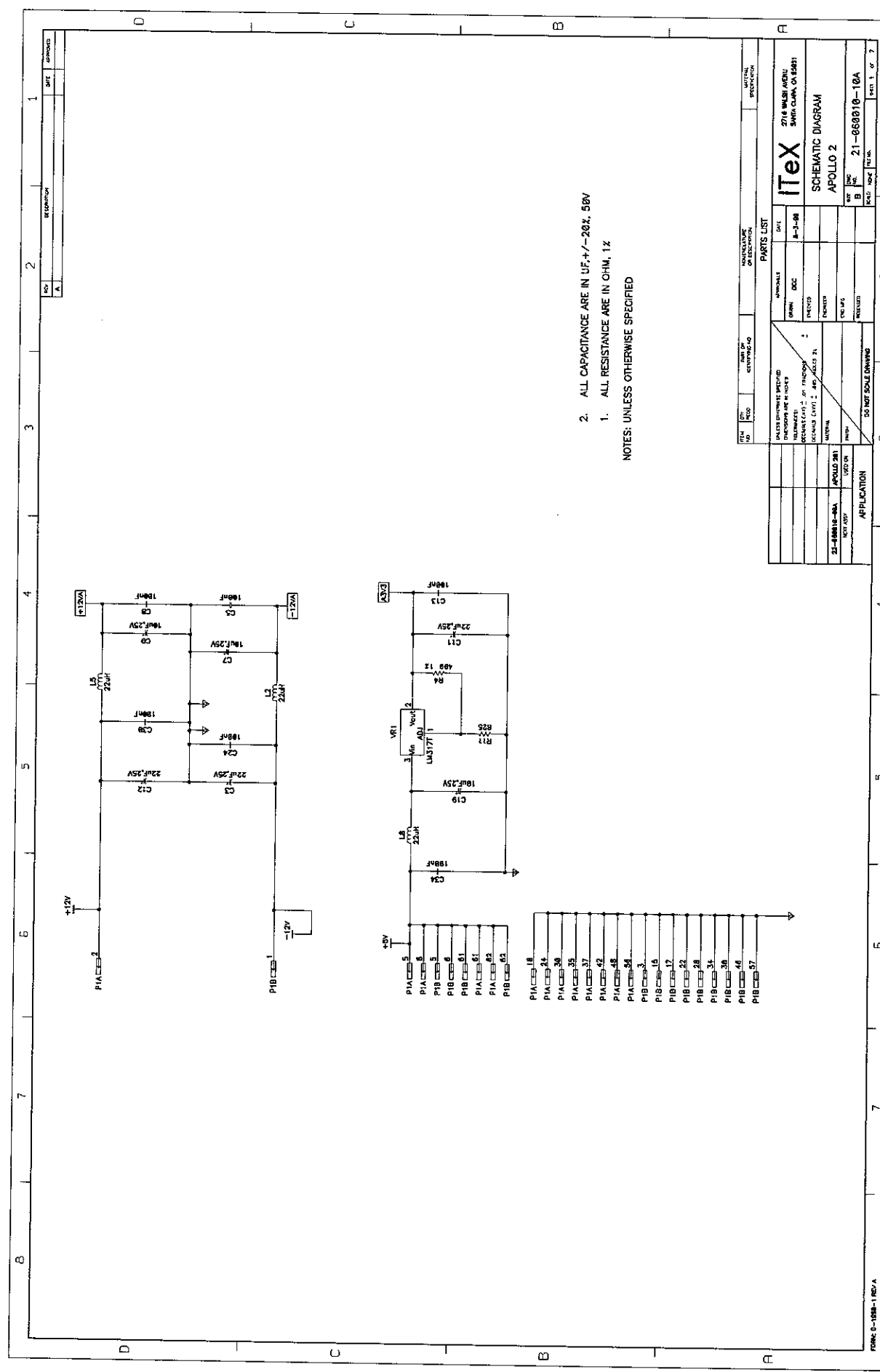


EXHIBIT D

Circuit Diagram



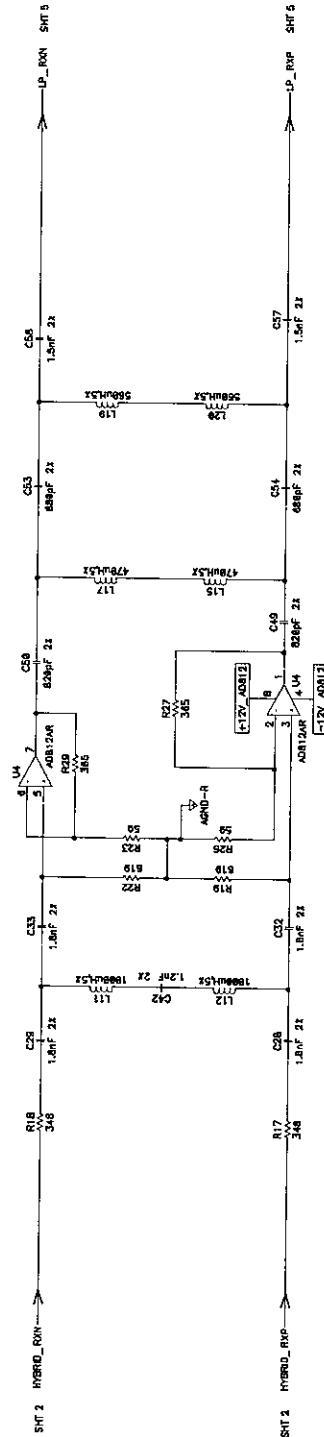
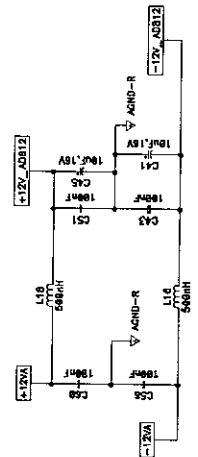
2. ALL CAPACITANCE ARE IN UF, +/- 20%, 50V
1. ALL RESISTANCE ARE IN OHM, 1%
NOTES: UNLESS OTHERWISE SPECIFIED

ITEM NO	QTY	DESCRIPTION	REVISION	DATE	BY	APP'D
1	1	21-050010-10A				
2	1	21-050010-10A				
3	1	21-050010-10A				
4	1	21-050010-10A				
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LINE PROTECTION + HPF + HYBRID

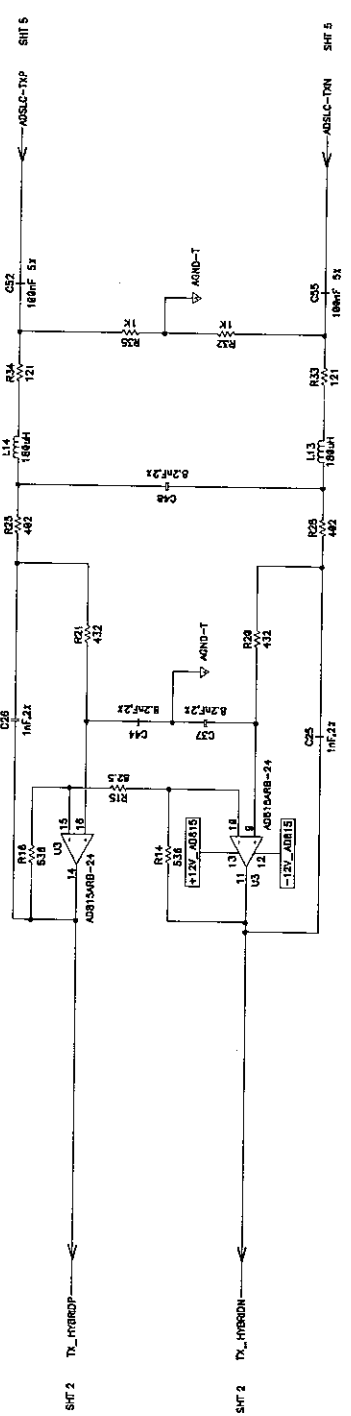
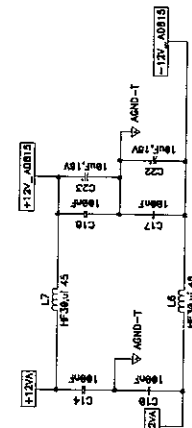
APOLLO 2
21-660810-10A
REV B
DATE 10/1/66
PAGE 2 OF 7

RECEIVE PATH



000024

TRANSMIT PATH



NOTE:
FOR U3 (ADR15), PIN 5, 7, 8, 17, 18, 19, 20
MUST BE TIED TO +12V
ACCORDING TO THE SPECIFICATION.

ADSL ANALOG FRONT END

ADSL ANALOG FRONT END

3.3V

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MAX923

MAX924

MAX925

MAX926

MAX927

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MAX929

MAX930

MAX931

MAX932

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