

1 BLOCK DIAGRAM(S) OF EQUIPMENT

1.1 Clock list of PCXPocketV3, VXPocket, VXPocketV2 product

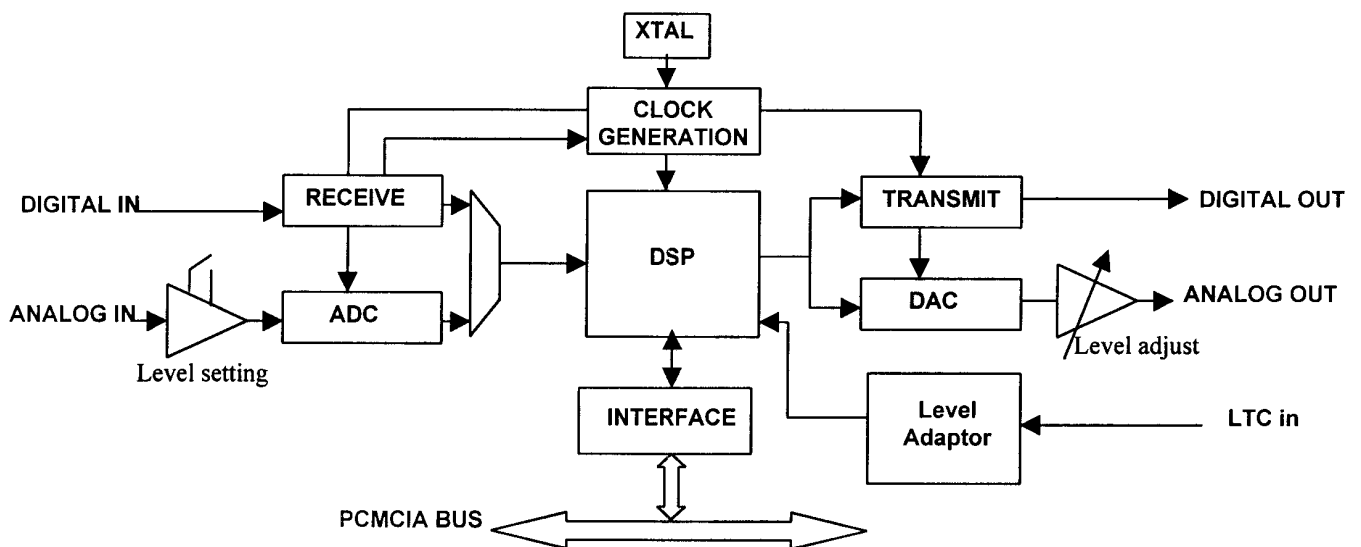
Oscillator: 28.224 MHz

DSP: Q2 = 28.224 MHz

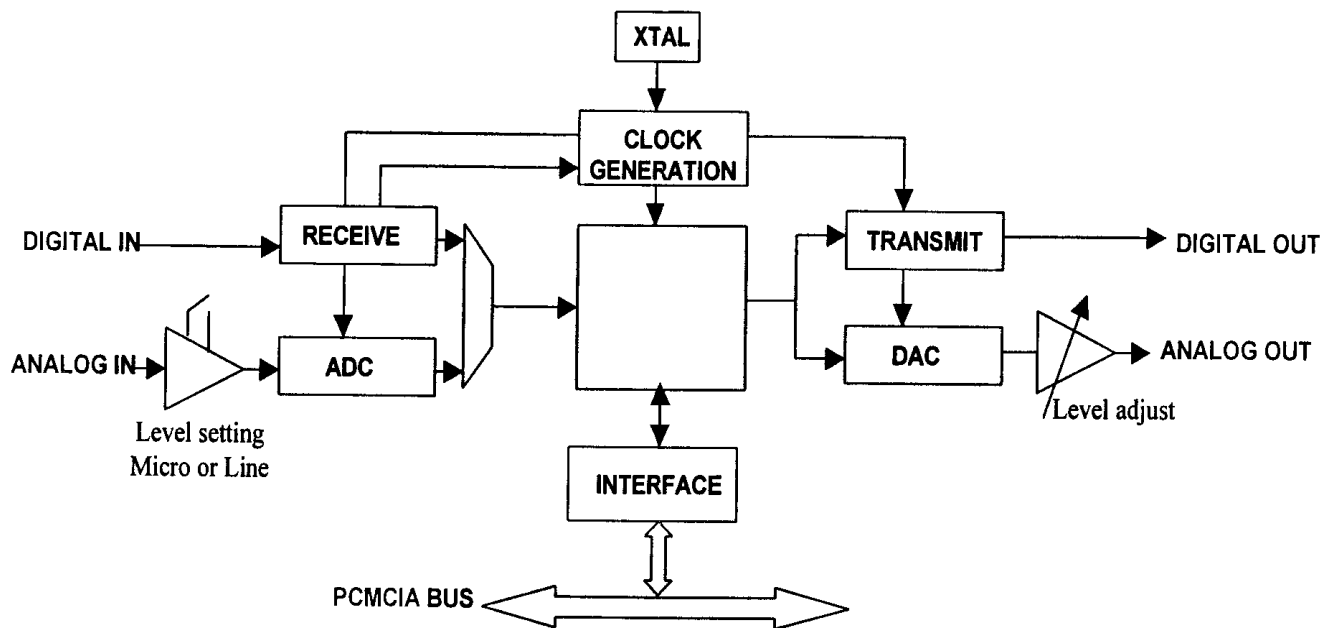
Sampling frequency: up to 48 KHz

1.2 Block diagram

1.2.1 Schematic diagram of PCXPocket V3



1.2.2 Schematic diagram of VXPocket



1.2.3 Schematic diagram of VXPocketV2

