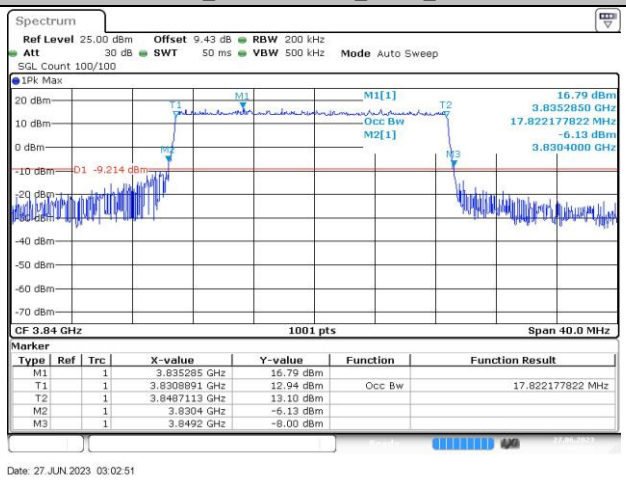
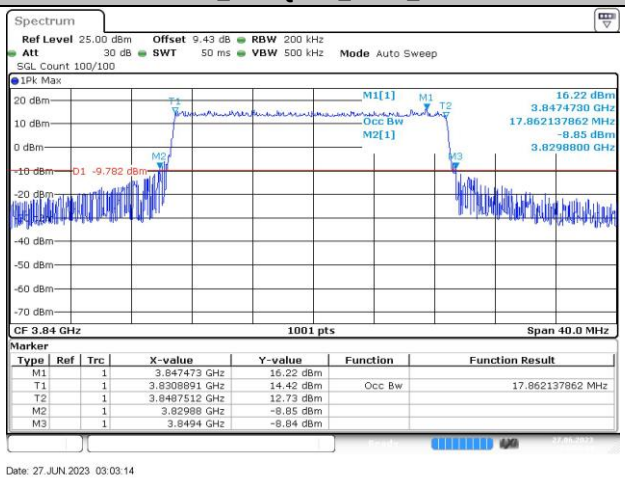
### 4.2.3. Bandwidth:20MHz

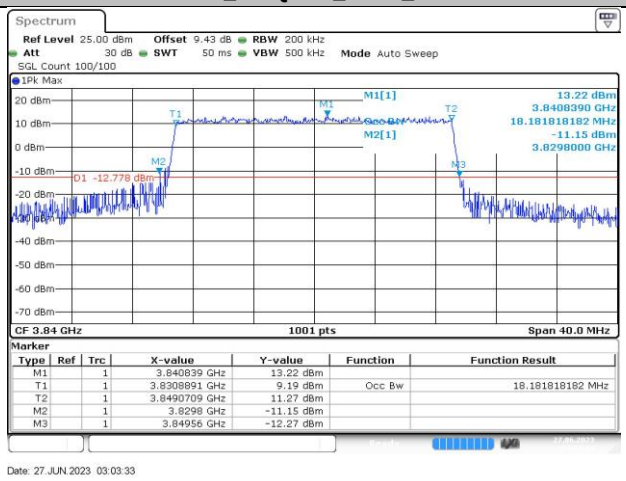
**MCH_DFT-Pi2BPSK_Outer_Full**



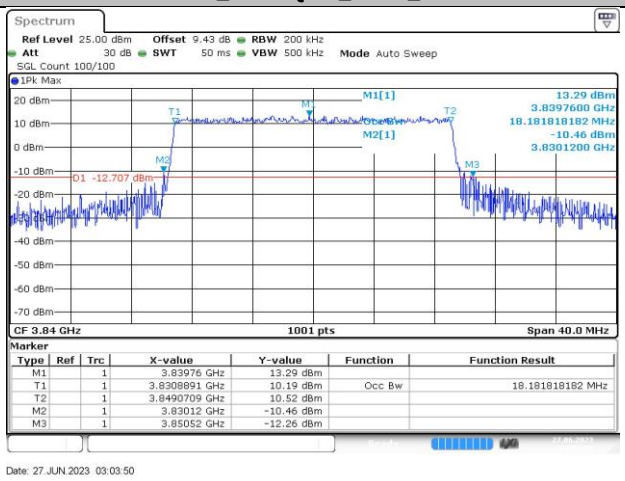

**MCH_DFT-QPSK_Outer_Full**



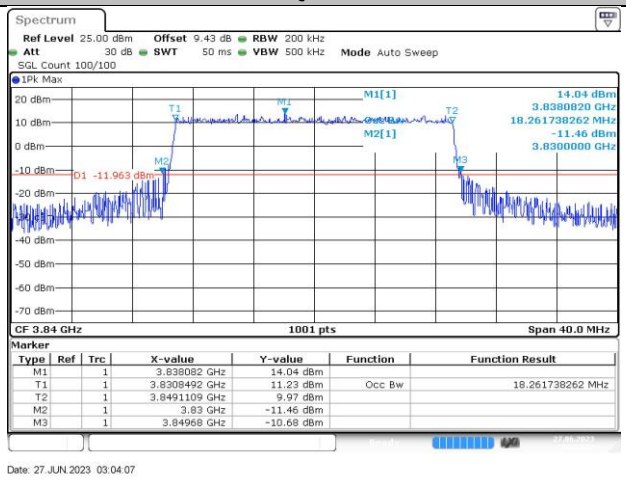

**MCH_CP-QPSK_Outer_Full**



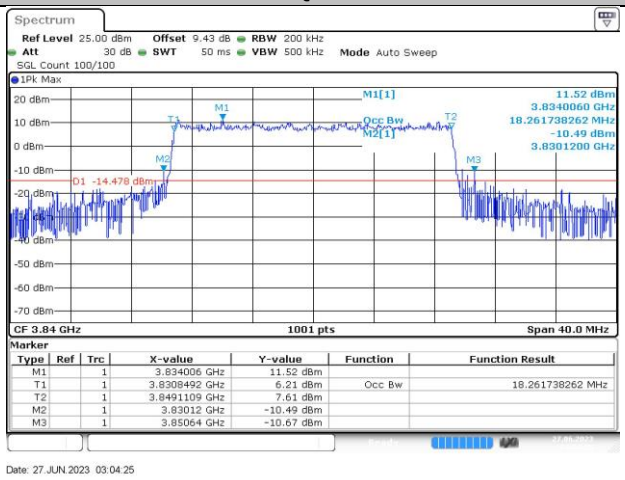

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**

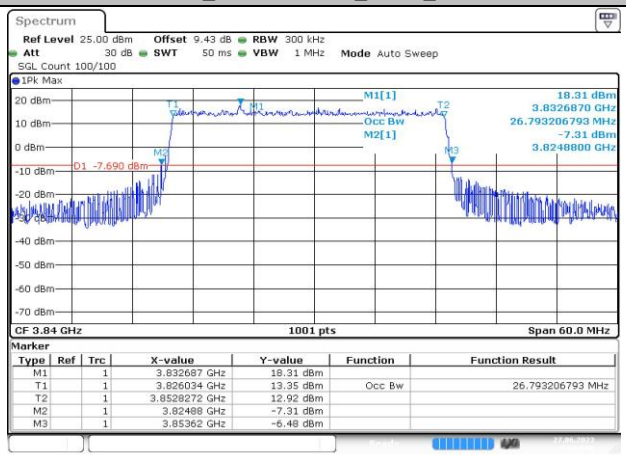



**MCH_CP-256QAM_Outer_Full**

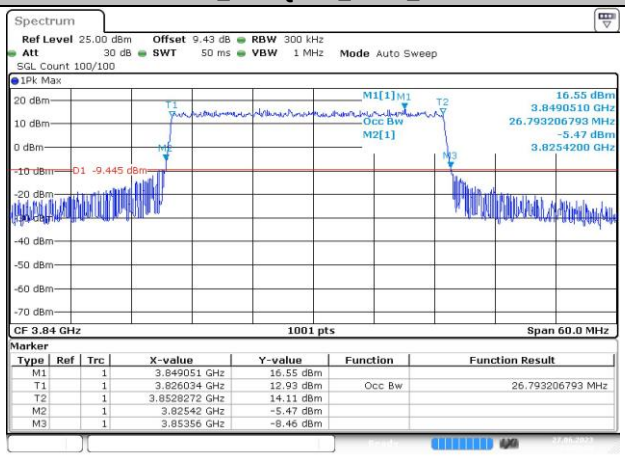


### 4.2.4. Bandwidth:30MHz

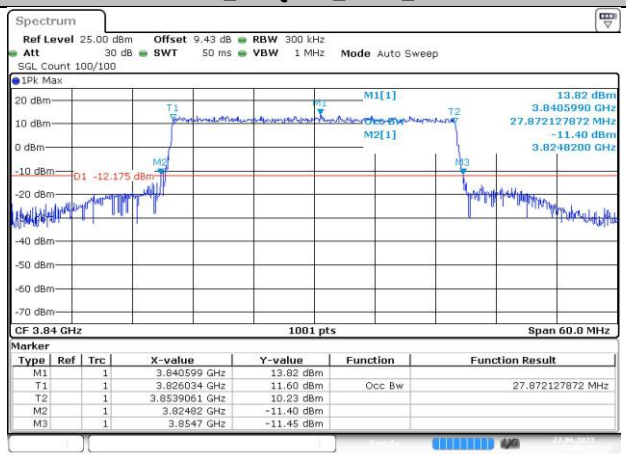
**MCH_DFT-Pi2BPSK_Outer_Full**



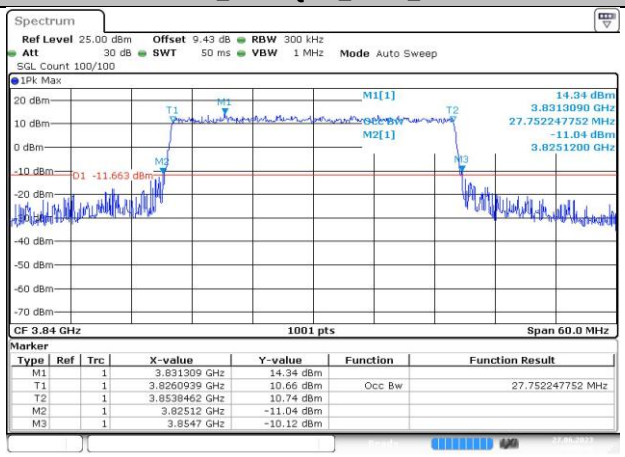

**MCH_DFT-QPSK_Outer_Full**



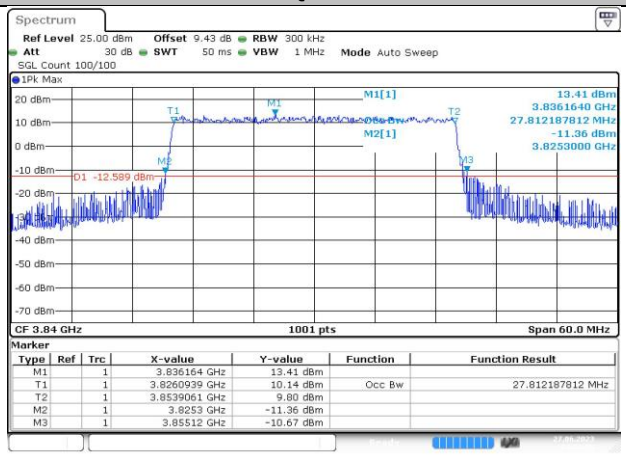

**MCH_CP-QPSK_Outer_Full**



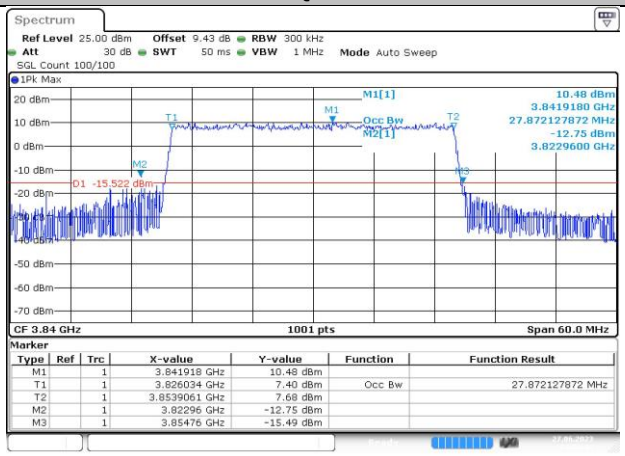

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**

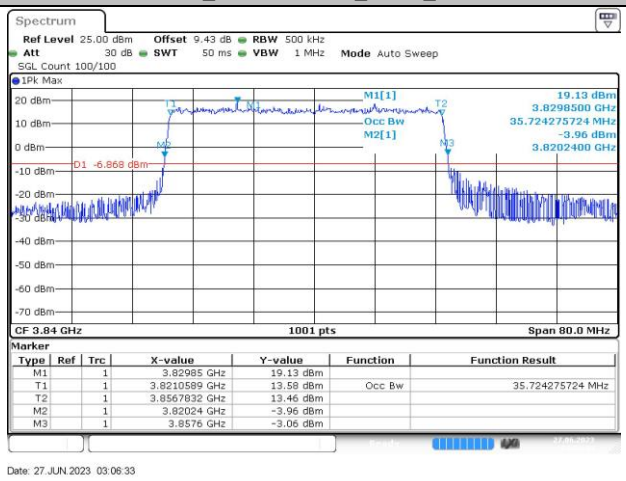



**MCH_CP-256QAM_Outer_Full**

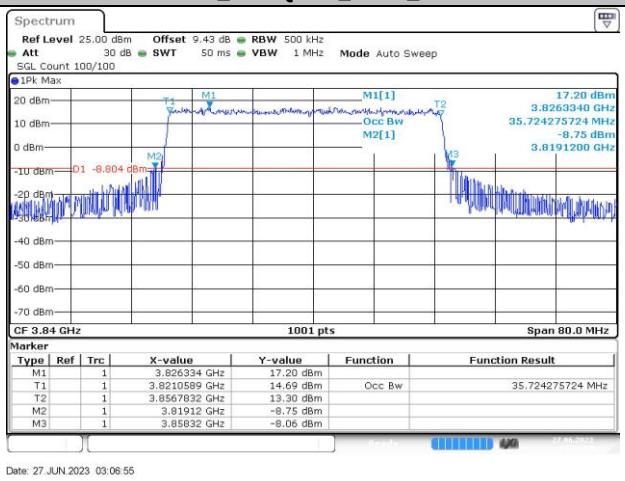


### 4.2.5. Bandwidth:40MHz

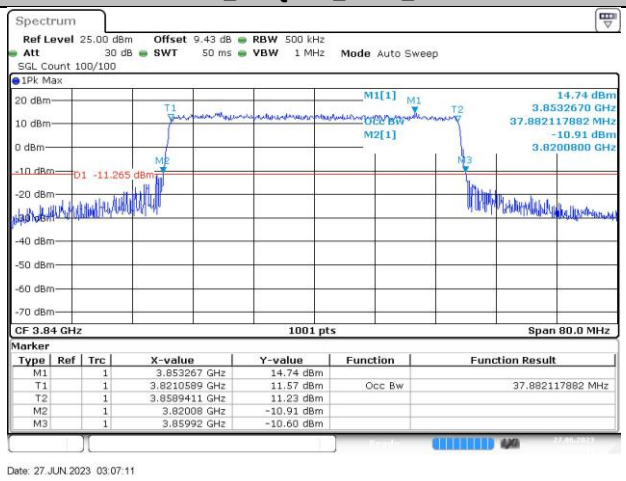
**MCH_DFT-Pi2BPSK_Outer_Full**



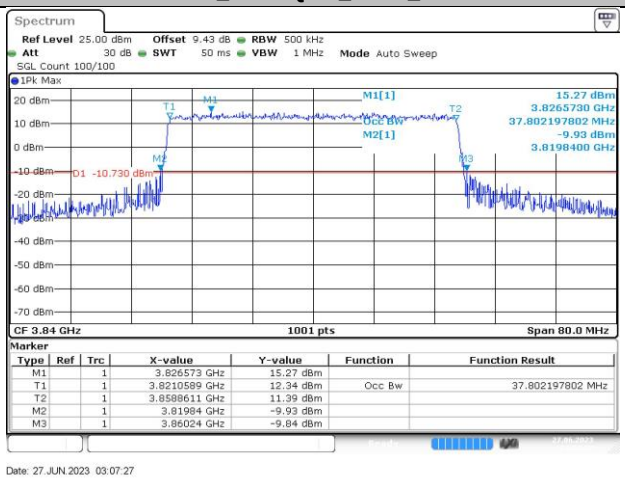

**MCH_DFT-QPSK_Outer_Full**



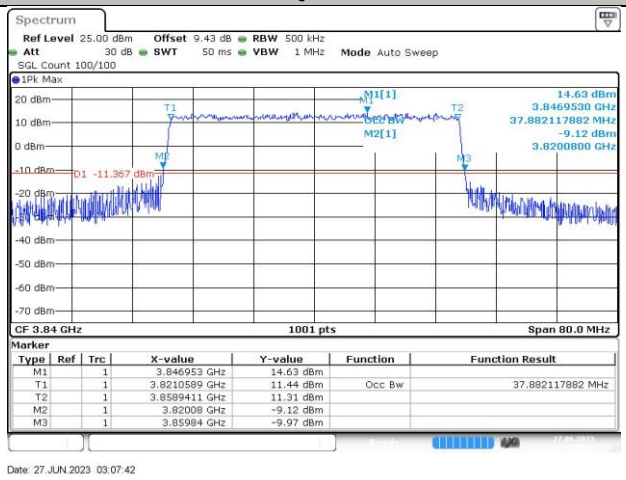

**MCH_CP-QPSK_Outer_Full**



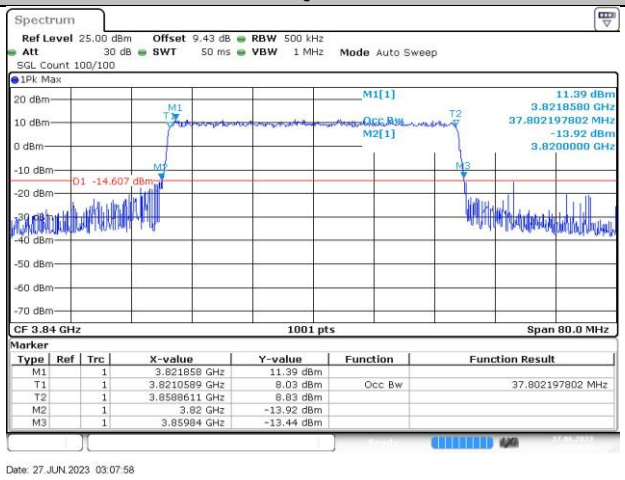

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**

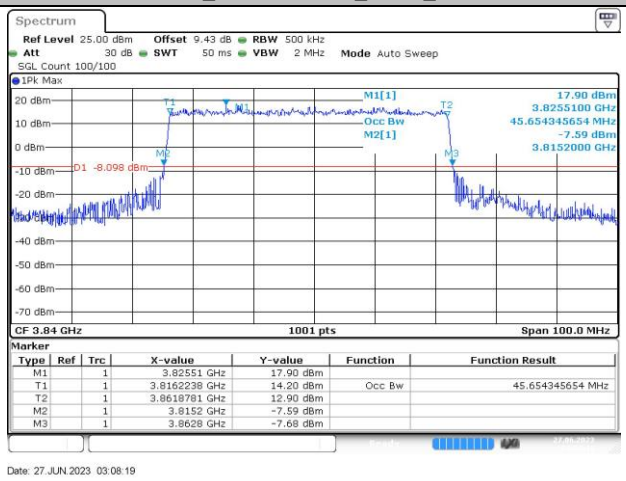



**MCH_CP-256QAM_Outer_Full**



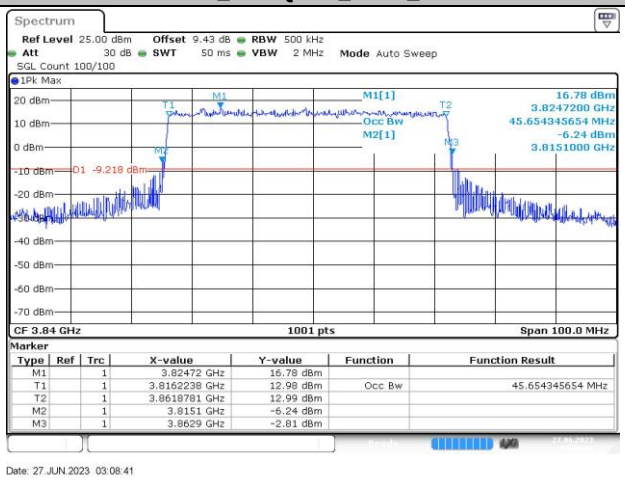
### 4.2.6. Bandwidth:50MHz

**MCH_DFT-Pi2BPSK_Outer_Full**



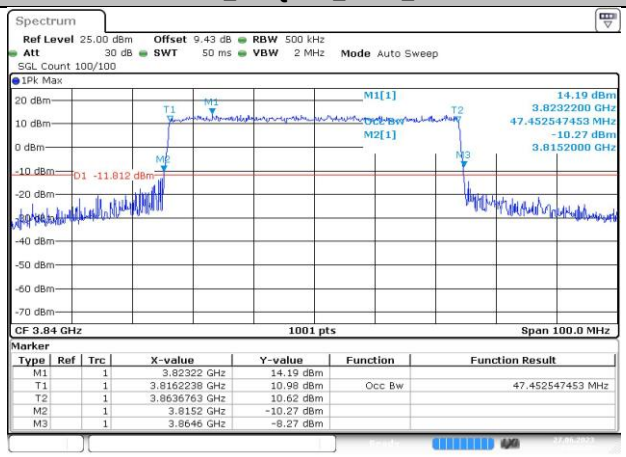

Date: 27.JUN.2023 03:08:19

**MCH_DFT-QPSK_Outer_Full**



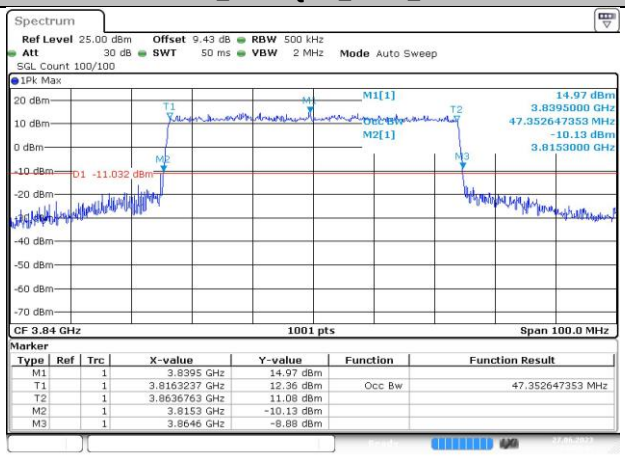

Date: 27.JUN.2023 03:08:41

**MCH_CP-QPSK_Outer_Full**



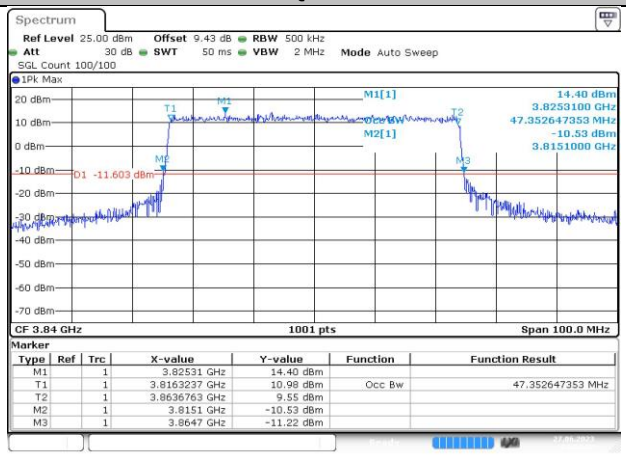

Date: 27.JUN.2023 03:08:57

**MCH_CP-16QAM_Outer_Full**



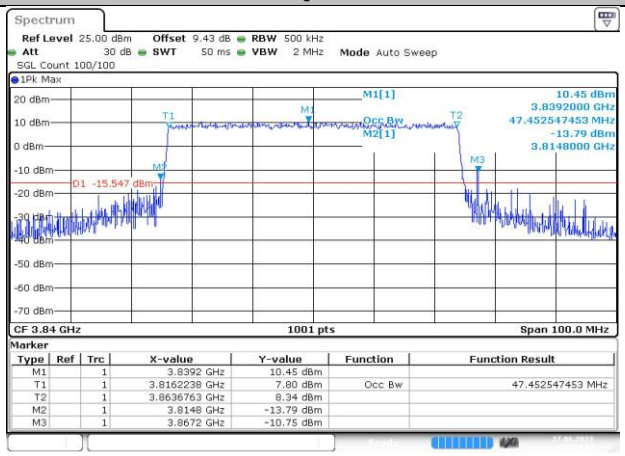

Date: 27.JUN.2023 03:09:13

**MCH_CP-64QAM_Outer_Full**




Date: 27.JUN.2023 03:09:28

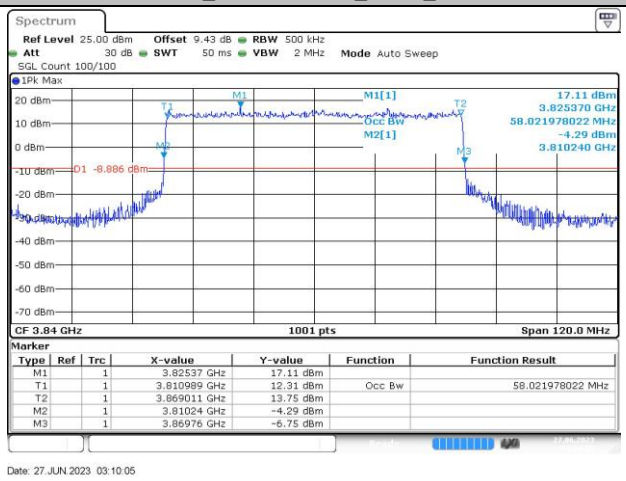
**MCH_CP-256QAM_Outer_Full**



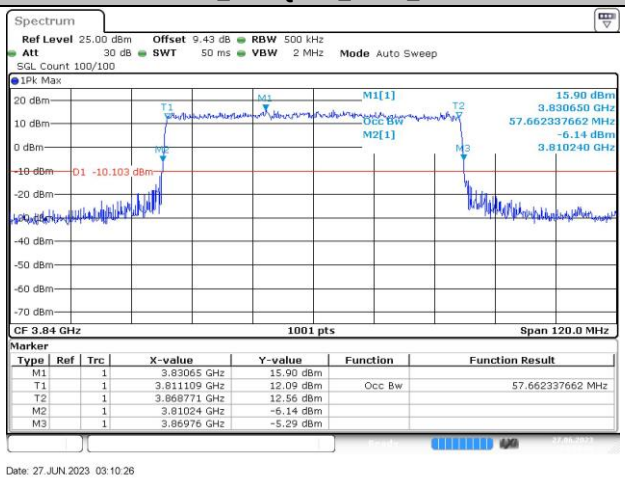

Date: 27.JUN.2023 03:09:44

### 4.2.7. Bandwidth:60MHz

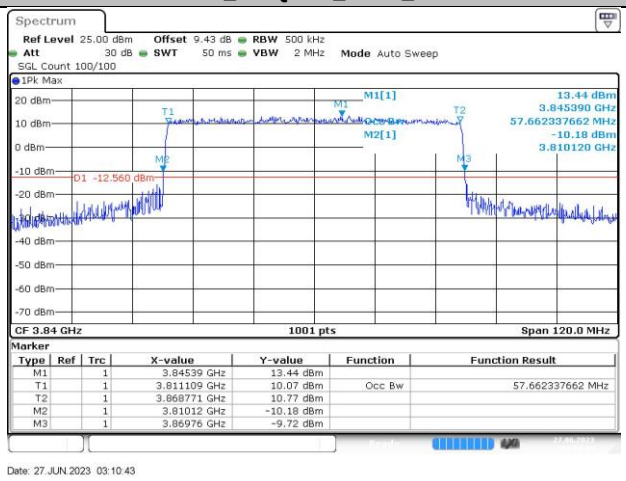
**MCH_DFT-Pi2BPSK_Outer_Full**



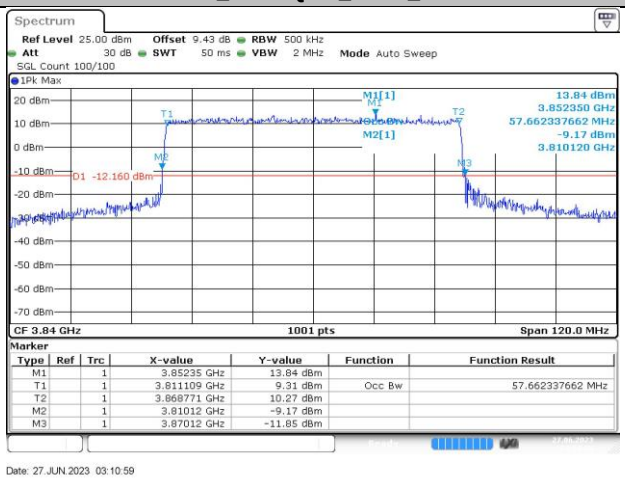

**MCH_DFT-QPSK_Outer_Full**



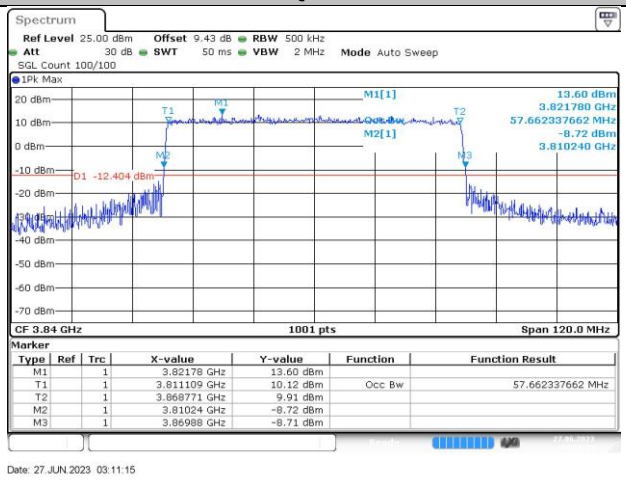

**MCH_CP-QPSK_Outer_Full**



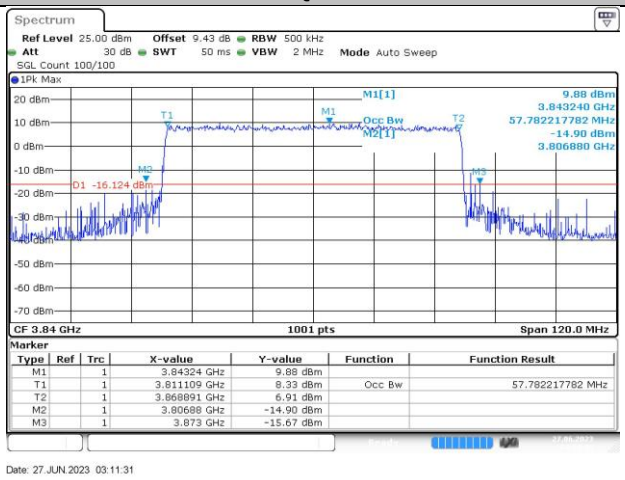

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**

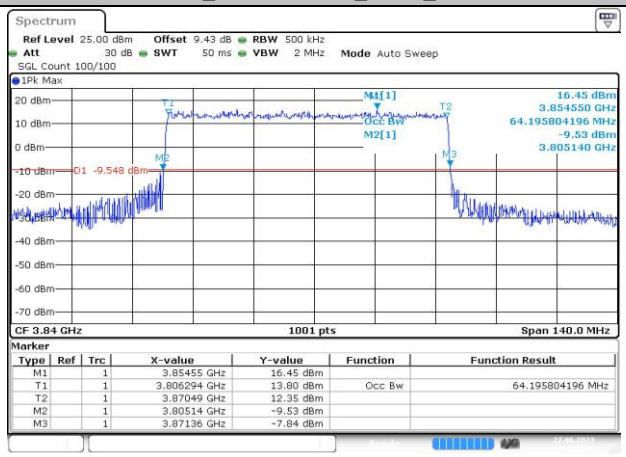



**MCH_CP-256QAM_Outer_Full**

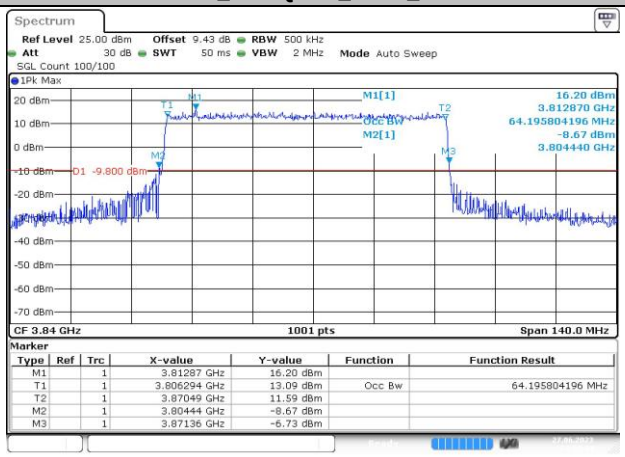


### 4.2.8. Bandwidth:70MHz

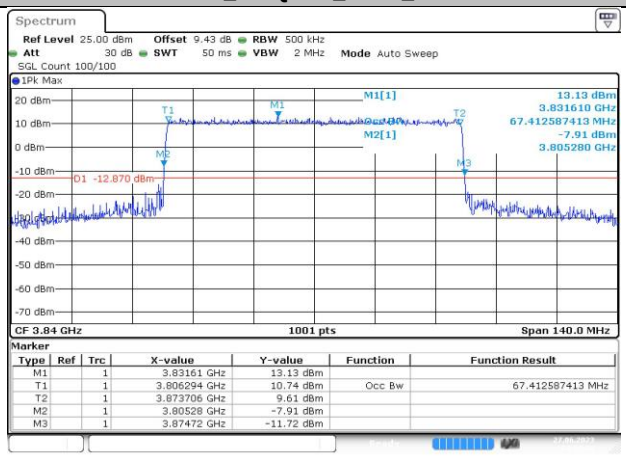
**MCH_DFT-Pi2BPSK_Outer_Full**



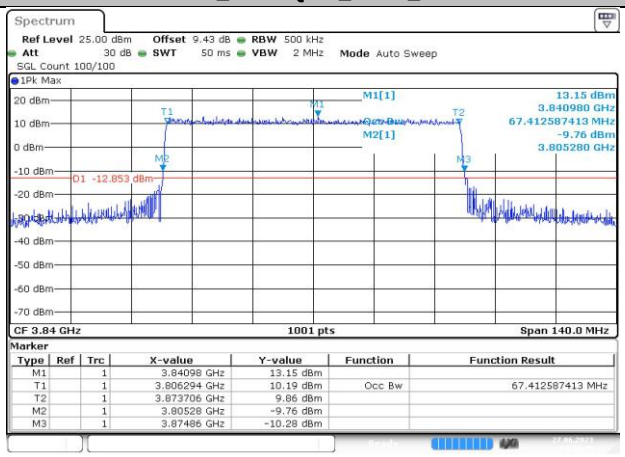

**MCH_DFT-QPSK_Outer_Full**



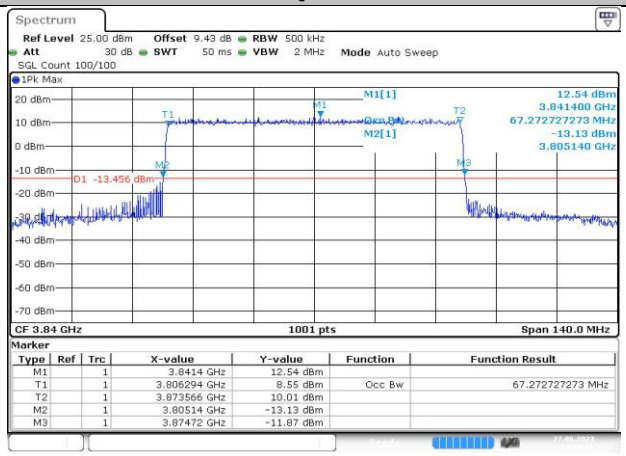

**MCH_CP-QPSK_Outer_Full**



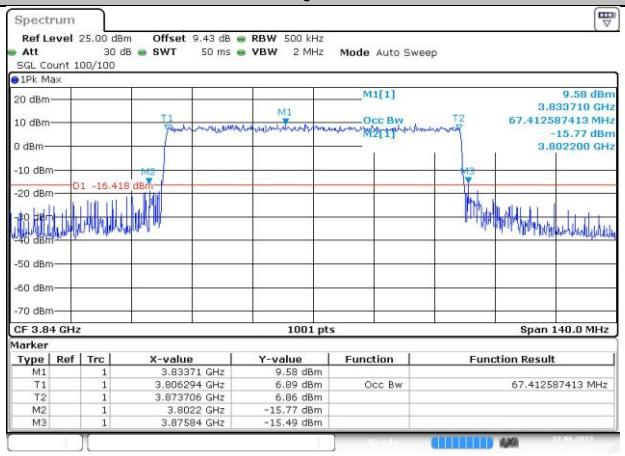

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**

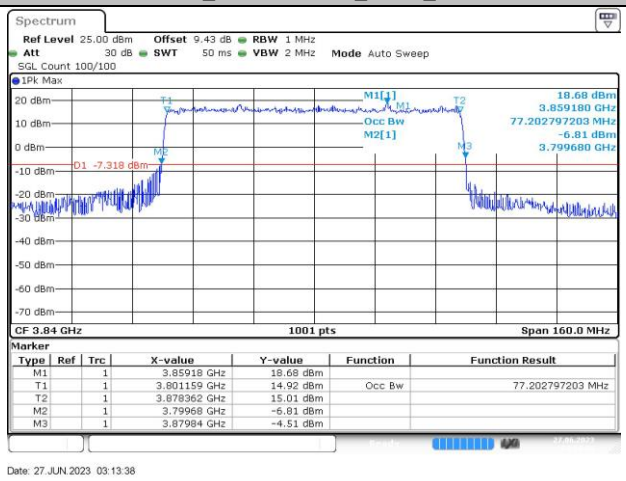



**MCH_CP-256QAM_Outer_Full**

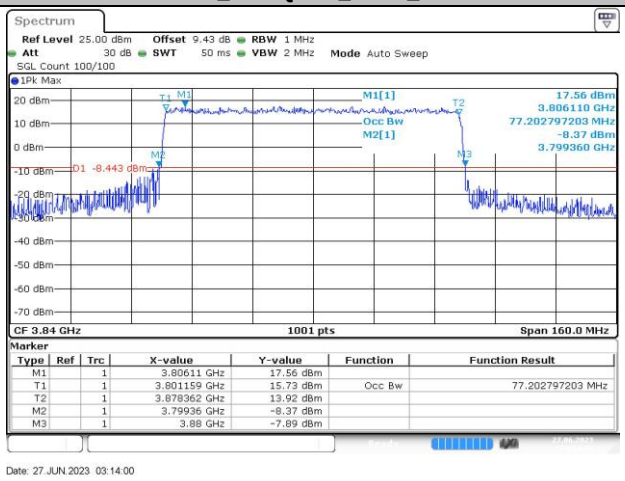


### 4.2.9. Bandwidth:80MHz

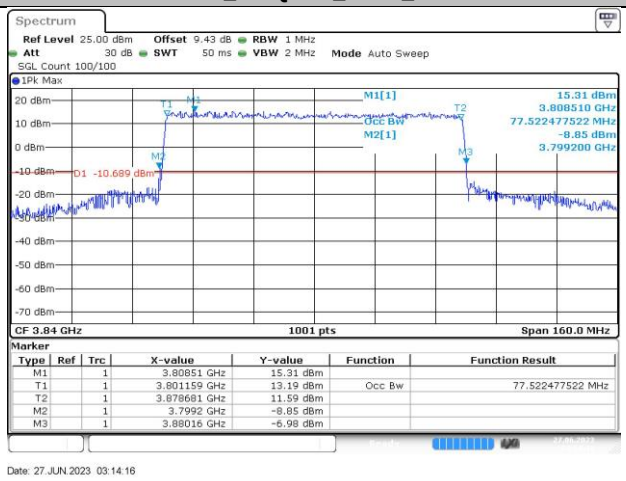
**MCH_DFT-Pi2BPSK_Outer_Full**



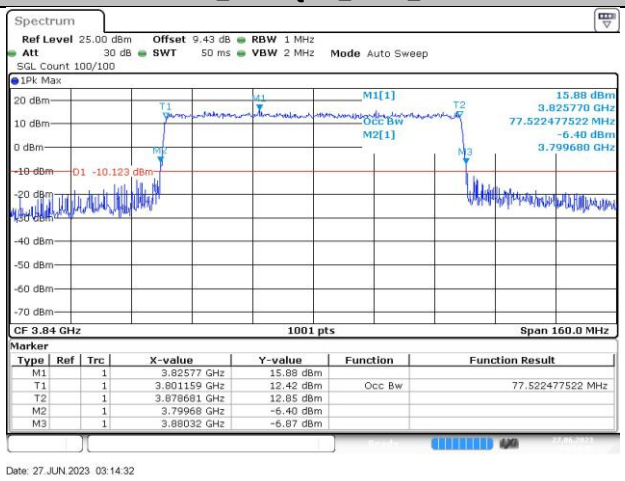

**MCH_DFT-QPSK_Outer_Full**



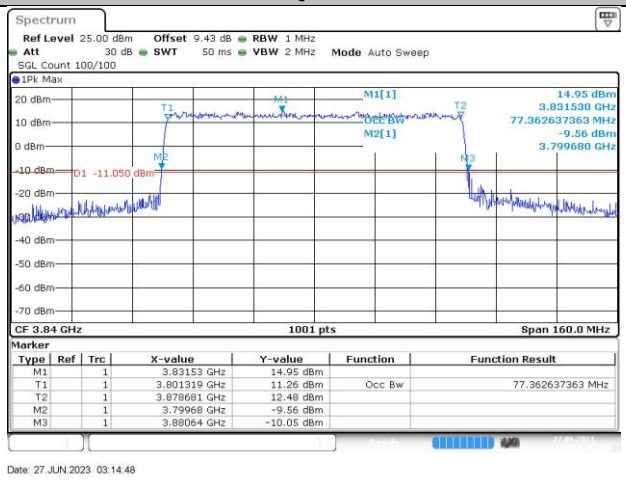

**MCH_CP-QPSK_Outer_Full**



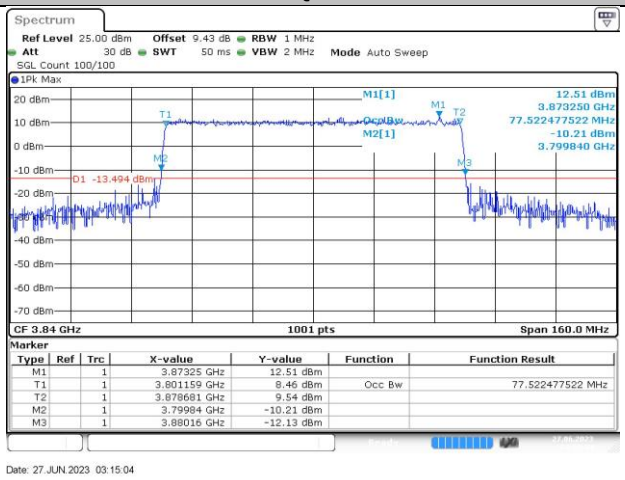

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**

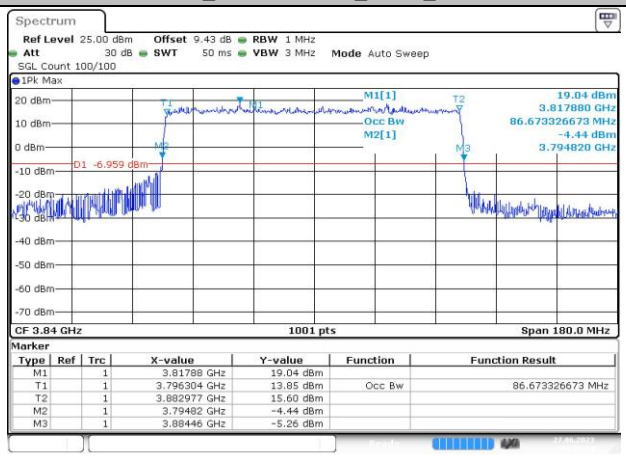



**MCH_CP-256QAM_Outer_Full**

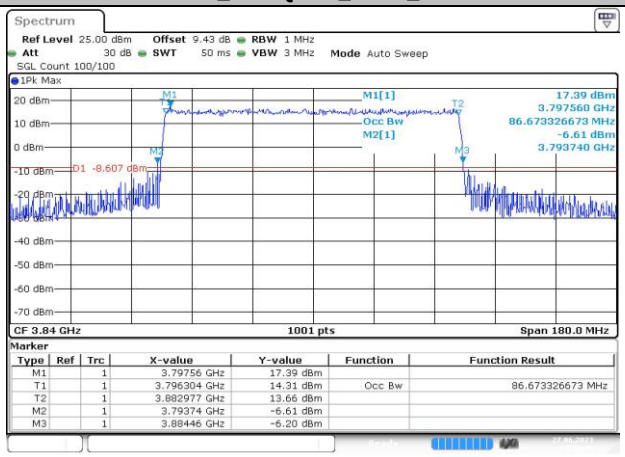


### 4.2.10. Bandwidth:90MHz

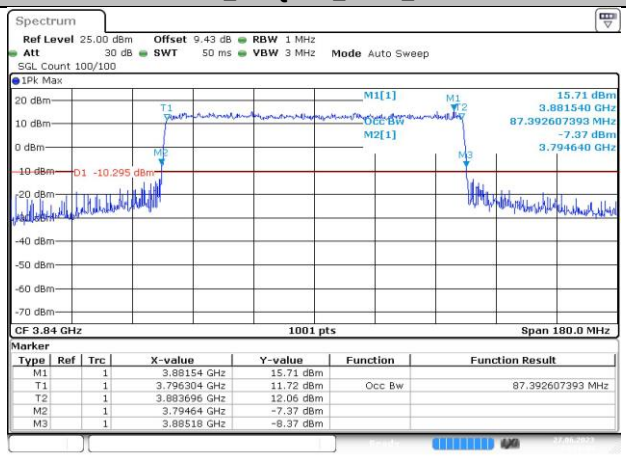
**MCH_DFT-Pi2BPSK_Outer_Full**



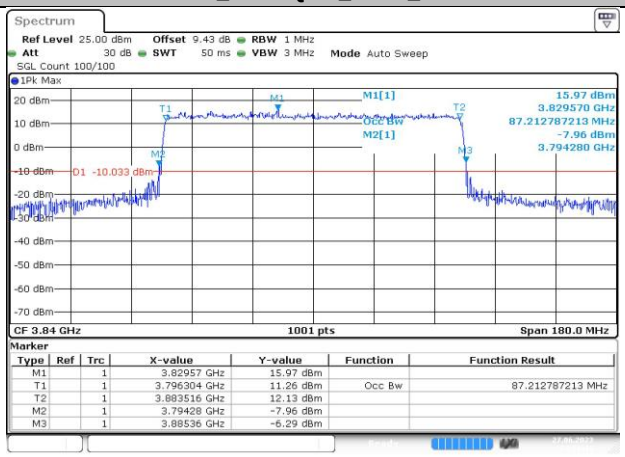

**MCH_DFT-QPSK_Outer_Full**



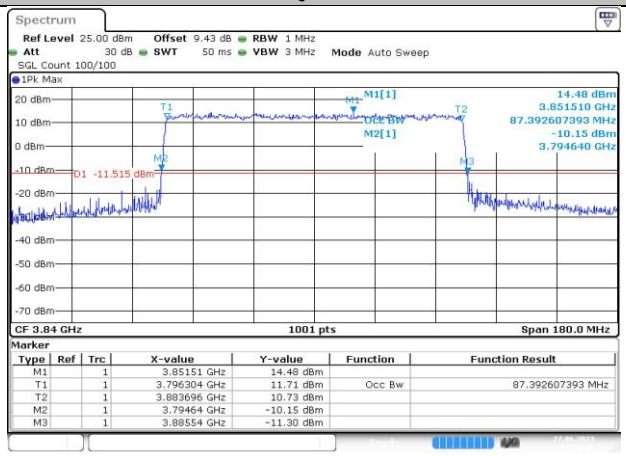

**MCH_CP-QPSK_Outer_Full**



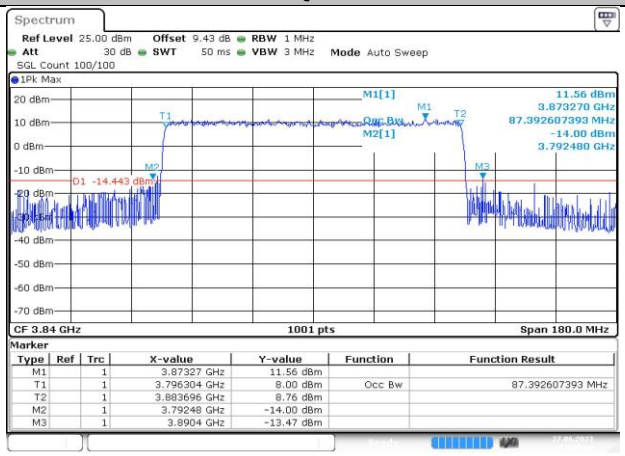

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**

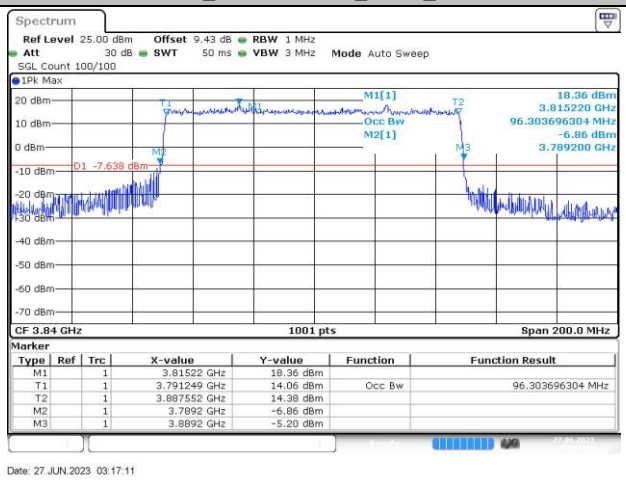



**MCH_CP-256QAM_Outer_Full**

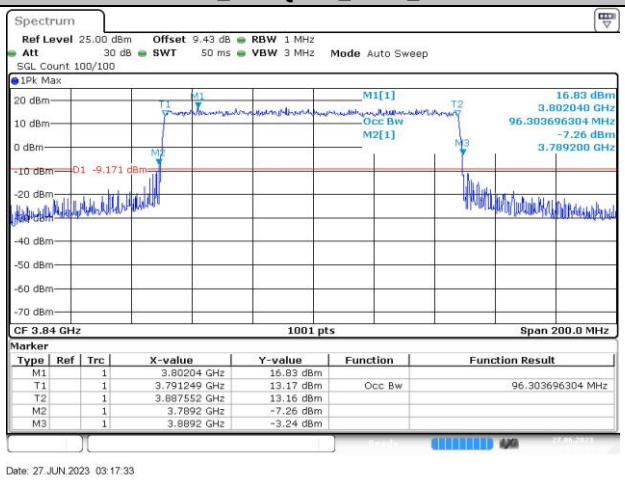


### 4.2.11. Bandwidth:100MHz

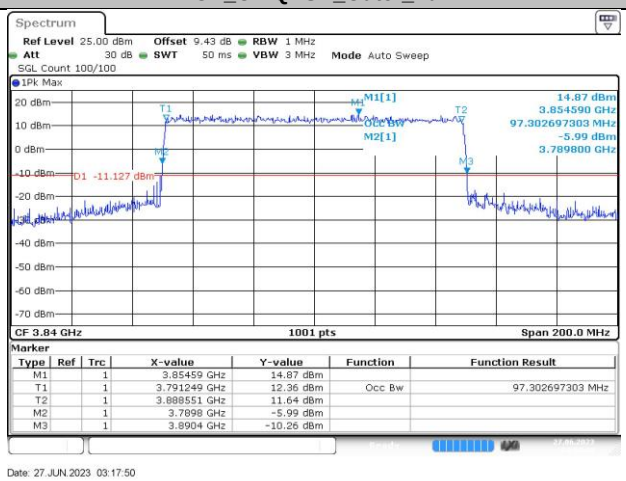
**MCH_DFT-Pi2BPSK_Outer_Full**



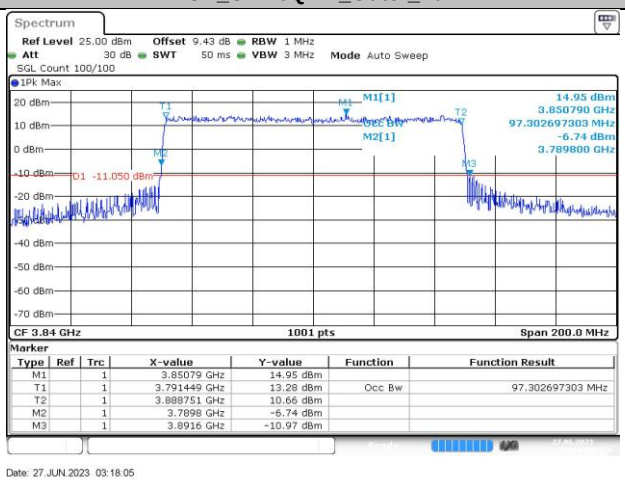

**MCH_DFT-QPSK_Outer_Full**



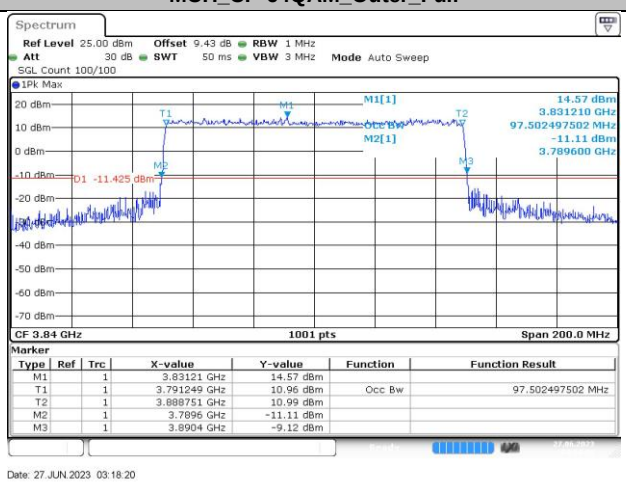

**MCH_CP-QPSK_Outer_Full**



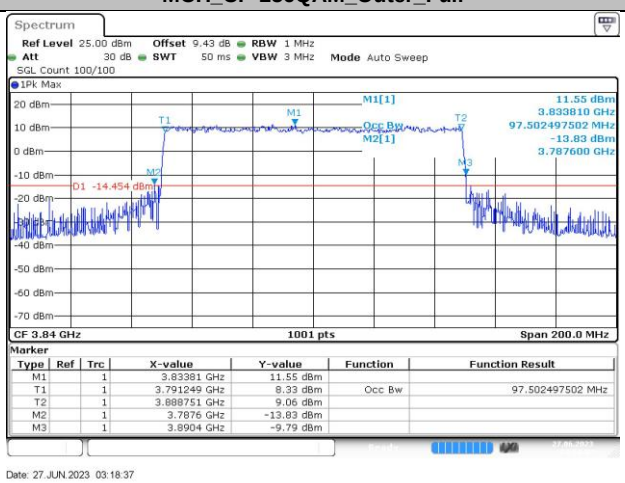

**MCH_CP-16QAM_Outer_Full**




**MCH_CP-64QAM_Outer_Full**




**MCH_CP-256QAM_Outer_Full**

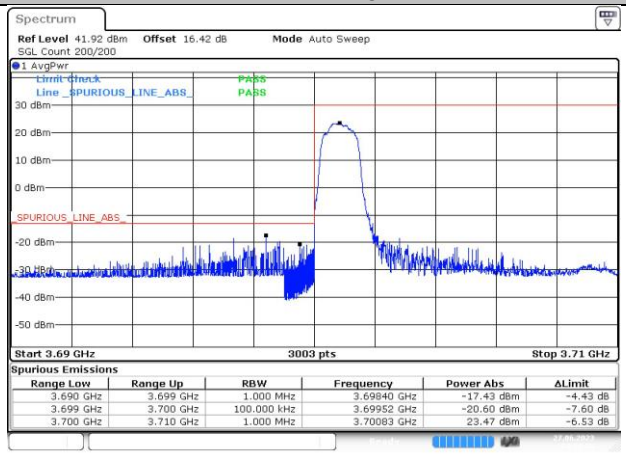


# 5. Conducted Band Edges

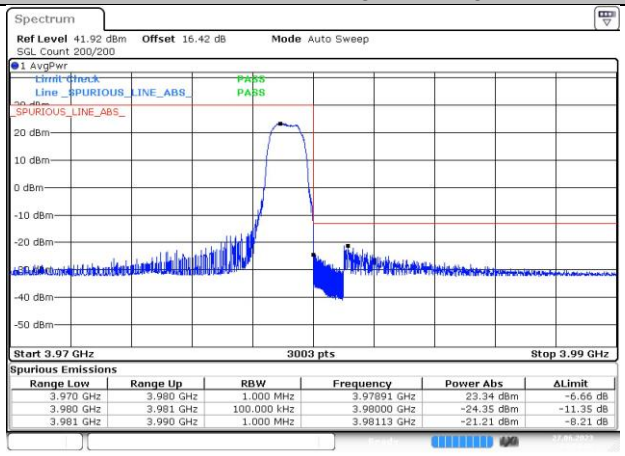
## 5.1. Test Plots

### 5.1.1. Bandwidth:10MHz

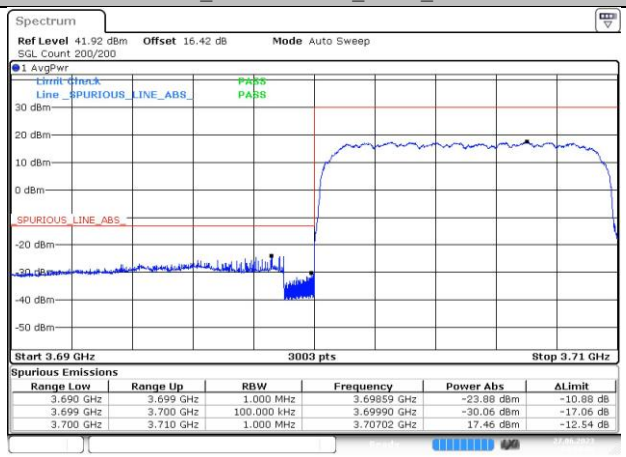
**LCH_DFT-Pi2BPSK_Edge_1RB_Left**



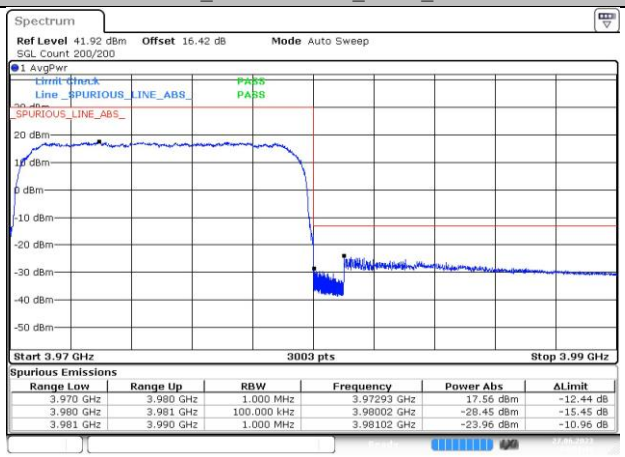

**HCH_DFT-Pi2BPSK_Edge_1RB_Right**



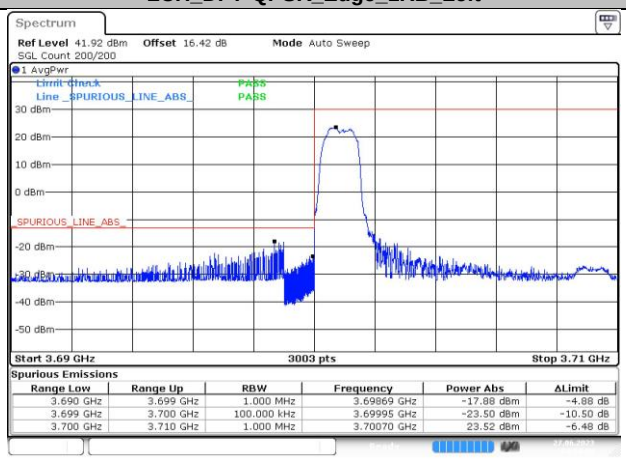

**LCH_DFT-Pi2BPSK_Outer_Full**



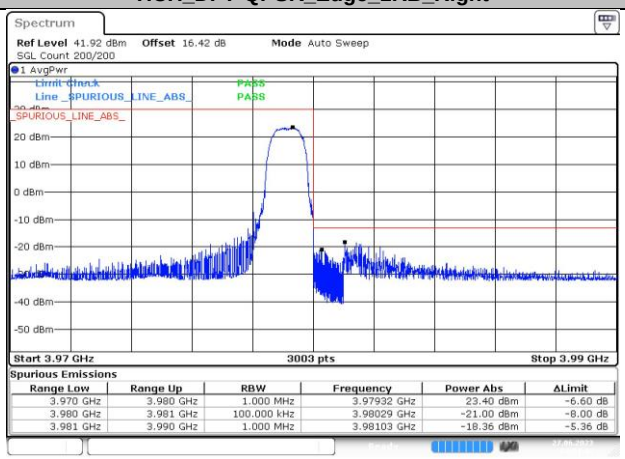

**HCH_DFT-Pi2BPSK_Outer_Full**



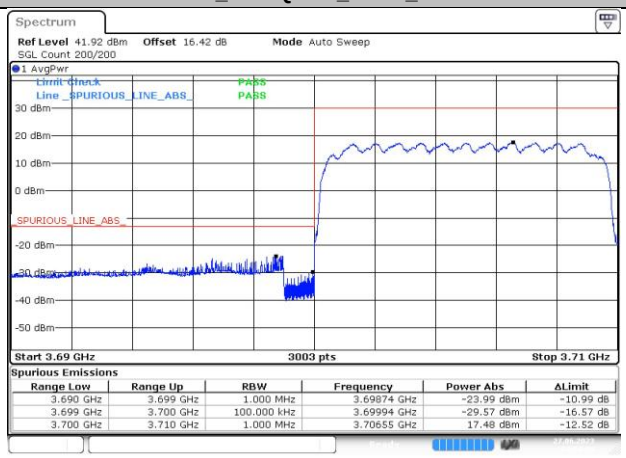

**LCH_DFT-QPSK_Edge_1RB_Left**

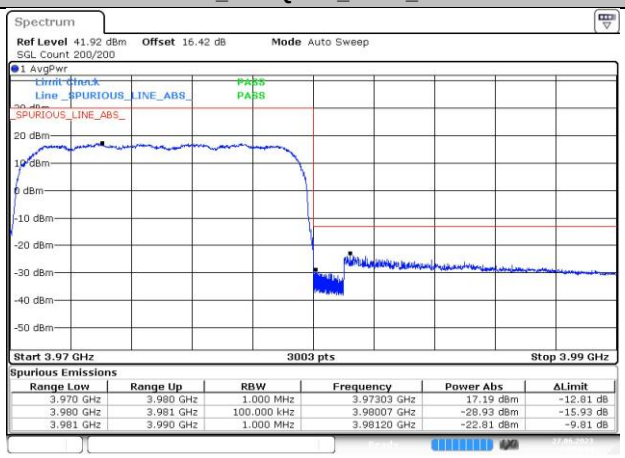



**HCH_DFT-QPSK_Edge_1RB_Right**



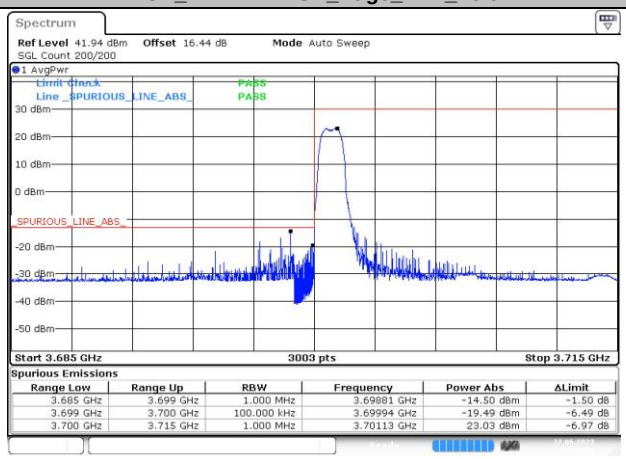
| LCH_DFT-QPSK_Outer_Full | HCH_DFT-QPSK_Outer_Full |
|---|---|

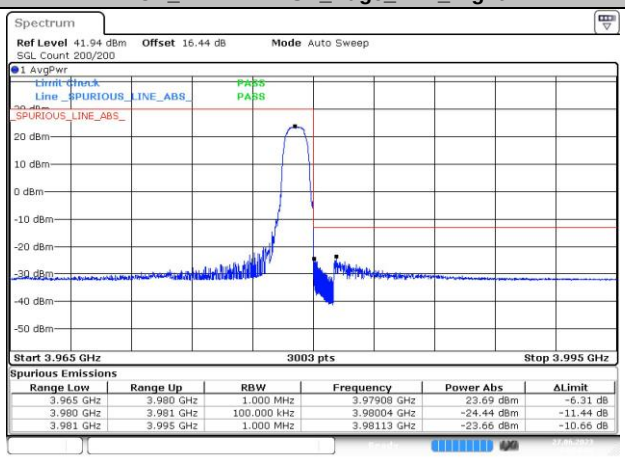


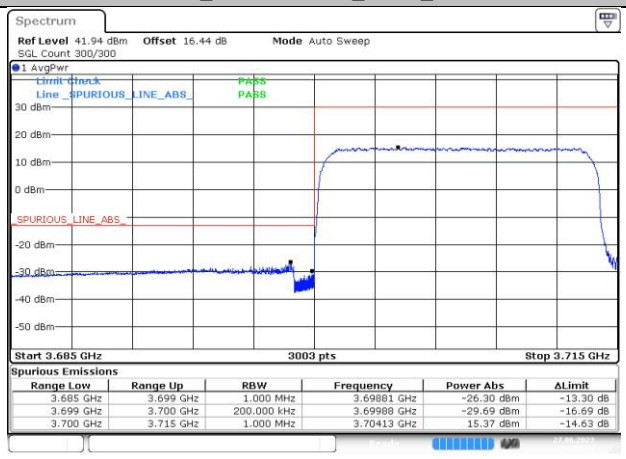

### 5.1.2. Bandwidth:15MHz

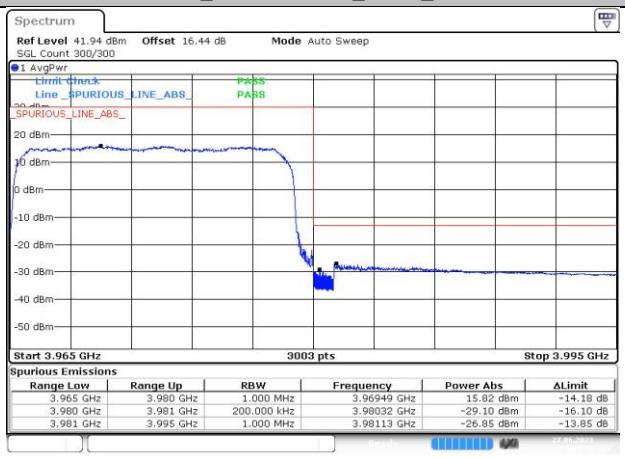
| LCH_DFT-Pi2BPSK_Edge_1RB_Left | HCH_DFT-Pi2BPSK_Edge_1RB_Right |
|---|---|



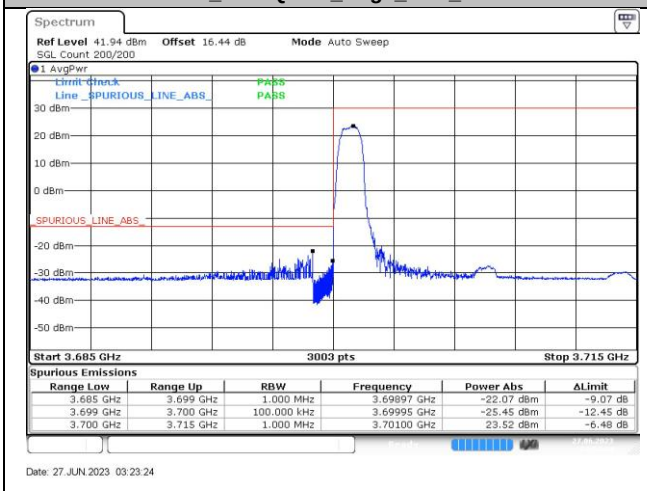




| LCH_DFT-Pi2BPSK_Outer_Full | HCH_DFT-Pi2BPSK_Outer_Full |
|---|---|

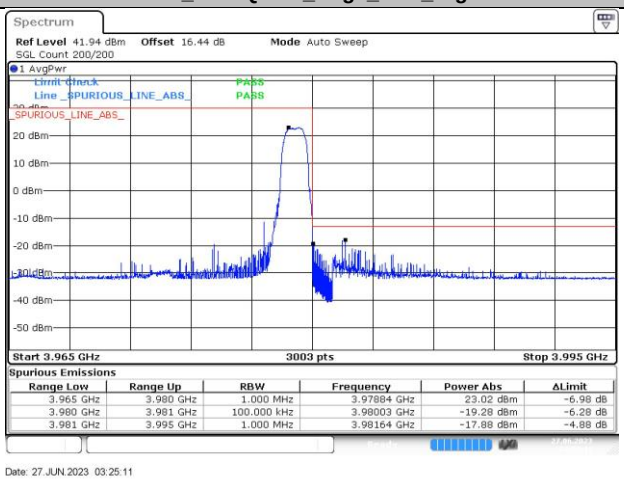


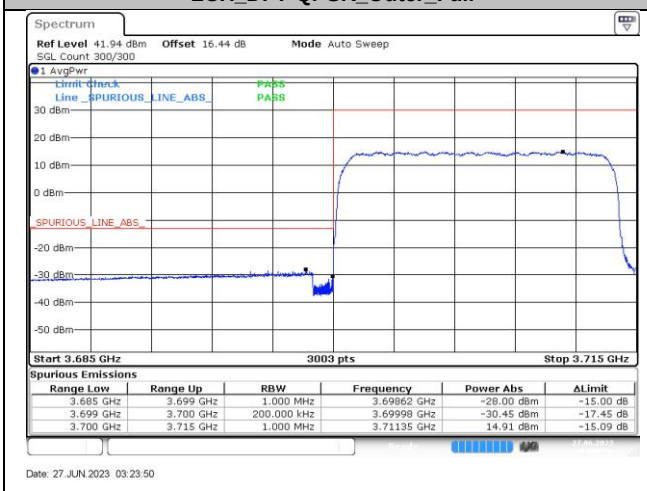
**LCH_DFT-QPSK_Edge_1RB_Left**



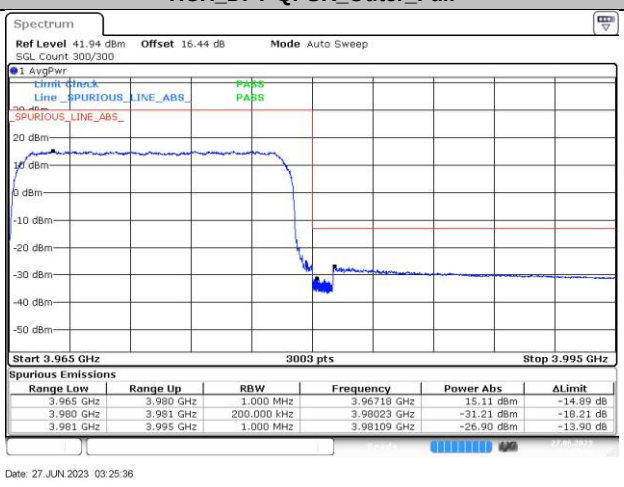

**HCH_DFT-QPSK_Edge_1RB_Right**




**LCH_DFT-QPSK_Outer_Full**

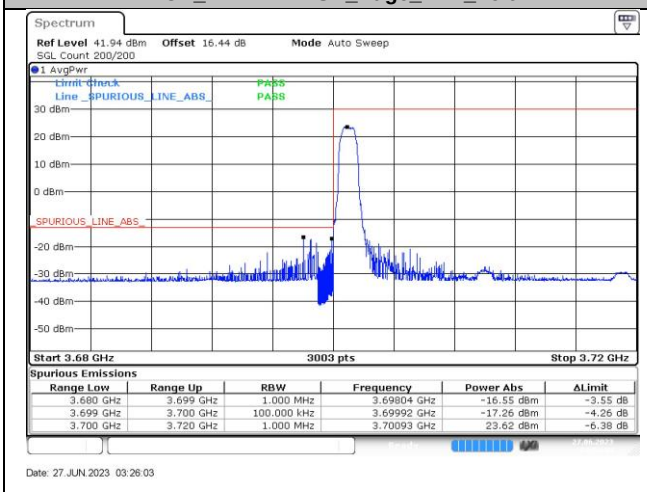



**HCH_DFT-QPSK_Outer_Full**

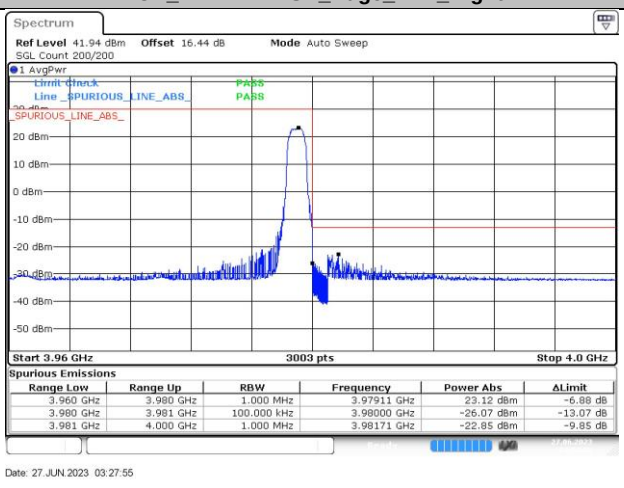



### 5.1.3. Bandwidth:20MHz

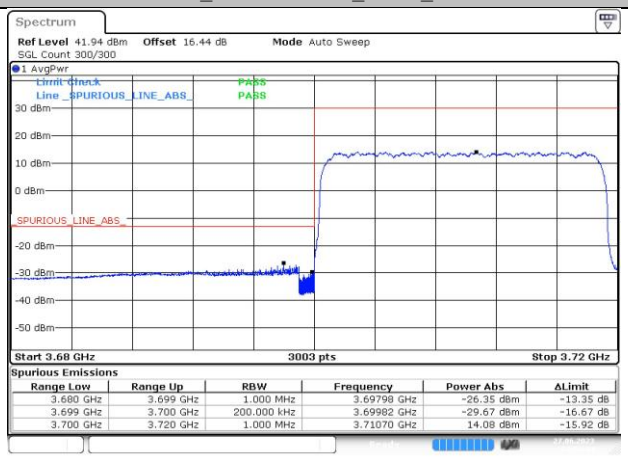
**LCH_DFT-Pi2BPSK_Edge_1RB_Left**



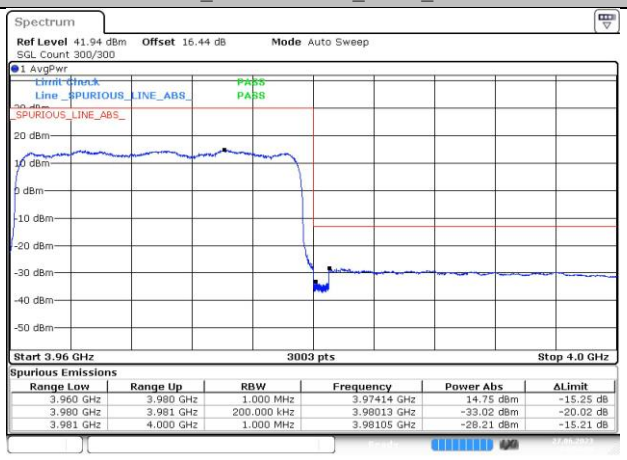

**HCH_DFT-Pi2BPSK_Edge_1RB_Right**



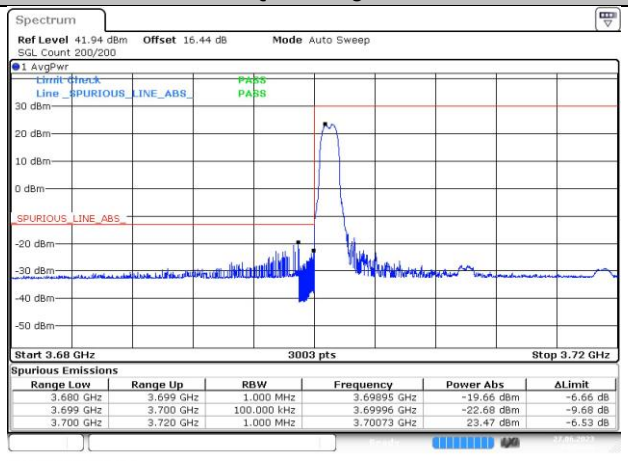
**LCH_DFT-Pi2BPSK_Outer_Full**



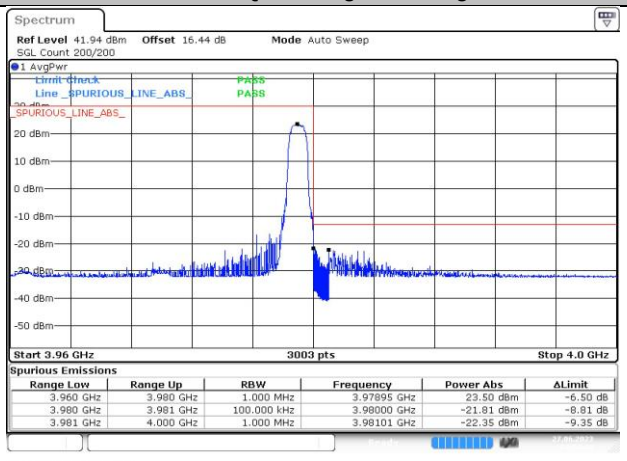

**HCH_DFT-Pi2BPSK_Outer_Full**



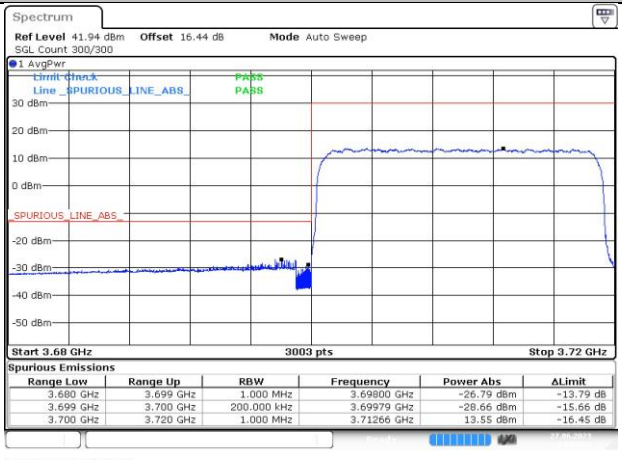

**LCH_DFT-QPSK_Edge_1RB_Left**



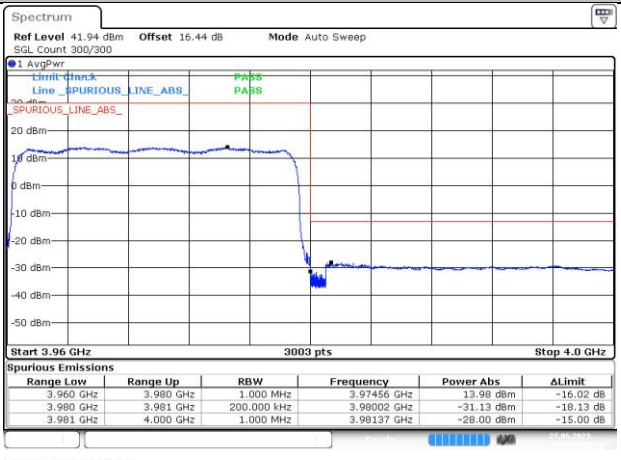

**HCH_DFT-QPSK_Edge_1RB_Right**




**LCH_DFT-QPSK_Outer_Full**

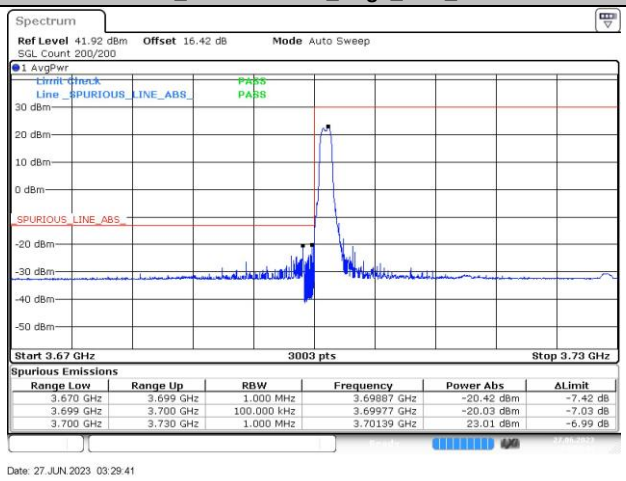



**HCH_DFT-QPSK_Outer_Full**

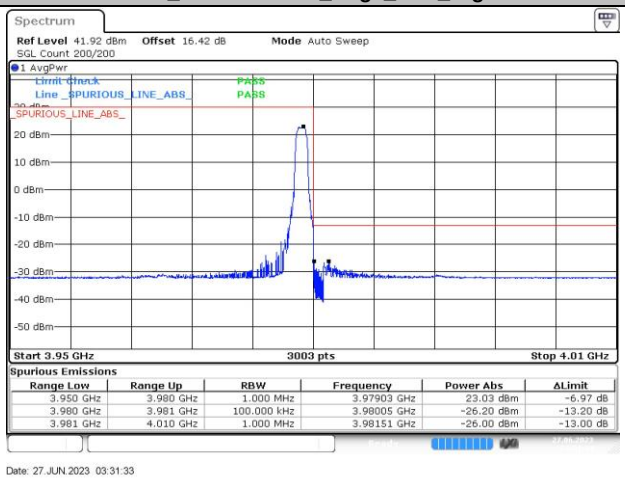


### 5.1.4. Bandwidth:30MHz

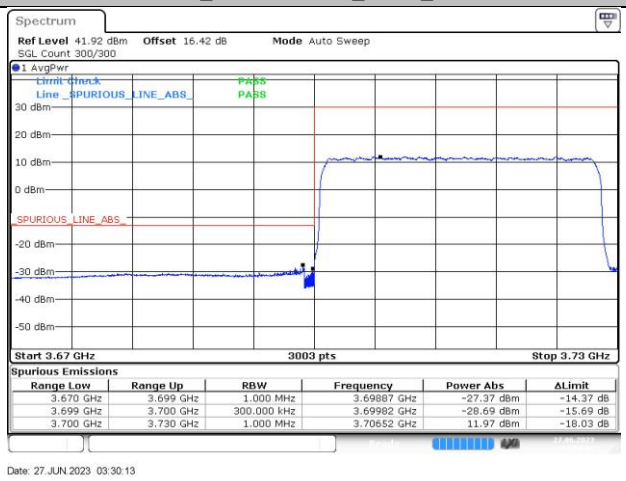
**LCH_DFT-Pi2BPSK_Edge_1RB_Left**



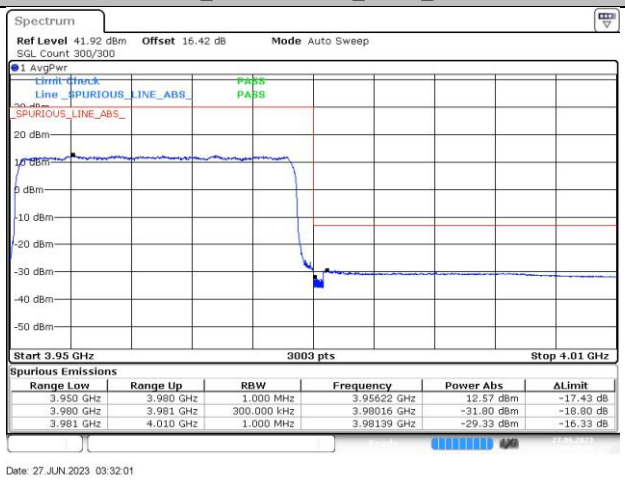

**HCH_DFT-Pi2BPSK_Edge_1RB_Right**



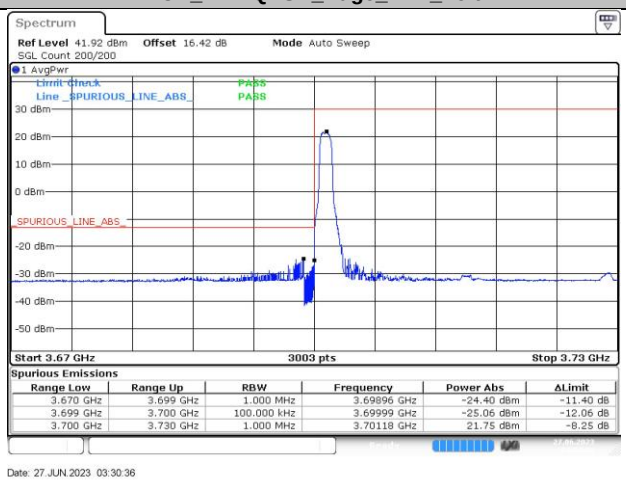

**LCH_DFT-Pi2BPSK_Outer_Full**



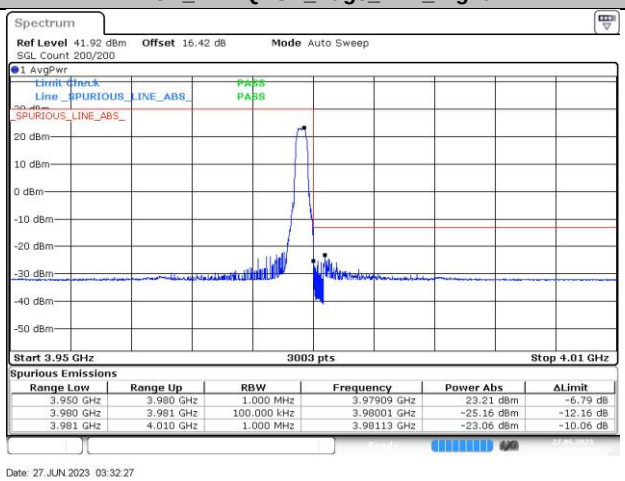

**HCH_DFT-Pi2BPSK_Outer_Full**



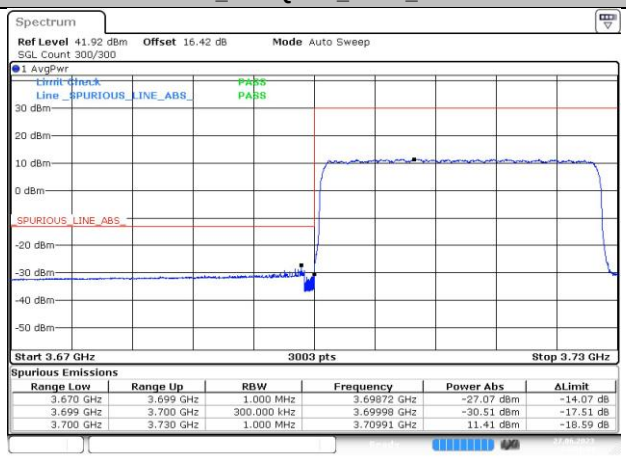

**LCH_DFT-QPSK_Edge_1RB_Left**

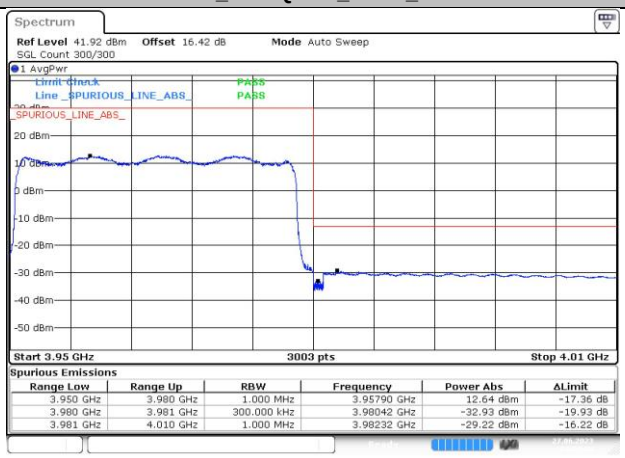



**HCH_DFT-QPSK_Edge_1RB_Right**



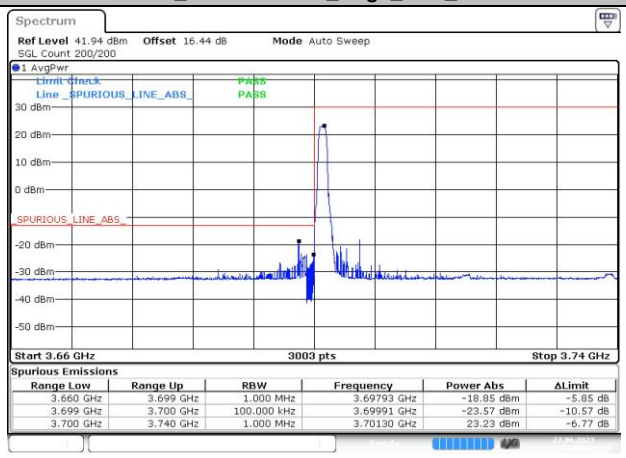
| LCH_DFT-QPSK_Outer_Full | HCH_DFT-QPSK_Outer_Full |
|---|---|

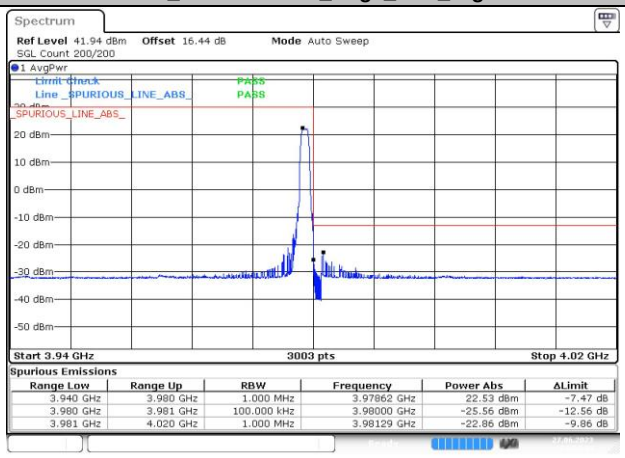


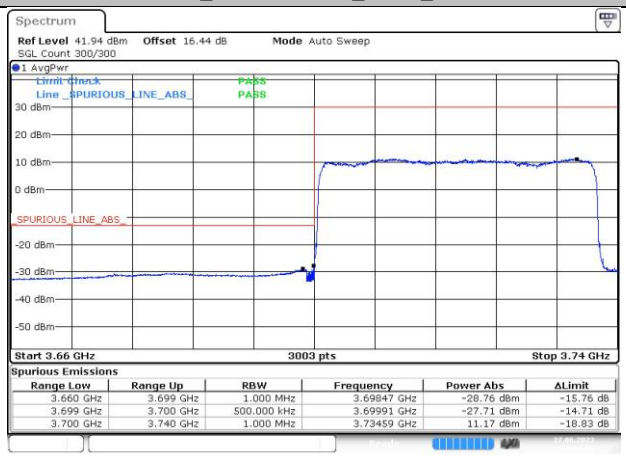

### 5.1.5. Bandwidth:40MHz

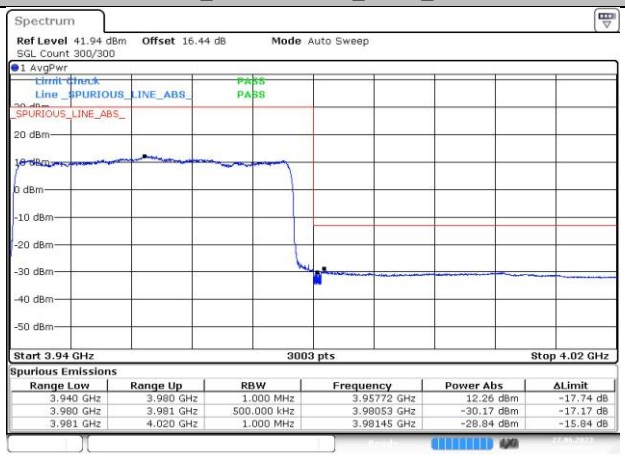
| LCH_DFT-Pi2BPSK_Edge_1RB_Left | HCH_DFT-Pi2BPSK_Edge_1RB_Right |
|---|---|



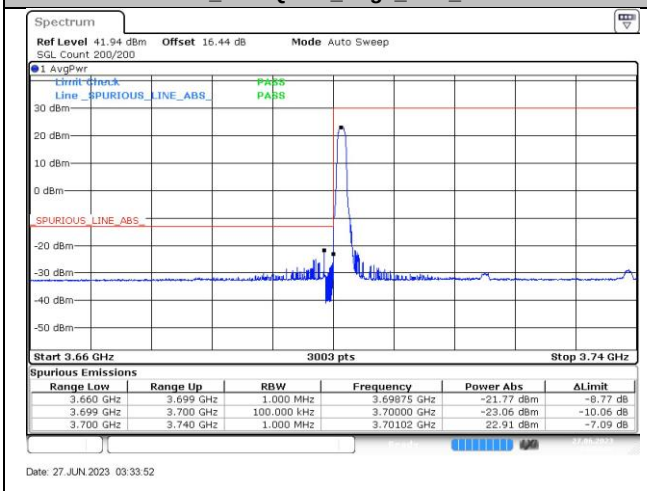




| LCH_DFT-Pi2BPSK_Outer_Full | HCH_DFT-Pi2BPSK_Outer_Full |
|---|---|

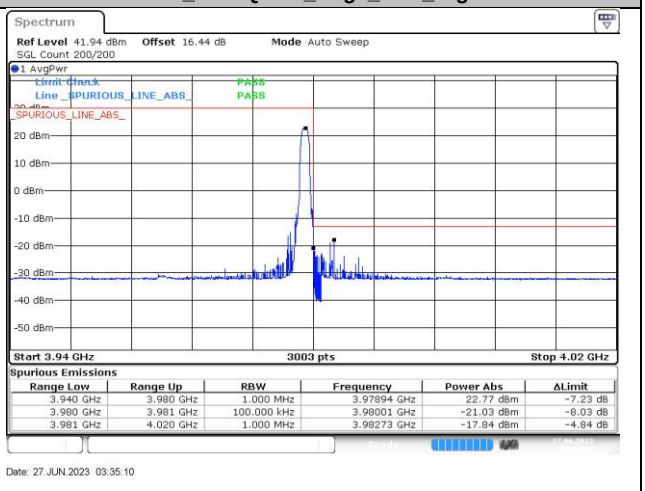


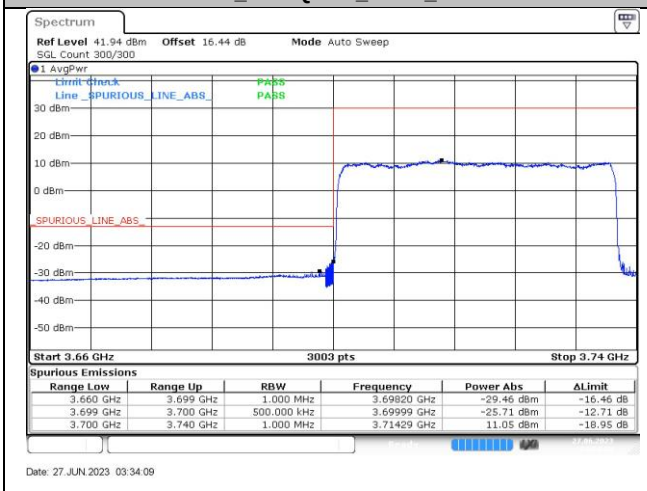
**LCH_DFT-QPSK_Edge_1RB_Left**



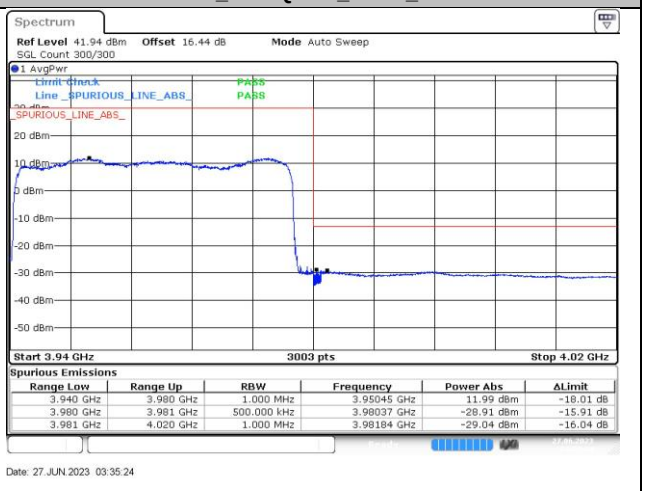

**HCH_DFT-QPSK_Edge_1RB_Right**




**LCH_DFT-QPSK_Outer_Full**

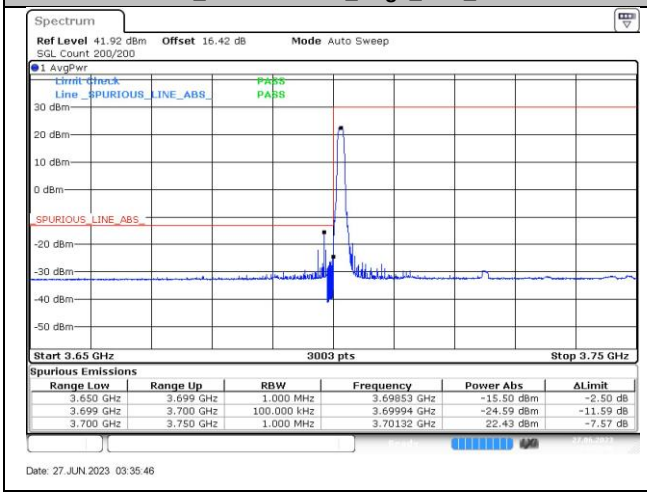


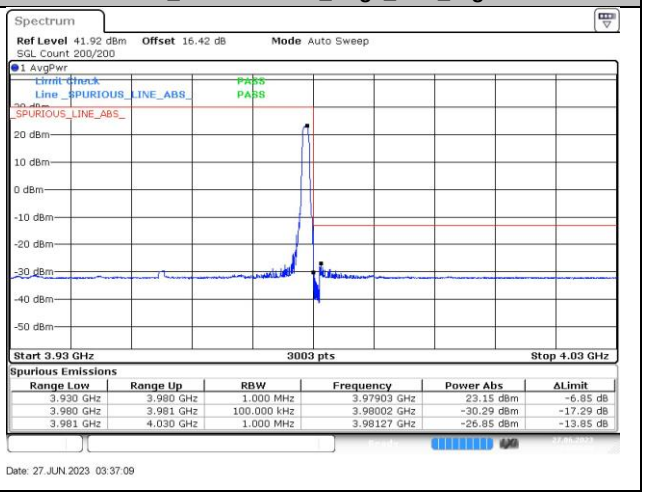

**HCH_DFT-QPSK_Outer_Full**




### 5.1.6. Bandwidth:50MHz

**LCH_DFT-Pi2BPSK_Edge_1RB_Left**




**HCH_DFT-Pi2BPSK_Edge_1RB_Right**