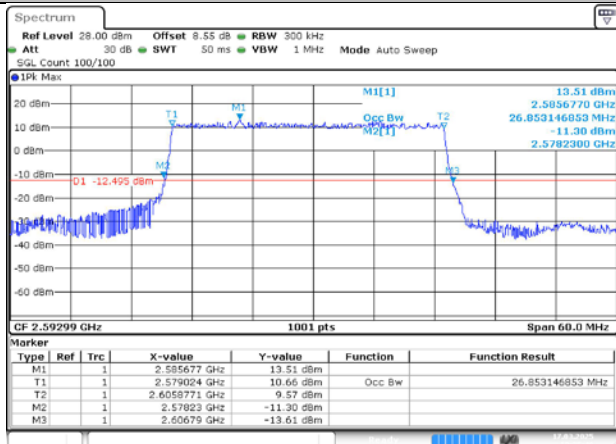


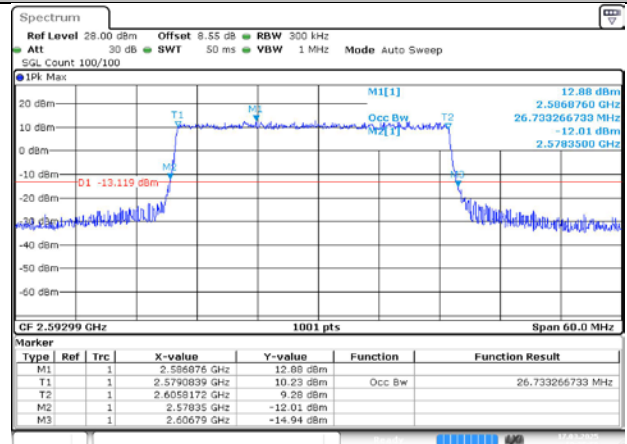
n41A / 30KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



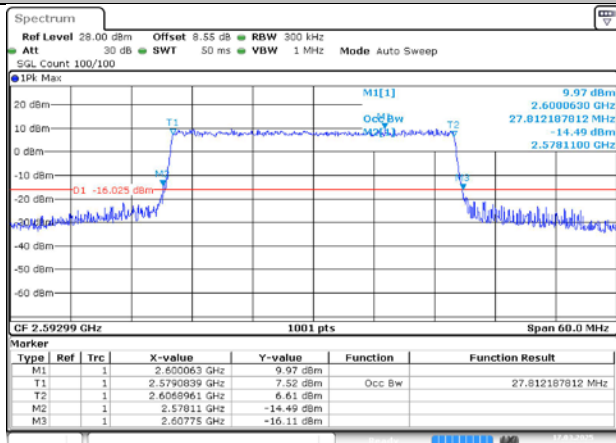
Date: 17.MAR.2025 19:25:48

MCH / DFT-QPSK / Outer_Full



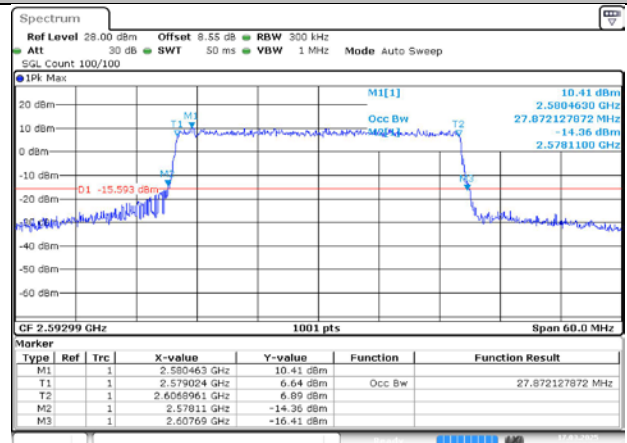
Date: 17.MAR.2025 19:25:59

MCH / CP-QPSK / Outer_Full



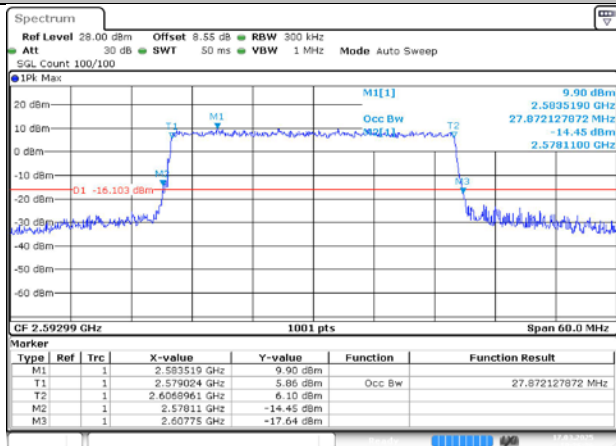
Date: 17.MAR.2025 19:26:09

MCH / CP-16QAM / Outer_Full



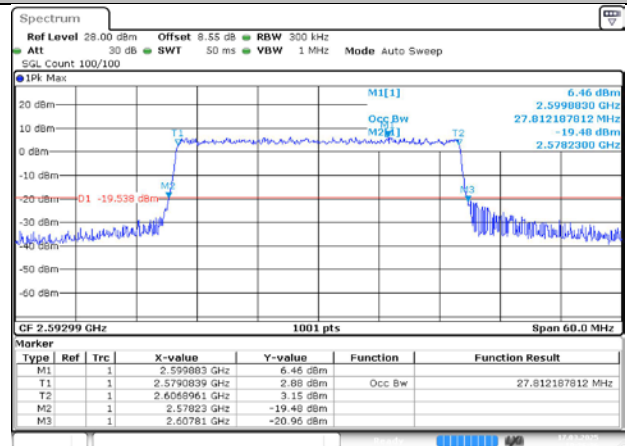
Date: 17.MAR.2025 19:26:17

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:26:26

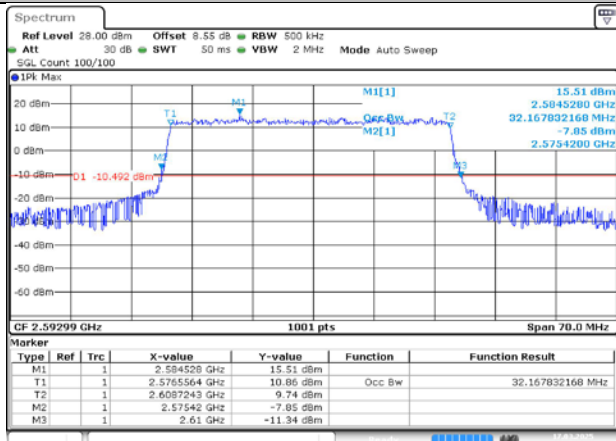
MCH / CP-256QAM / Outer_Full



Date: 17.MAR.2025 19:26:36

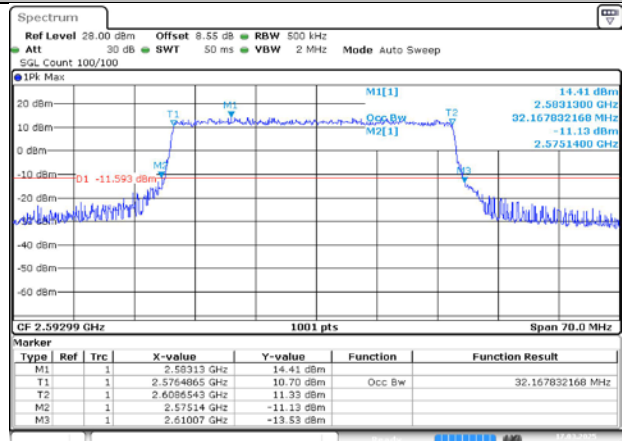
n41A / 30KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



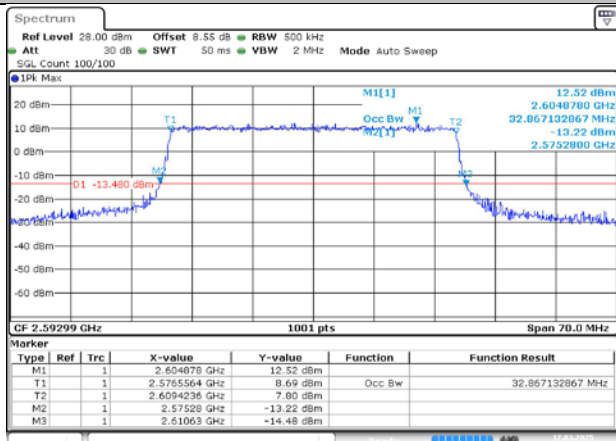
Date: 17.MAR.2025 19:26:49

MCH / DFT-QPSK / Outer_Full



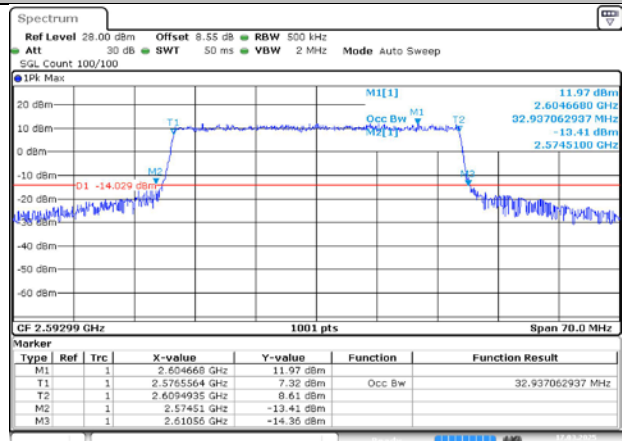
Date: 17.MAR.2025 19:26:59

MCH / CP-QPSK / Outer_Full



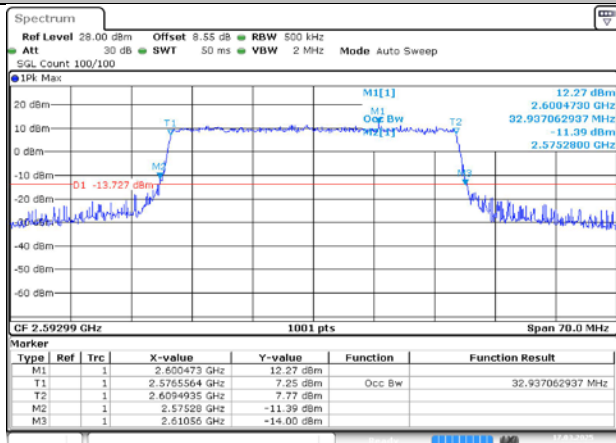
Date: 17.MAR.2025 19:27:09

MCH / CP-16QAM / Outer_Full



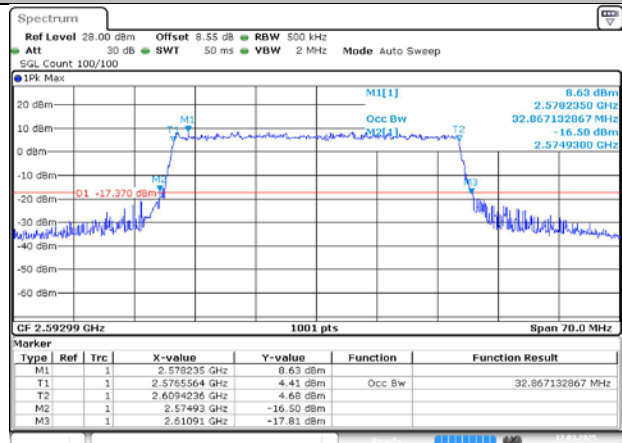
Date: 17.MAR.2025 19:27:18

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:27:26

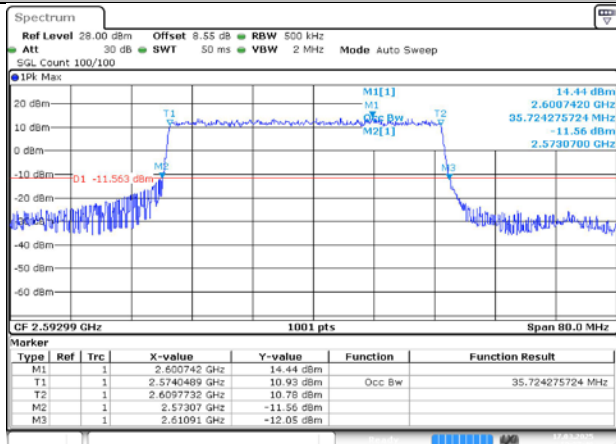
MCH / CP-256QAM / Outer_Full



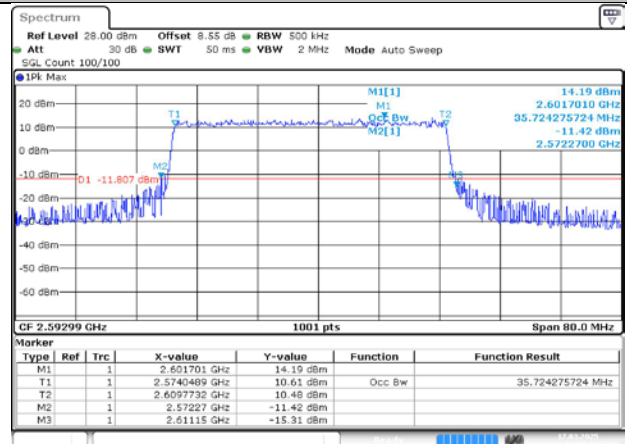
Date: 17.MAR.2025 19:27:37

n41A / 30KHz / 40MHz

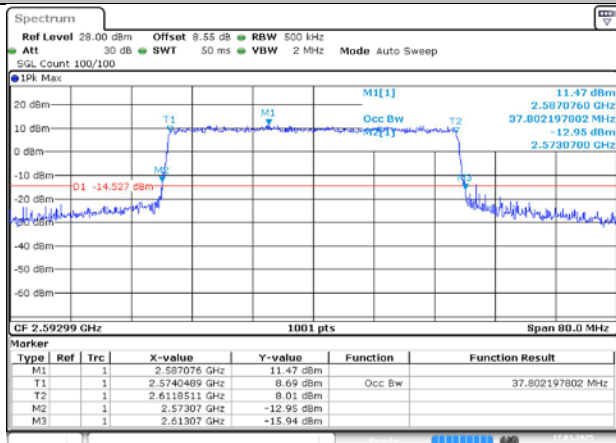
MCH / DFT-Pi2BPSK / Outer_Full



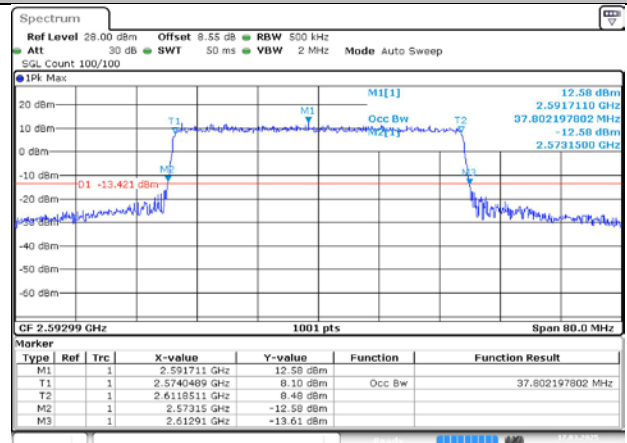
MCH / DFT-QPSK / Outer_Full



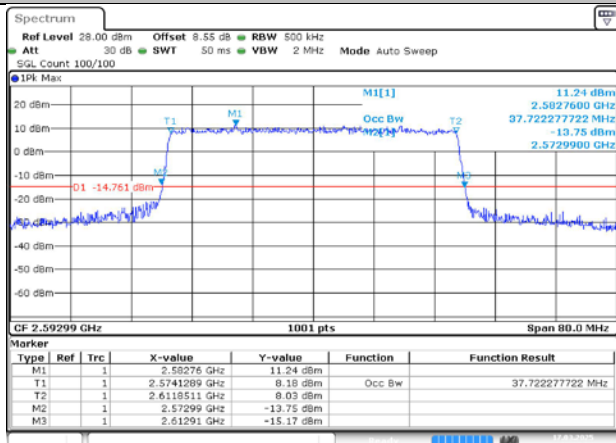
MCH / CP-QPSK / Outer_Full



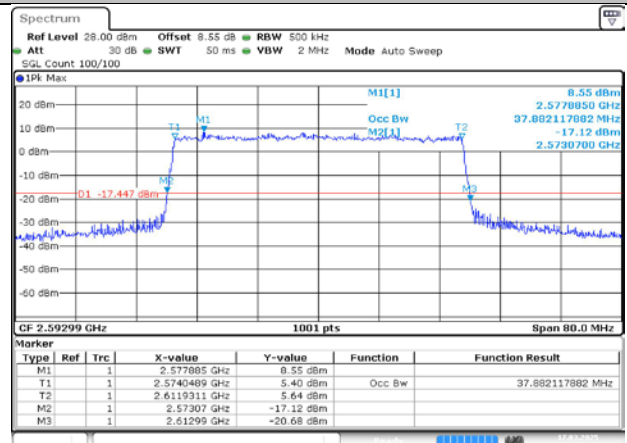
MCH / CP-16QAM / Outer_Full



MCH / CP-64QAM / Outer_Full

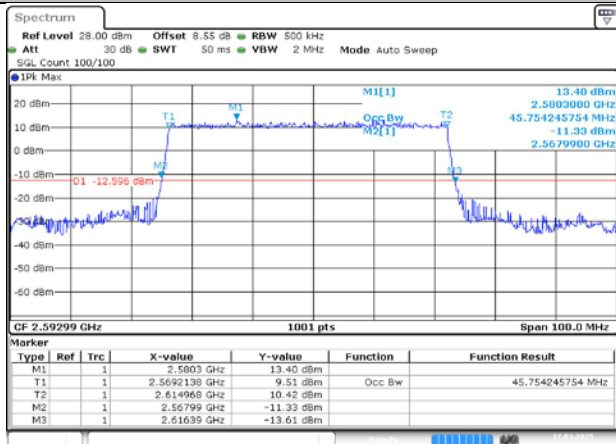


MCH / CP-256QAM / Outer_Full



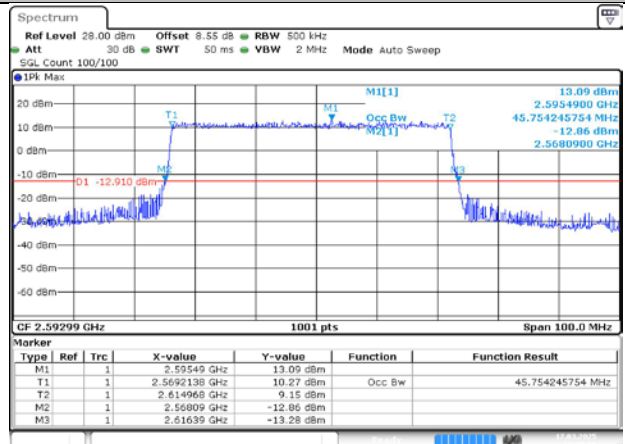
n41A / 30KHz / 50MHz

MCH / DFT-Pi2BPSK / Outer_Full



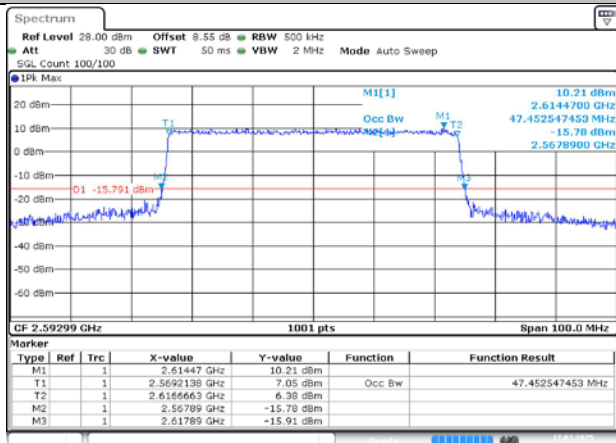
Date: 17.MAR.2025 19:28:49

MCH / DFT-QPSK / Outer_Full



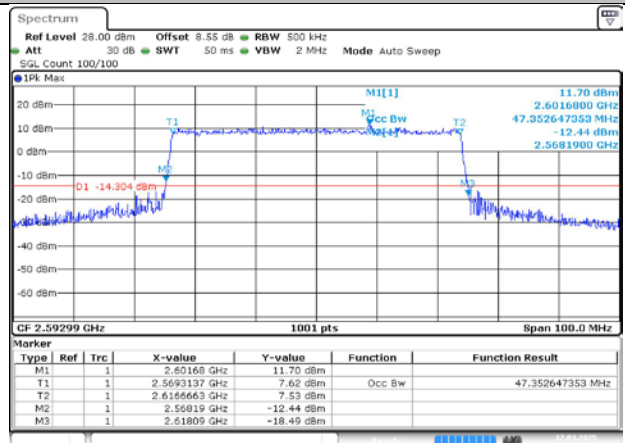
Date: 17.MAR.2025 19:29:00

MCH / CP-QPSK / Outer_Full



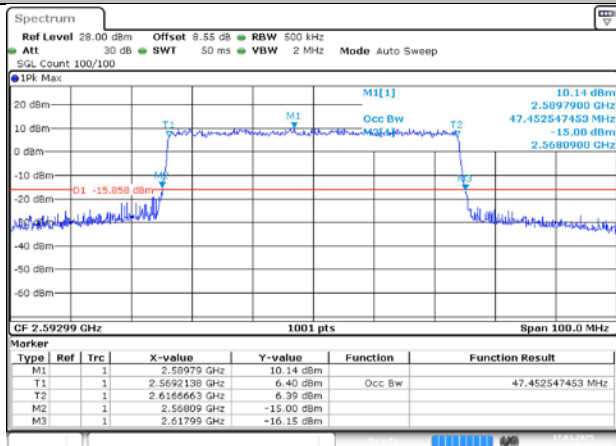
Date: 17.MAR.2025 19:29:09

MCH / CP-16QAM / Outer_Full



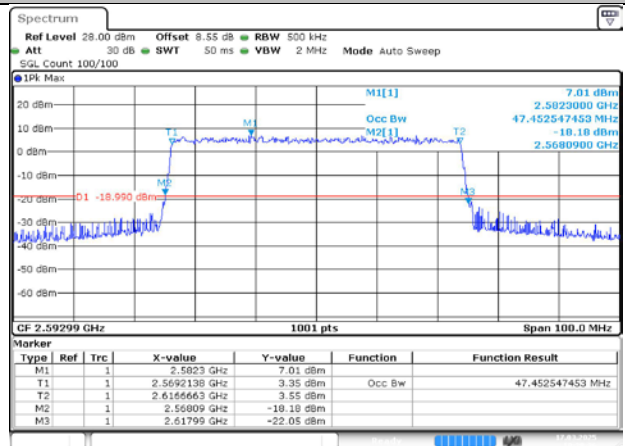
Date: 17.MAR.2025 19:29:18

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:29:26

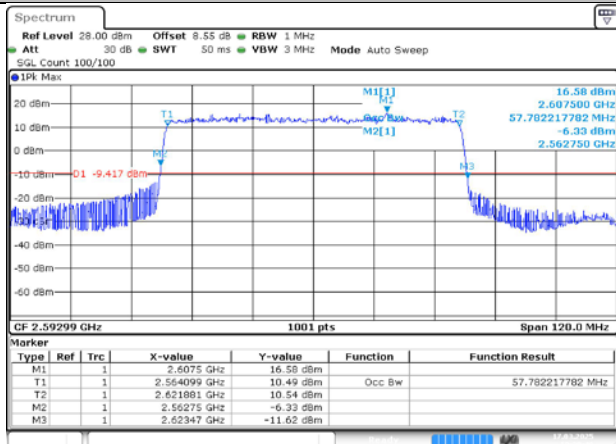
MCH / CP-256QAM / Outer_Full



Date: 17.MAR.2025 19:29:37

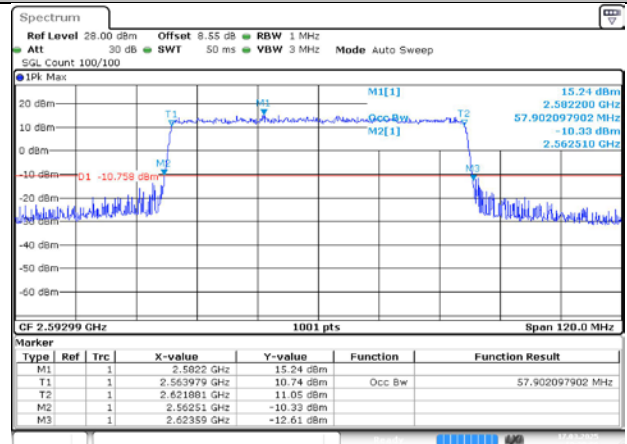
n41A / 30KHz / 60MHz

MCH / DFT-Pi2BPSK / Outer_Full



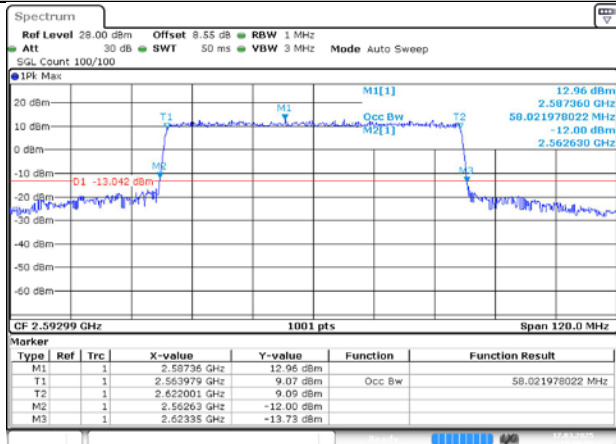
Date: 17.MAR.2025 19:29:49

MCH / DFT-QPSK / Outer_Full



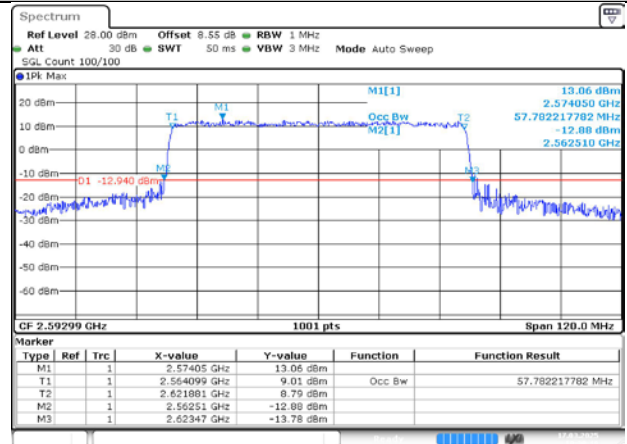
Date: 17.MAR.2025 19:30:00

MCH / CP-QPSK / Outer_Full



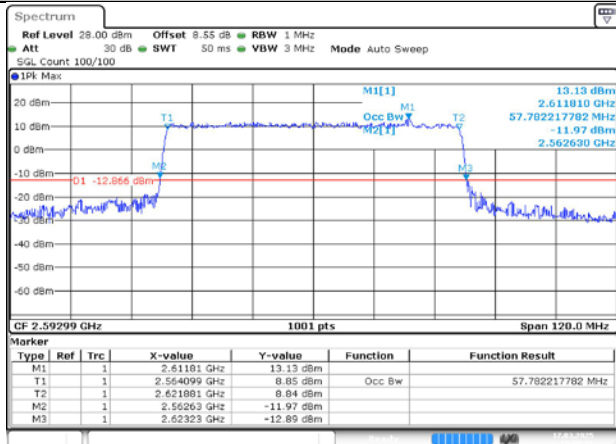
Date: 17.MAR.2025 19:30:09

MCH / CP-16QAM / Outer_Full



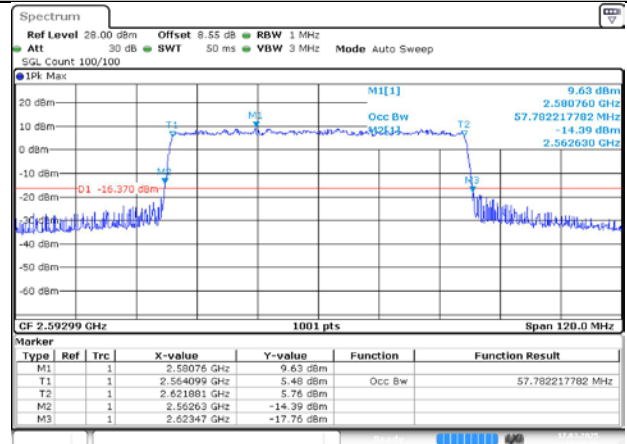
Date: 17.MAR.2025 19:30:18

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:30:26

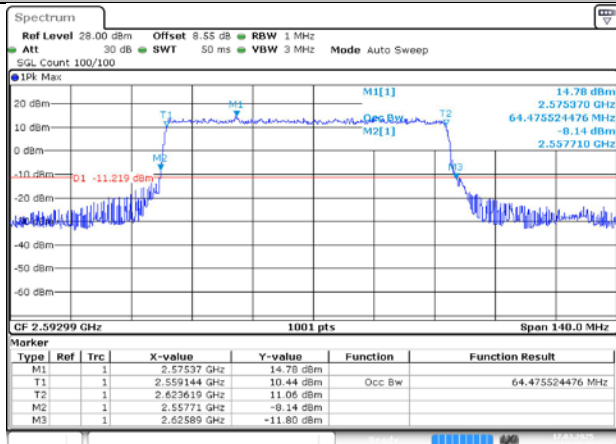
MCH / CP-256QAM / Outer_Full



Date: 17.MAR.2025 19:30:37

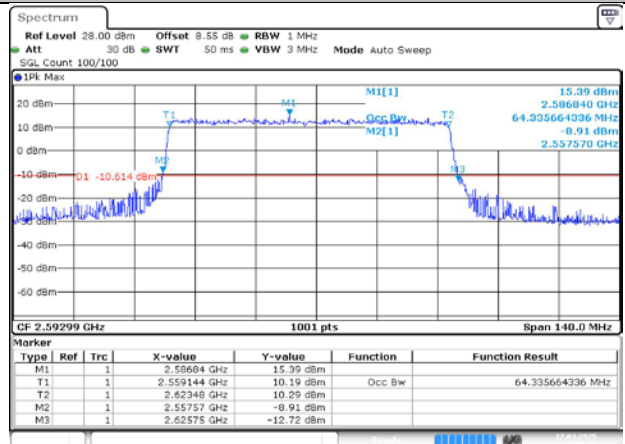
n41A / 30KHz / 70MHz

MCH / DFT-Pi2BPSK / Outer_Full



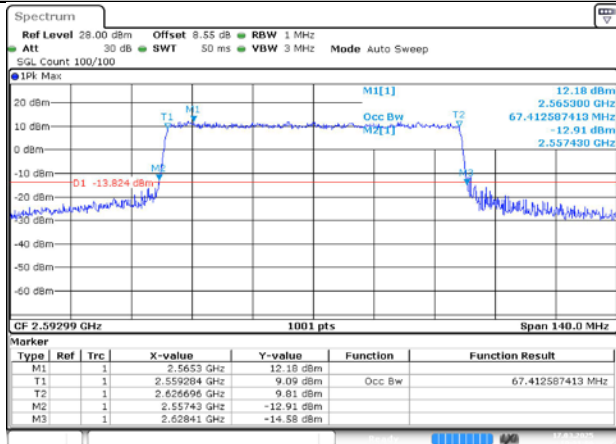
Date: 17.MAR.2025 19:30:49

MCH / DFT-QPSK / Outer_Full



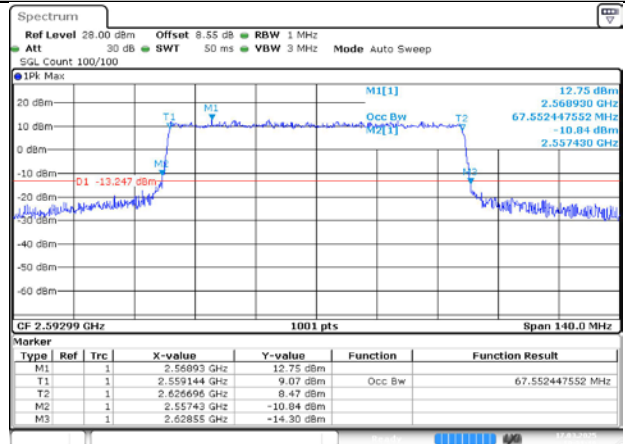
Date: 17.MAR.2025 19:31:00

MCH / CP-QPSK / Outer_Full



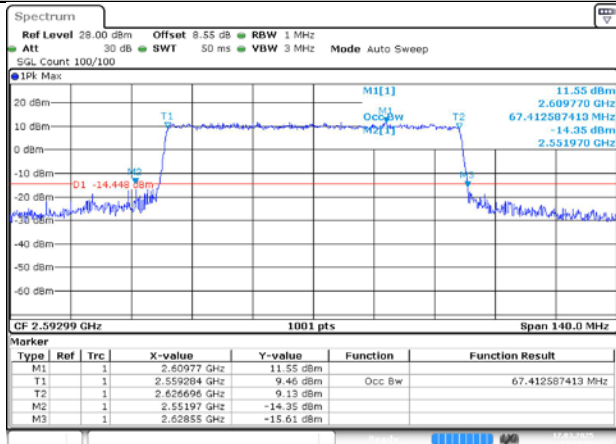
Date: 17.MAR.2025 19:31:09

MCH / CP-16QAM / Outer_Full



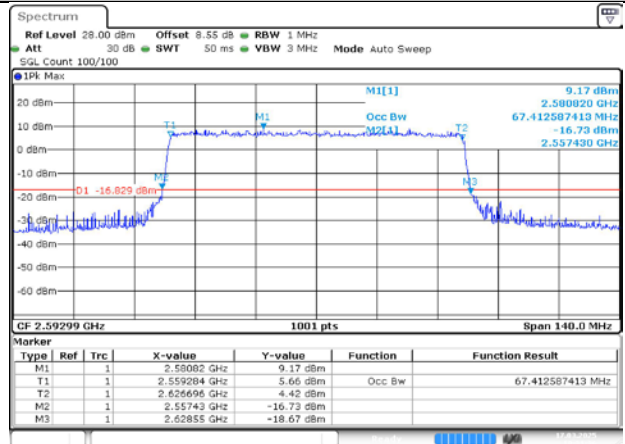
Date: 17.MAR.2025 19:31:18

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:31:26

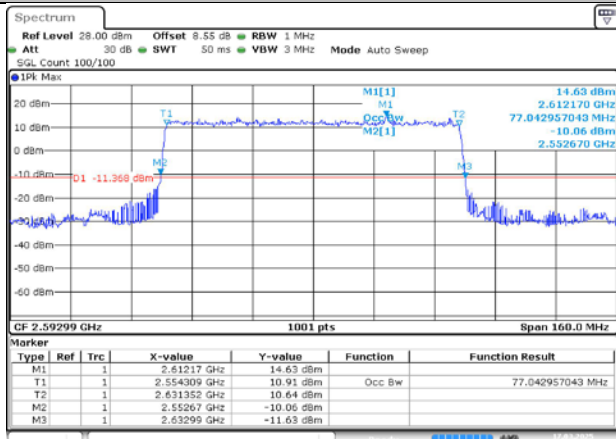
MCH / CP-256QAM / Outer_Full



Date: 17.MAR.2025 19:31:37

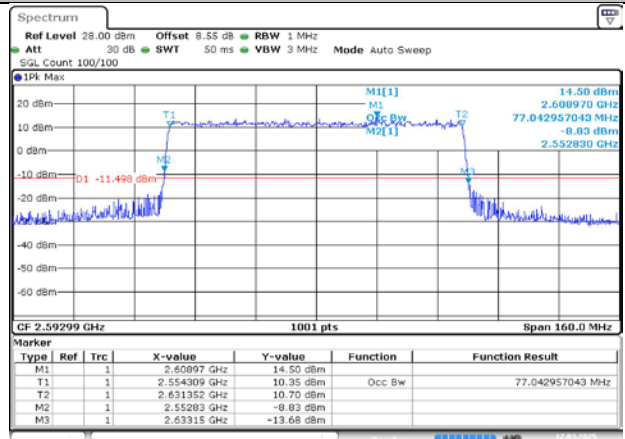
n41A / 30KHz / 80MHz

MCH / DFT-Pi2BPSK / Outer_Full



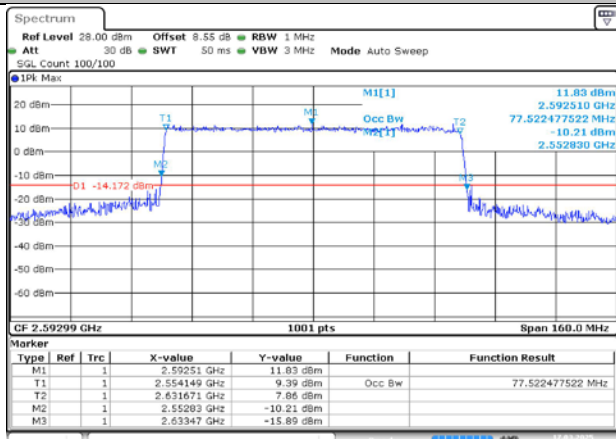
Date: 17.MAR.2025 19:31:50

MCH / DFT-QPSK / Outer_Full



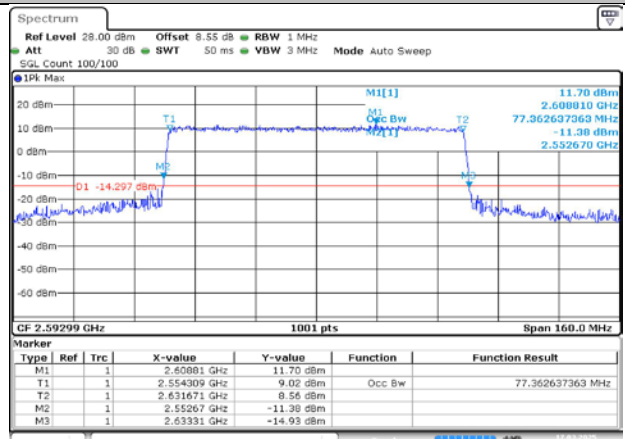
Date: 17.MAR.2025 19:32:01

MCH / CP-QPSK / Outer_Full



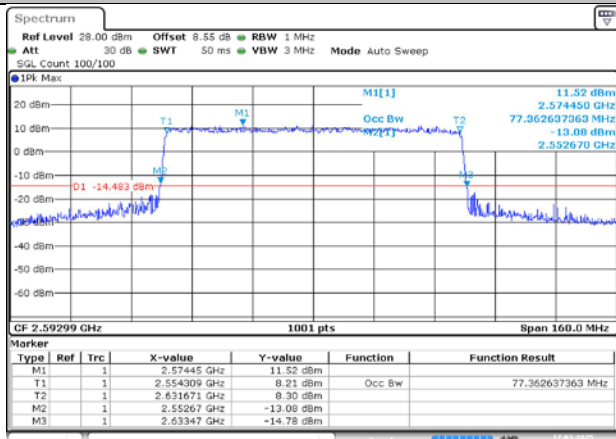
Date: 17.MAR.2025 19:32:10

MCH / CP-16QAM / Outer_Full



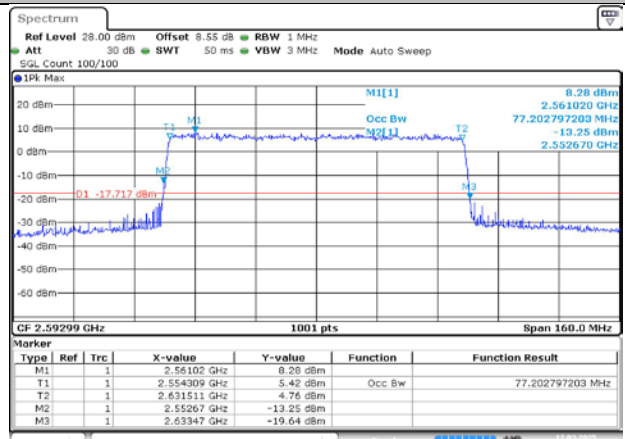
Date: 17.MAR.2025 19:32:19

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:32:28

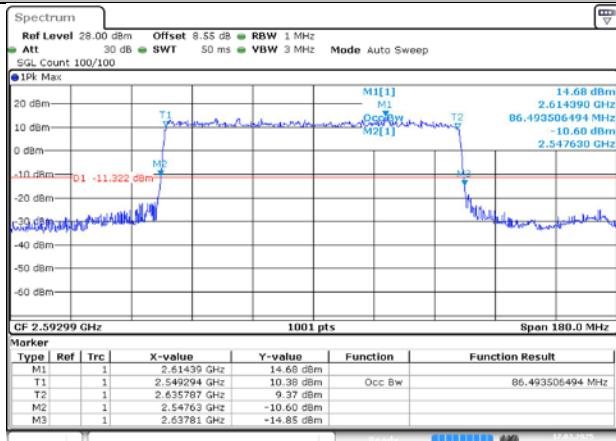
MCH / CP-256QAM / Outer_Full



Date: 17.MAR.2025 19:32:38

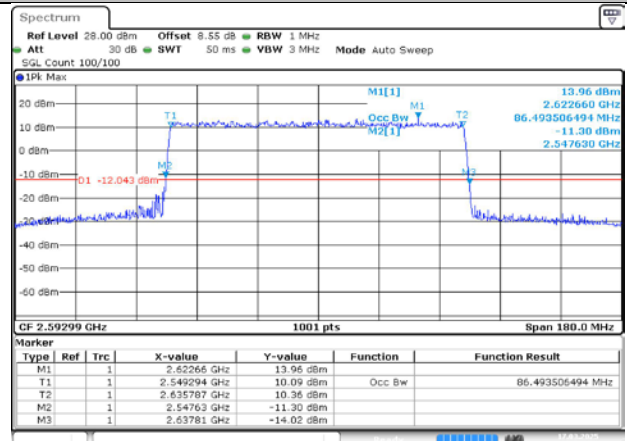
n41A / 30KHz / 90MHz

MCH / DFT-Pi2BPSK / Outer_Full



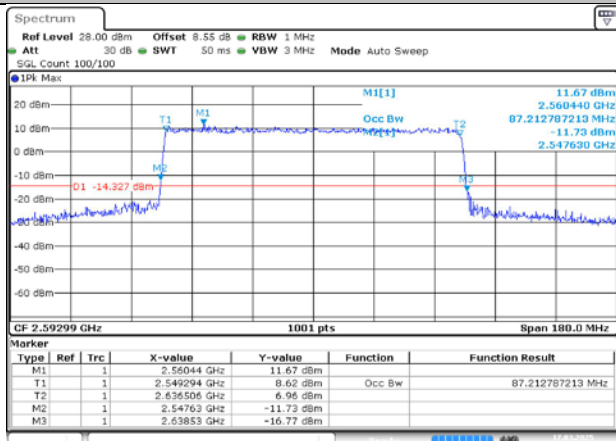
Date: 17.MAR.2025 19:32:51

MCH / DFT-QPSK / Outer_Full



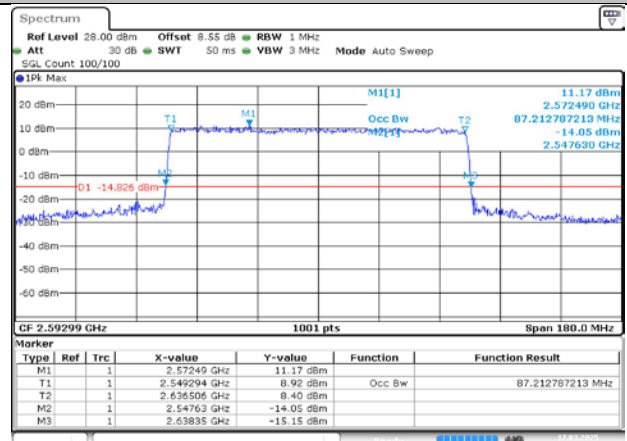
Date: 17.MAR.2025 19:33:02

MCH / CP-QPSK / Outer_Full



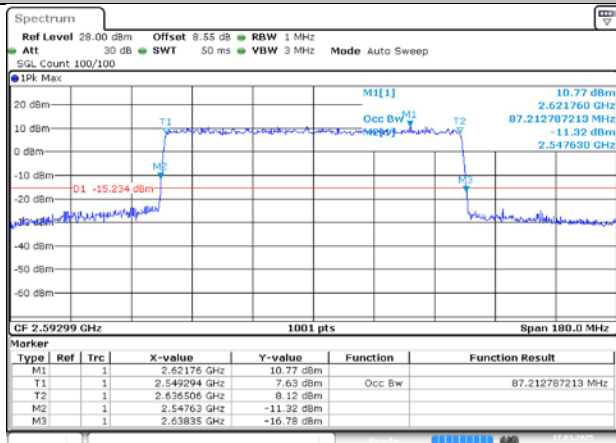
Date: 17.MAR.2025 19:33:12

MCH / CP-16QAM / Outer_Full



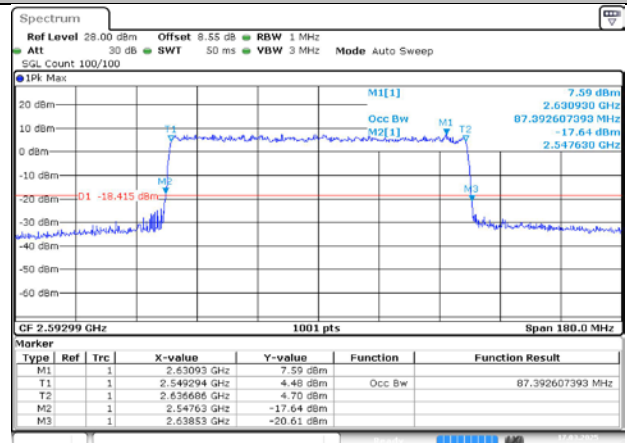
Date: 17.MAR.2025 19:33:21

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:33:30

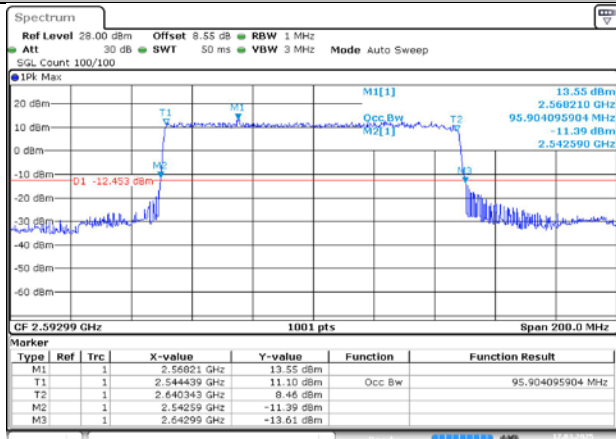
MCH / CP-256QAM / Outer_Full



Date: 17.MAR.2025 19:33:40

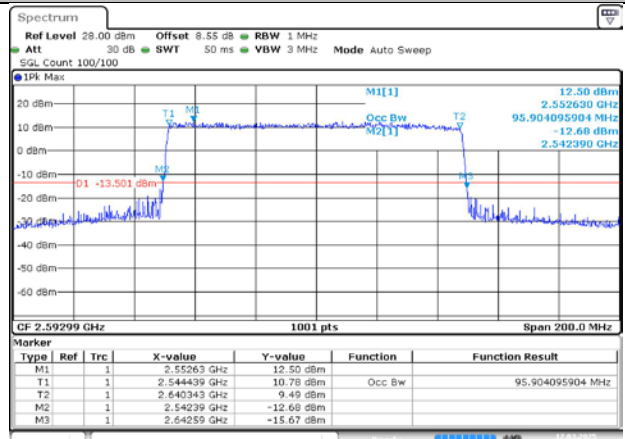
n41A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



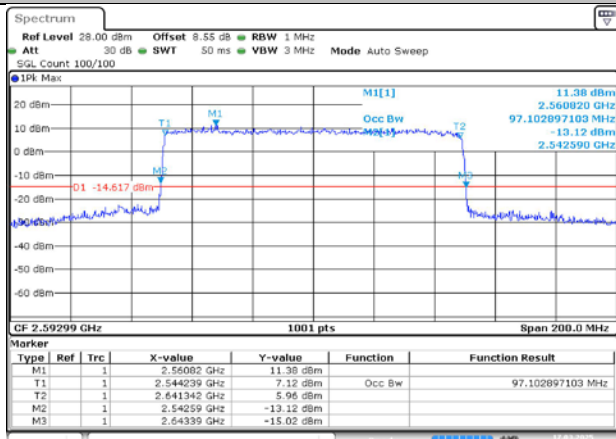
Date: 17.MAR.2025 19:33:53

MCH / DFT-QPSK / Outer_Full



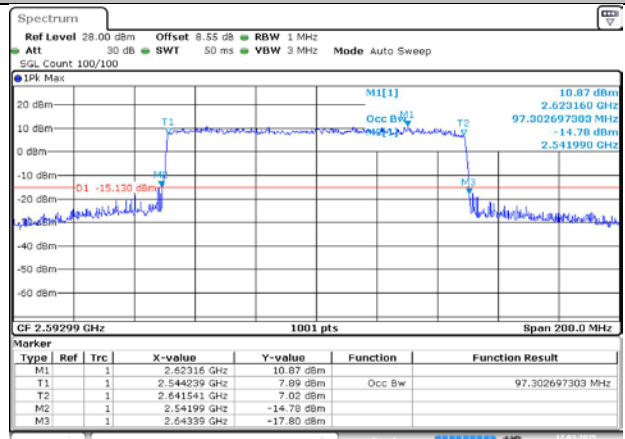
Date: 17.MAR.2025 19:34:04

MCH / CP-QPSK / Outer_Full



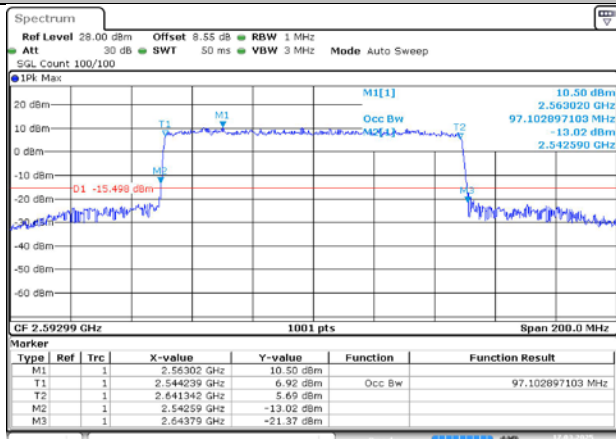
Date: 17.MAR.2025 19:34:14

MCH / CP-16QAM / Outer_Full



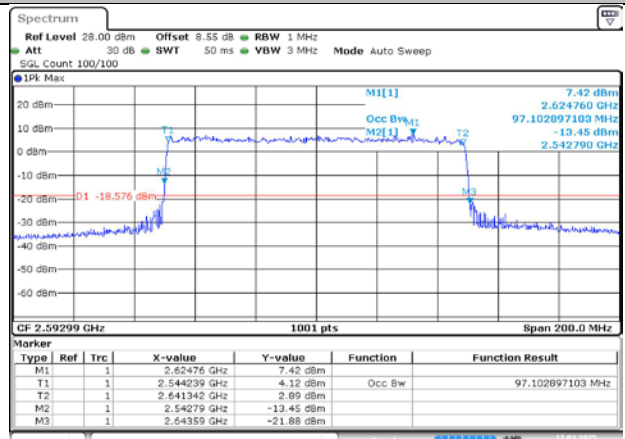
Date: 17.MAR.2025 19:34:23

MCH / CP-64QAM / Outer_Full



Date: 17.MAR.2025 19:34:32

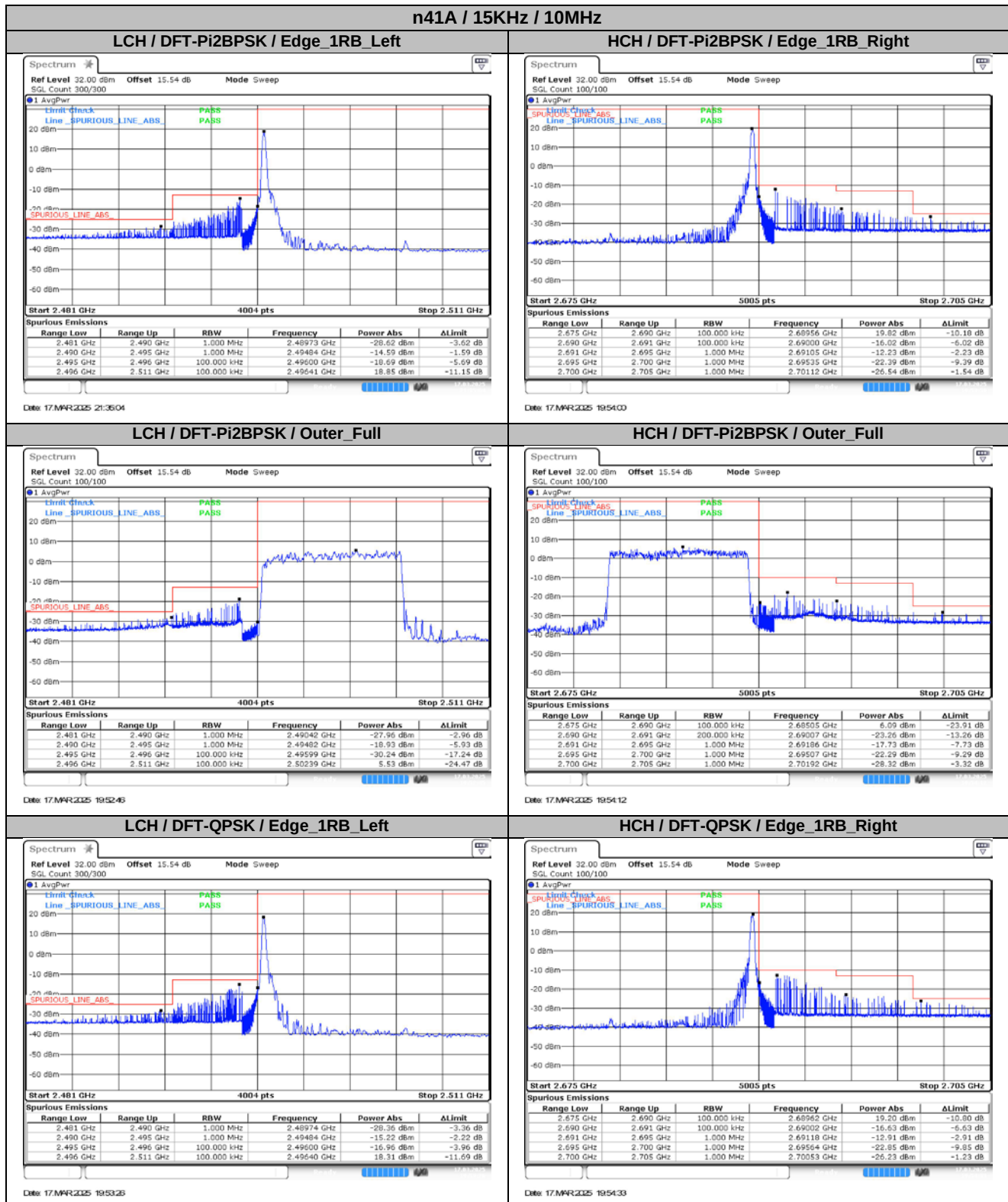
MCH / CP-256QAM / Outer_Full

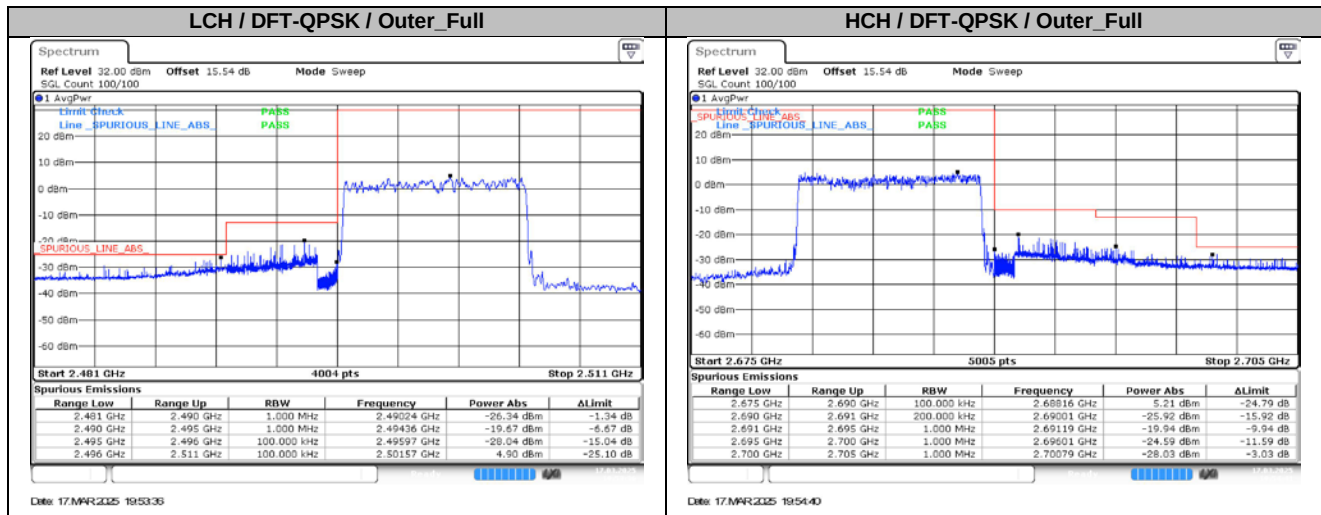
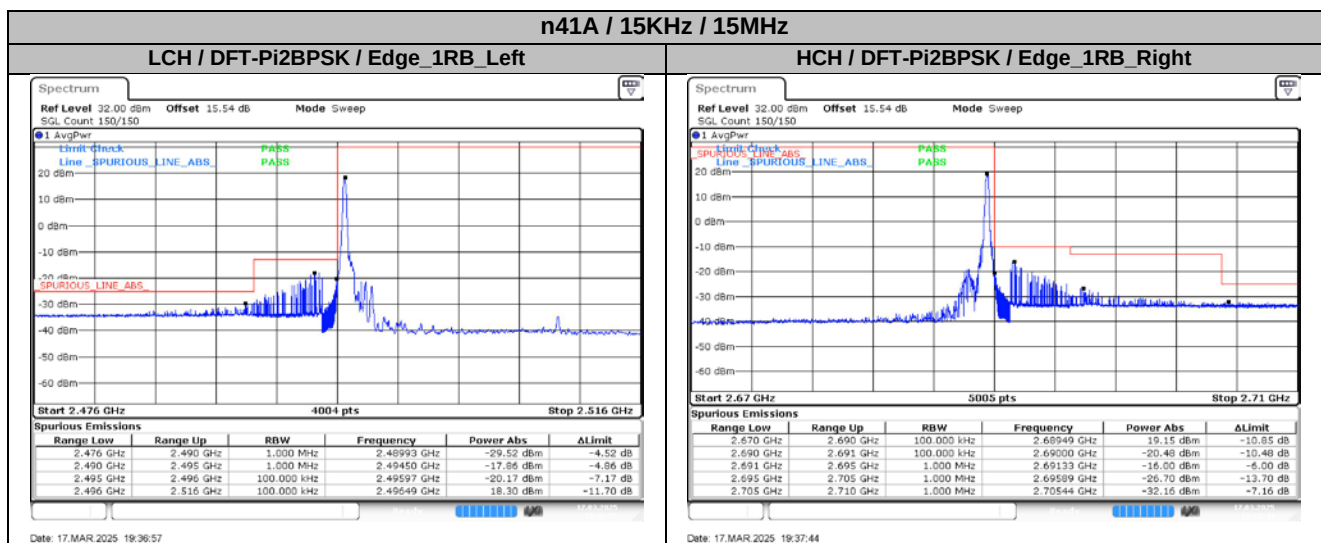
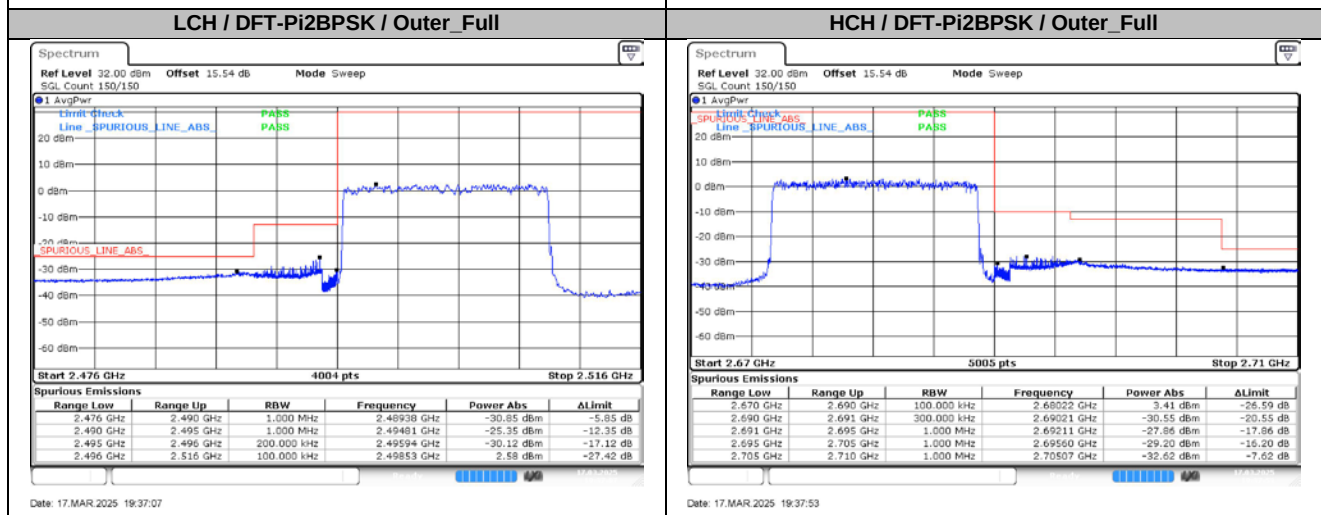


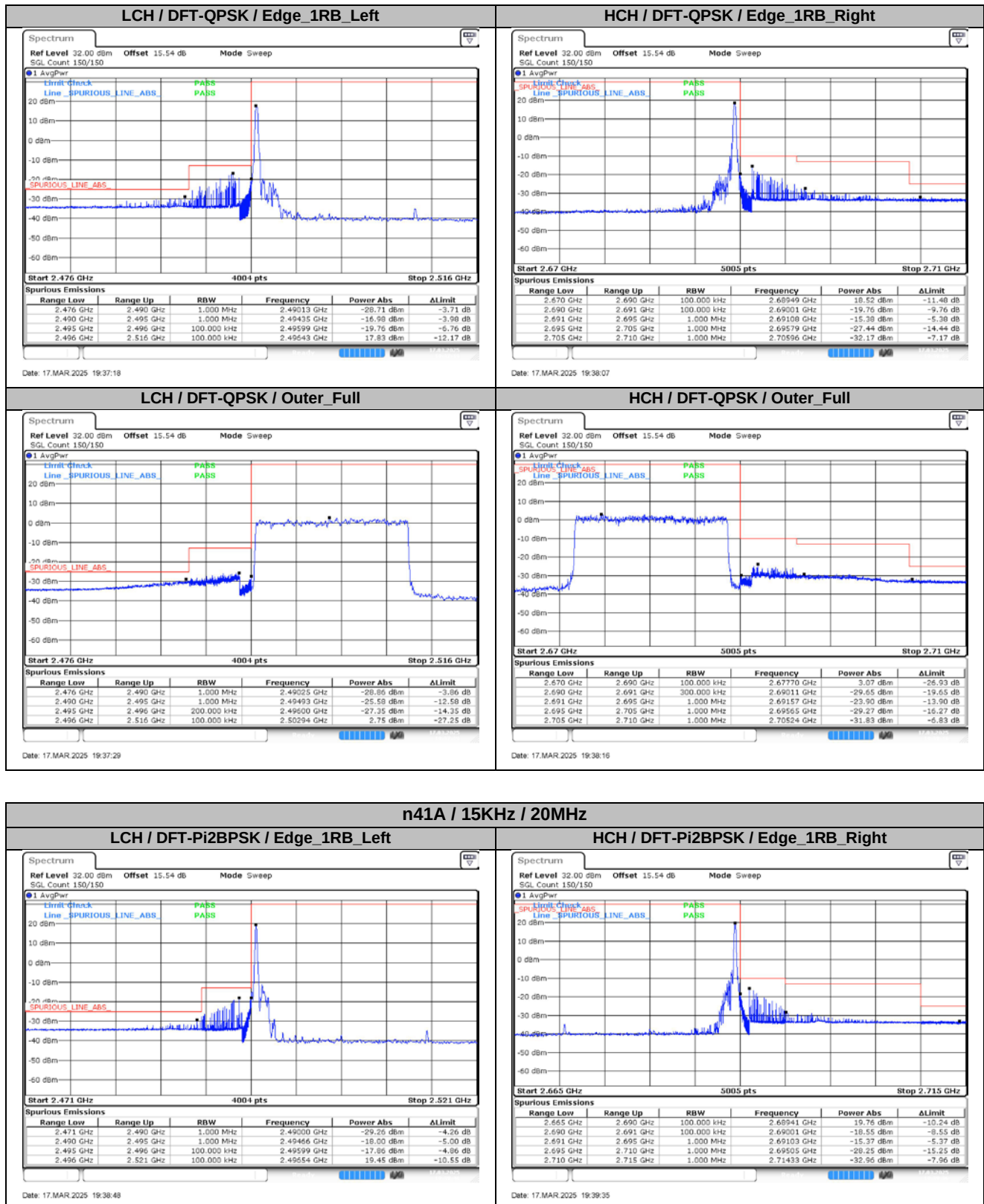
Date: 17.MAR.2025 19:34:42

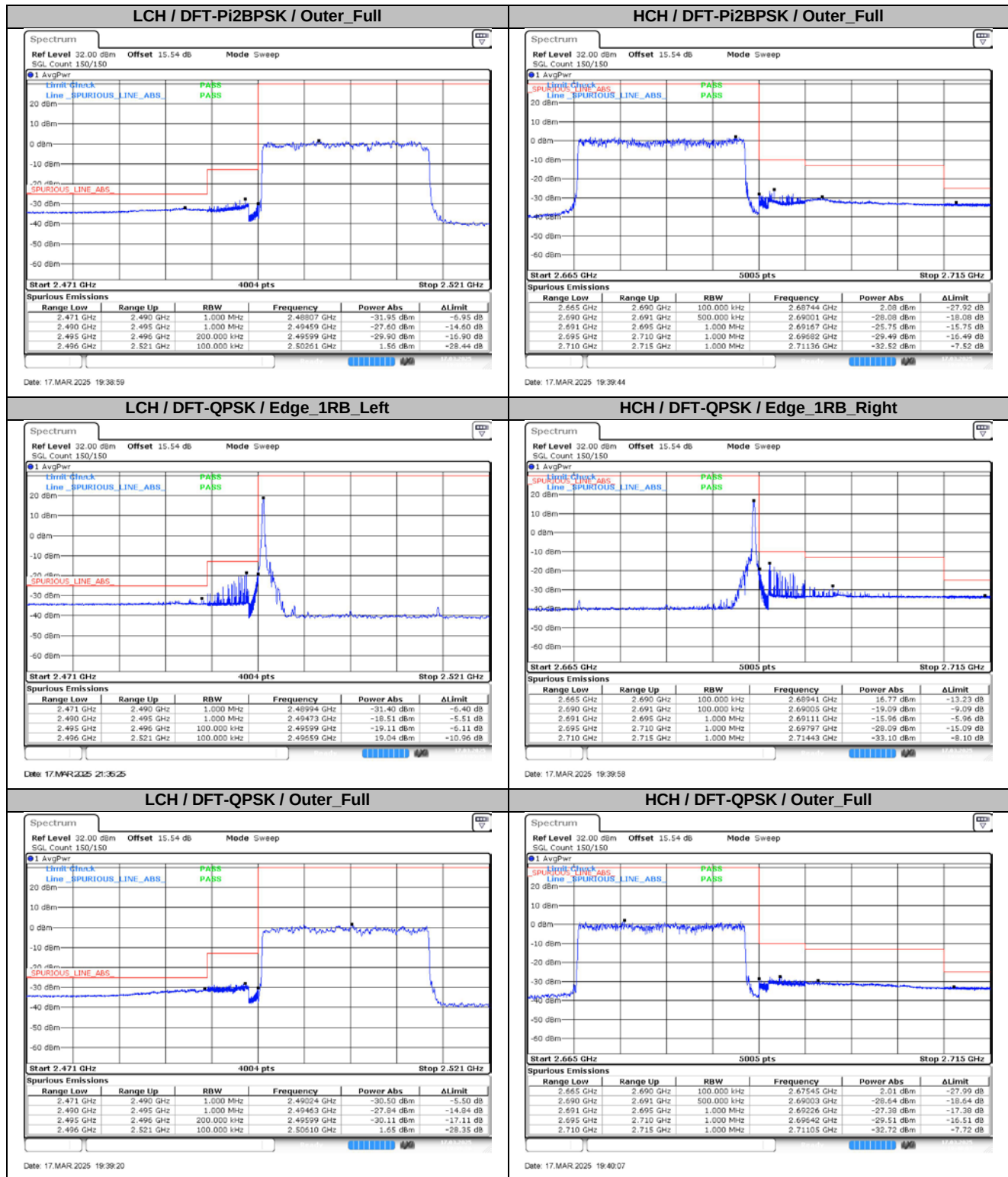
5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz



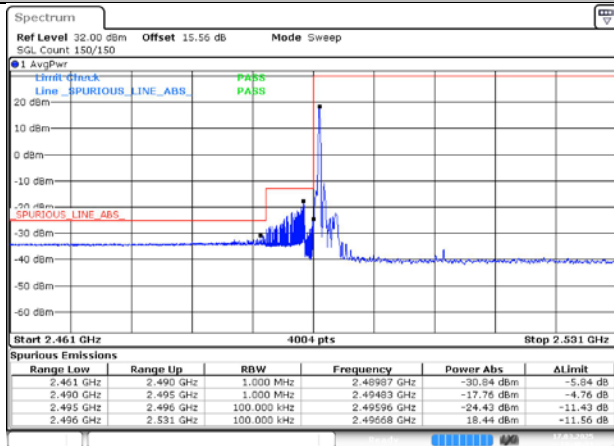






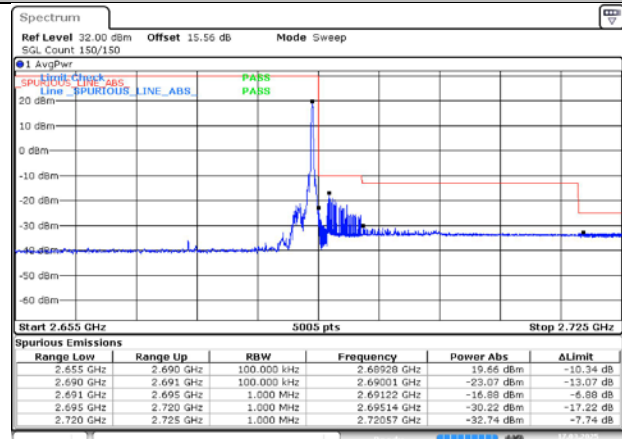
n41A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 17.MAR.2025 19:40:22

HCH / DFT-Pi2BPSK / Edge_1RB_Right



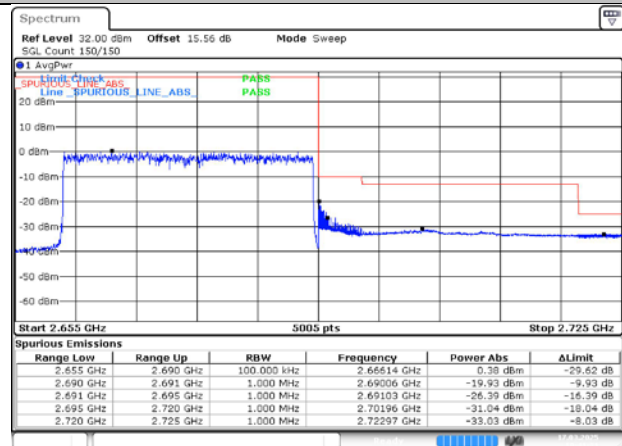
Date: 17.MAR.2025 19:41:11

LCH / DFT-Pi2BPSK / Outer_Full



Date: 17.MAR.2025 19:40:33

HCH / DFT-Pi2BPSK / Outer_Full



Date: 17.MAR.2025 19:41:21

LCH / DFT-QPSK / Edge_1RB_Left



Date: 17.MAR.2025 19:40:44

HCH / DFT-QPSK / Edge_1RB_Right



Date: 17.MAR.2025 19:41:36