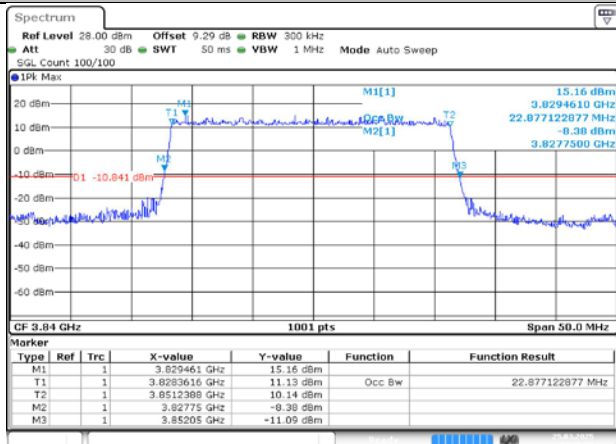


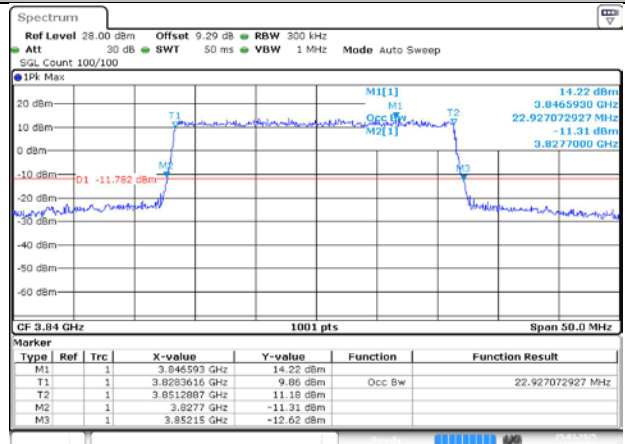
n77A / 30KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



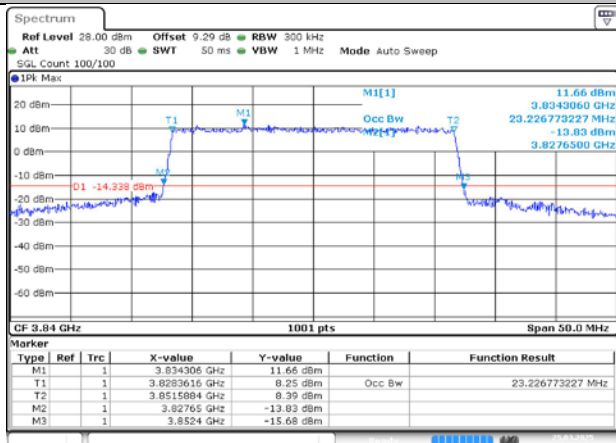
Date: 25.MAR.2025 13:35:40

MCH / DFT-QPSK / Outer_Full



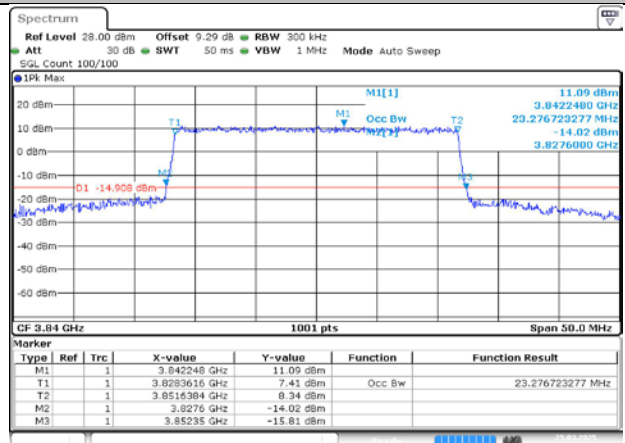
Date: 25.MAR.2025 13:35:51

MCH / CP-QPSK / Outer_Full



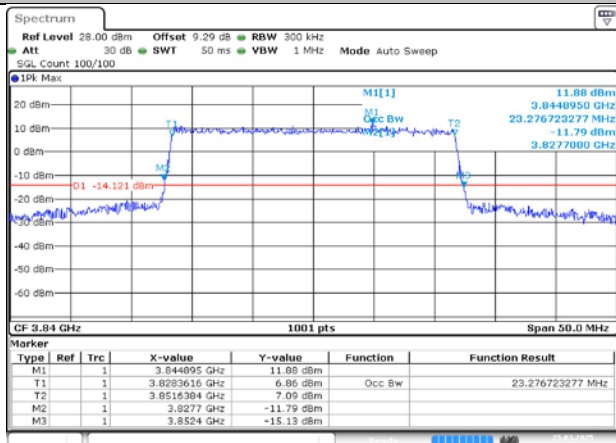
Date: 25.MAR.2025 13:36:00

MCH / CP-16QAM / Outer_Full



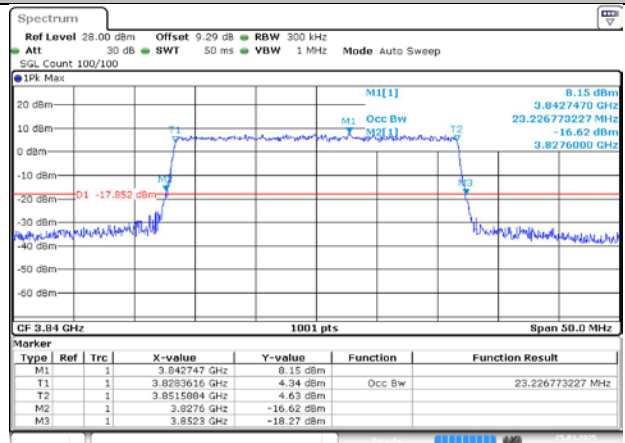
Date: 25.MAR.2025 13:36:09

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:36:18

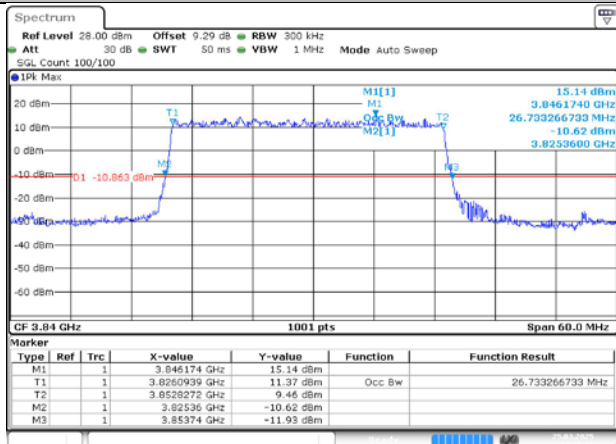
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:36:29

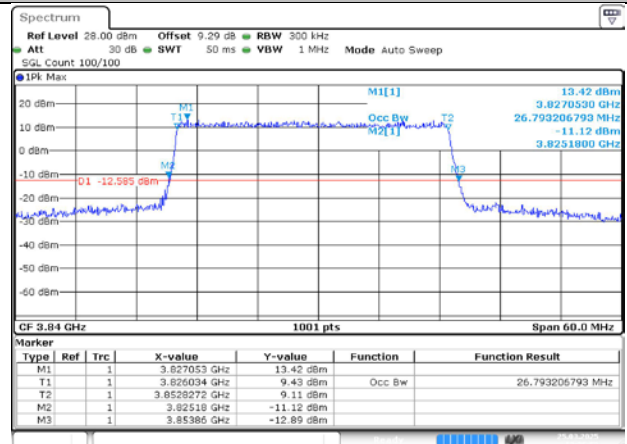
n77A / 30KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



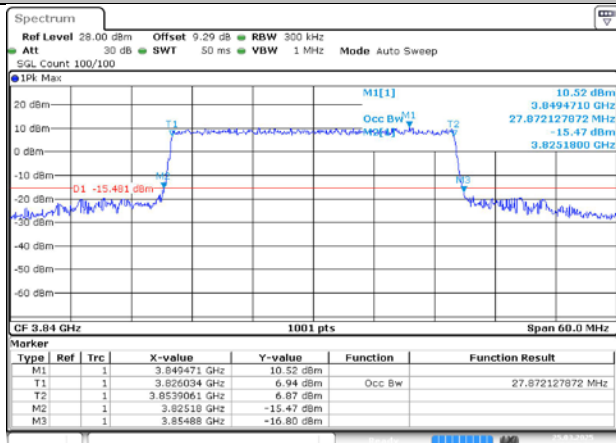
Date: 25.MAR.2025 13:36:42

MCH / DFT-QPSK / Outer_Full



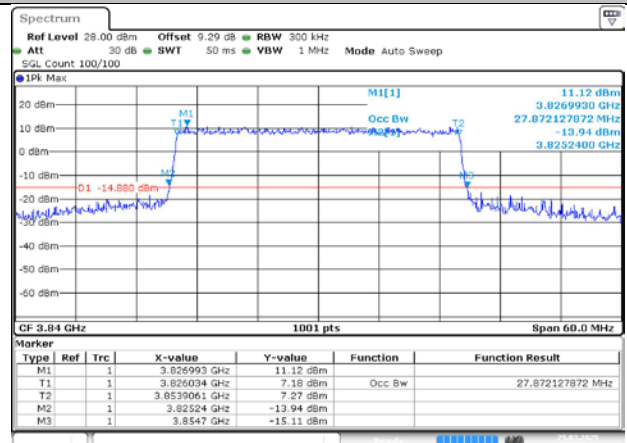
Date: 25.MAR.2025 13:36:53

MCH / CP-QPSK / Outer_Full



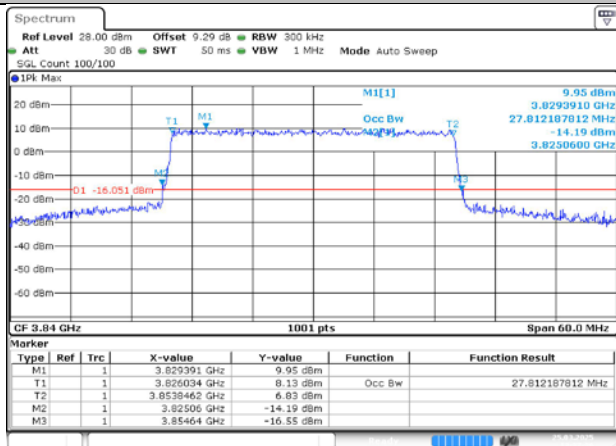
Date: 25.MAR.2025 13:37:02

MCH / CP-16QAM / Outer_Full



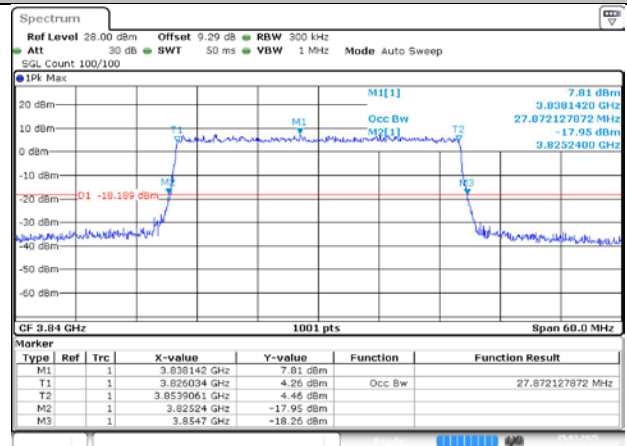
Date: 25.MAR.2025 13:37:11

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:37:20

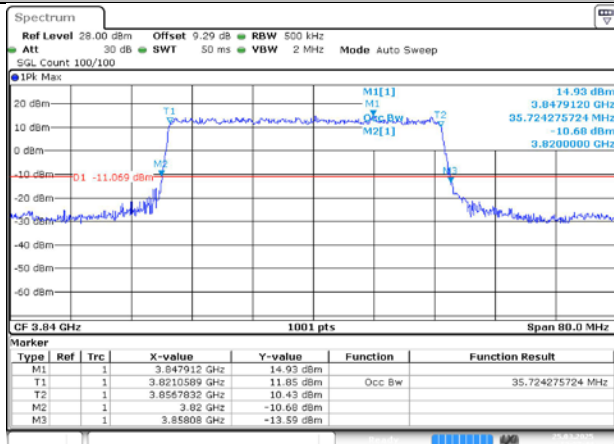
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:37:31

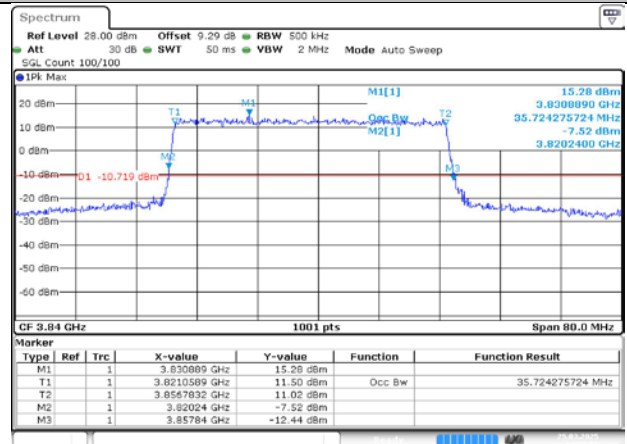
n77A / 30KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



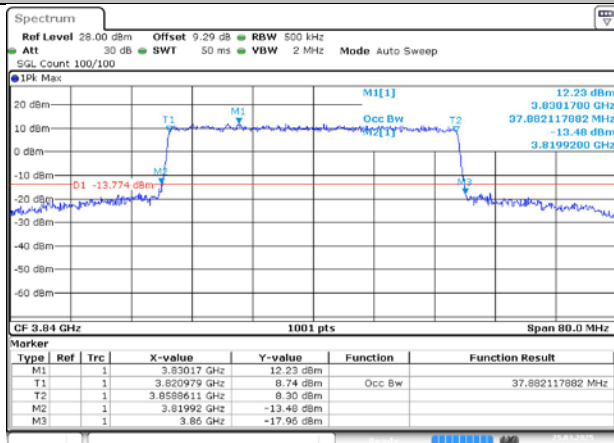
Date: 25.MAR.2025 13:37:44

MCH / DFT-QPSK / Outer_Full



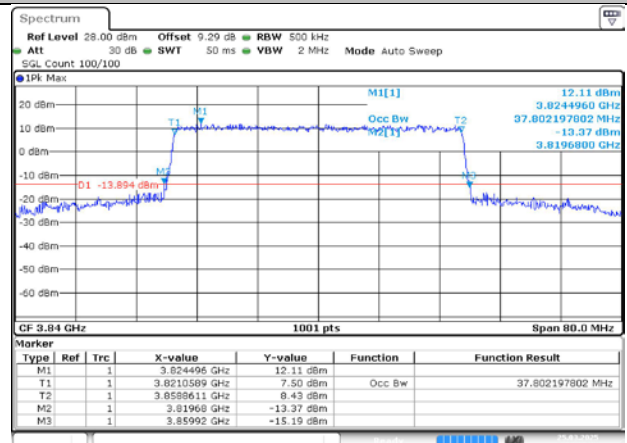
Date: 25.MAR.2025 13:37:55

MCH / CP-QPSK / Outer_Full



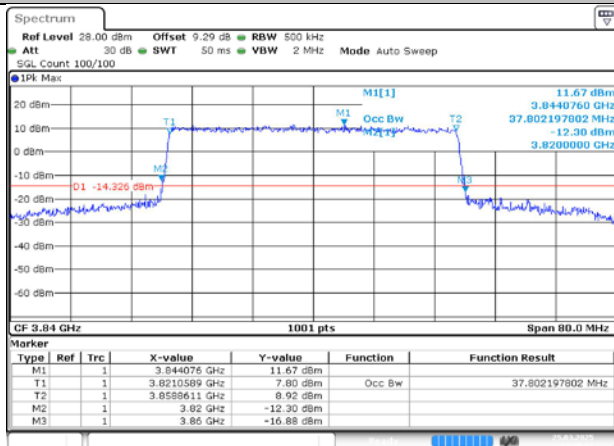
Date: 25.MAR.2025 13:38:05

MCH / CP-16QAM / Outer_Full



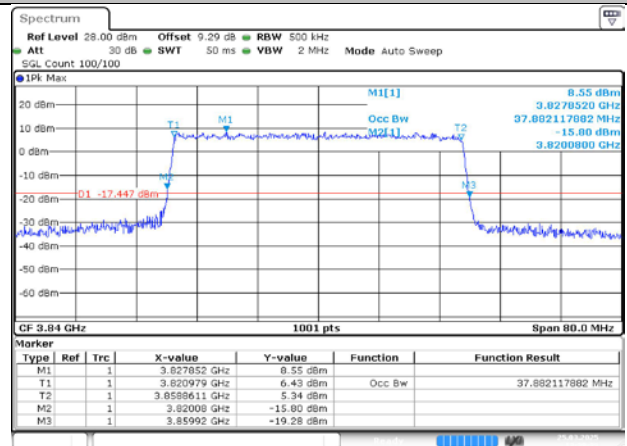
Date: 25.MAR.2025 13:38:14

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:38:22

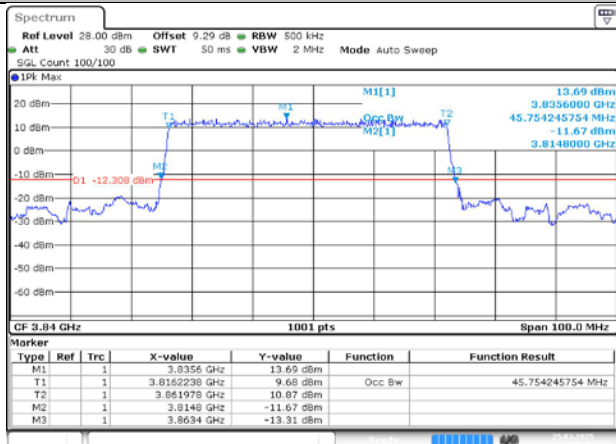
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:38:33

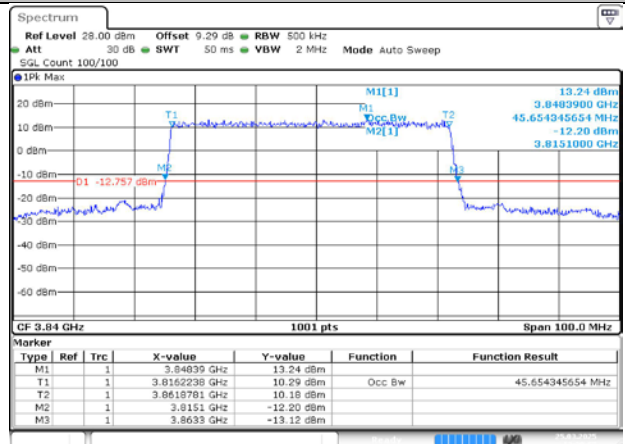
n77A / 30KHz / 50MHz

MCH / DFT-Pi2BPSK / Outer_Full



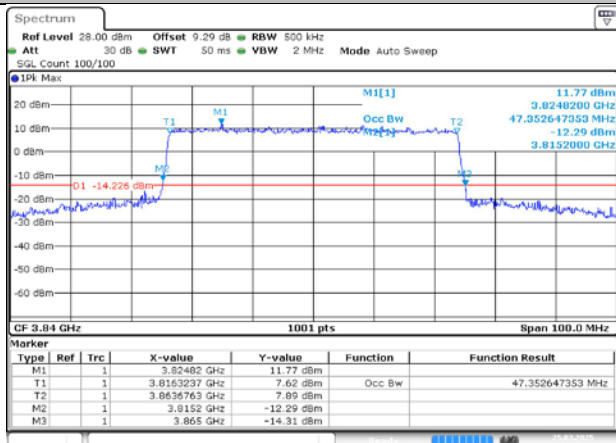
Date: 25.MAR.2025 13:38:46

MCH / DFT-QPSK / Outer_Full



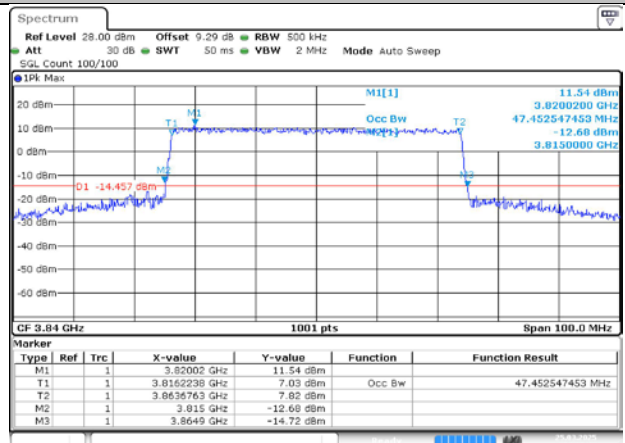
Date: 25.MAR.2025 13:38:57

MCH / CP-QPSK / Outer_Full



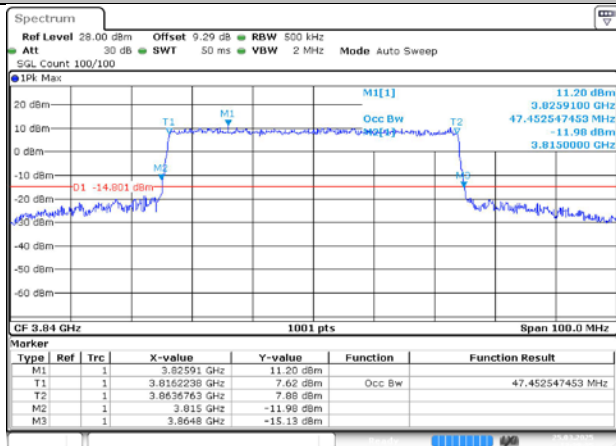
Date: 25.MAR.2025 13:39:07

MCH / CP-16QAM / Outer_Full



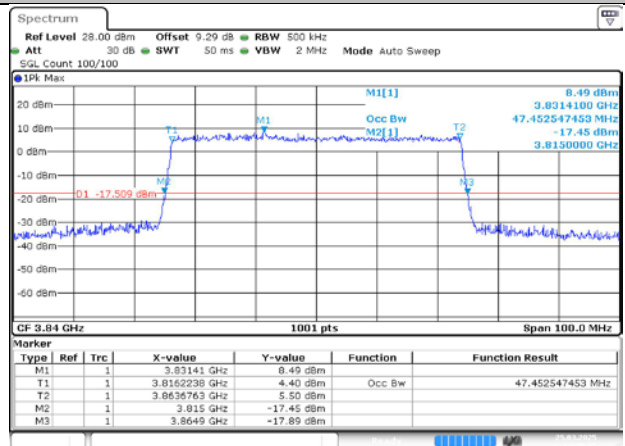
Date: 25.MAR.2025 13:39:16

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:39:24

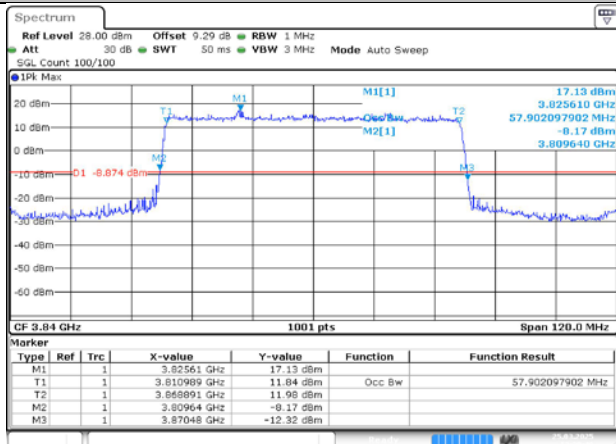
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:39:35

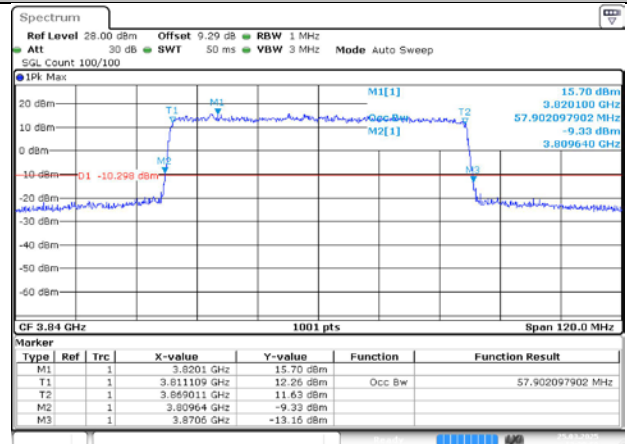
n77A / 30KHz / 60MHz

MCH / DFT-Pi2BPSK / Outer_Full



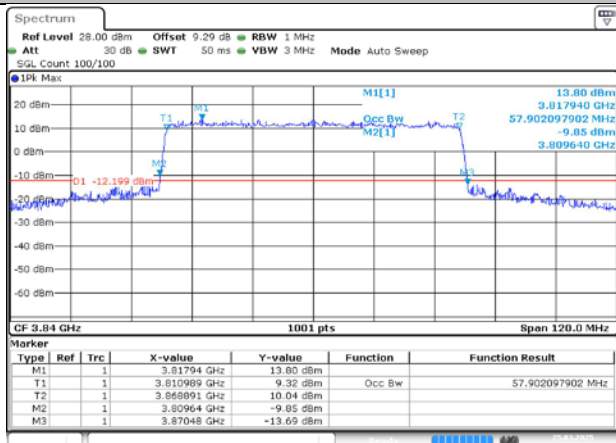
Date: 25.MAR.2025 13:39:48

MCH / DFT-QPSK / Outer_Full



Date: 25.MAR.2025 13:39:59

MCH / CP-QPSK / Outer_Full



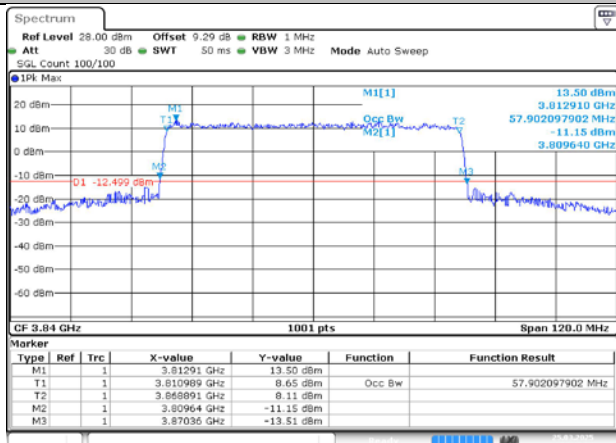
Date: 25.MAR.2025 13:40:09

MCH / CP-16QAM / Outer_Full



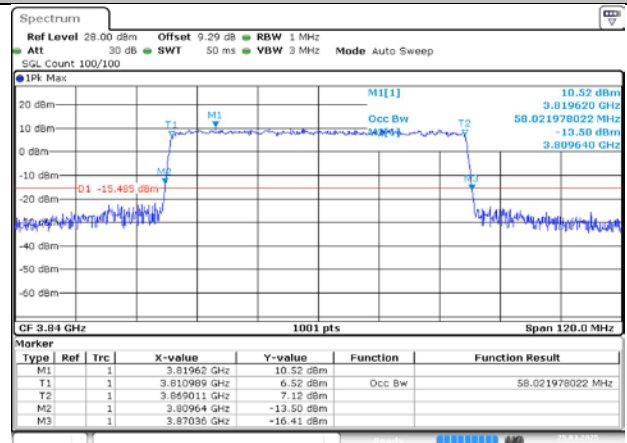
Date: 25.MAR.2025 13:40:18

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:40:27

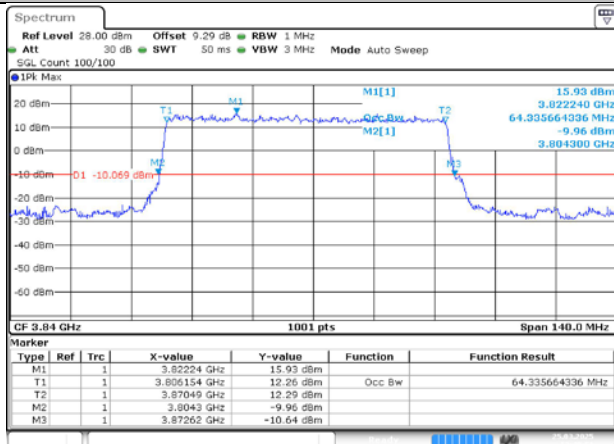
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:40:38

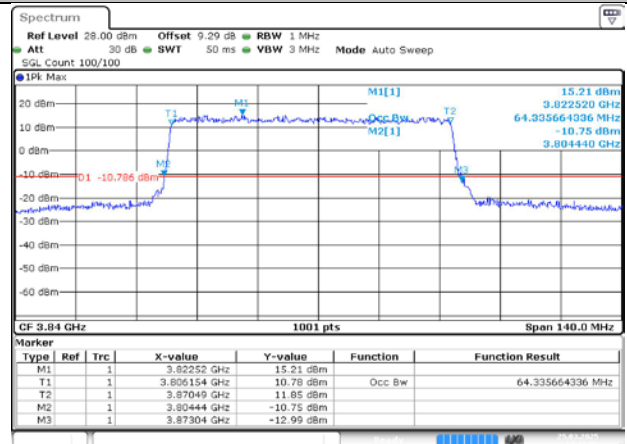
n77A / 30KHz / 70MHz

MCH / DFT-Pi2BPSK / Outer_Full



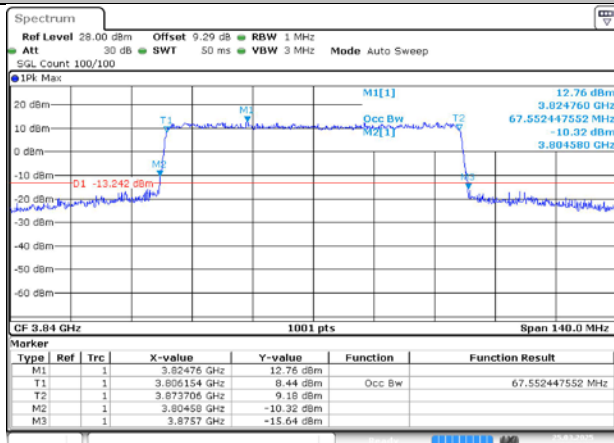
Date: 25.MAR.2025 13:40:51

MCH / DFT-QPSK / Outer_Full



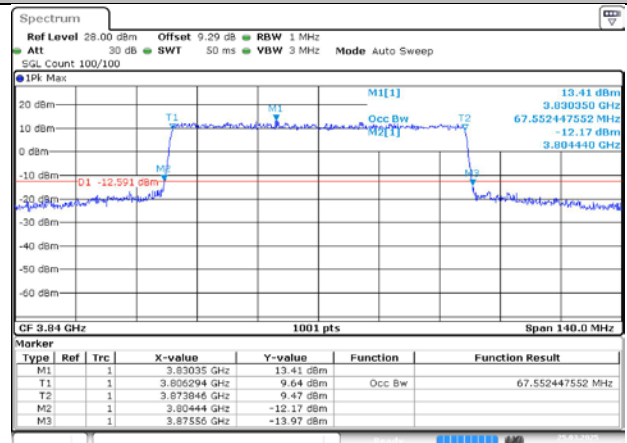
Date: 25.MAR.2025 13:41:02

MCH / CP-QPSK / Outer_Full



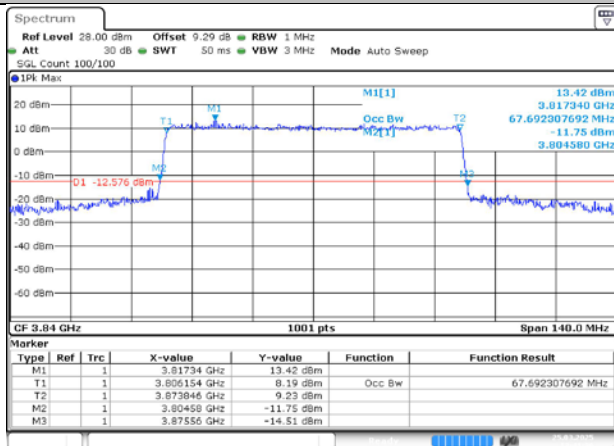
Date: 25.MAR.2025 13:41:12

MCH / CP-16QAM / Outer_Full



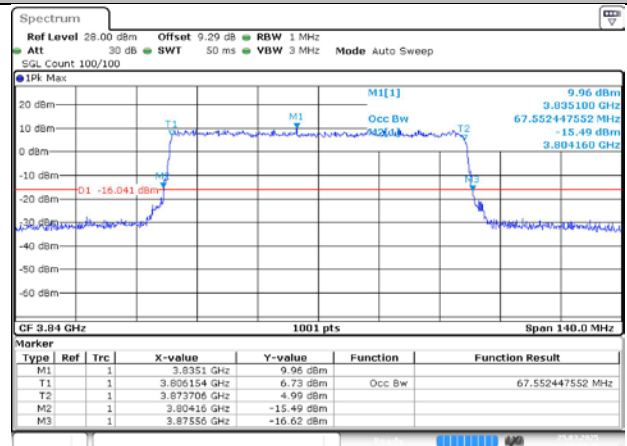
Date: 25.MAR.2025 13:41:21

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:41:30

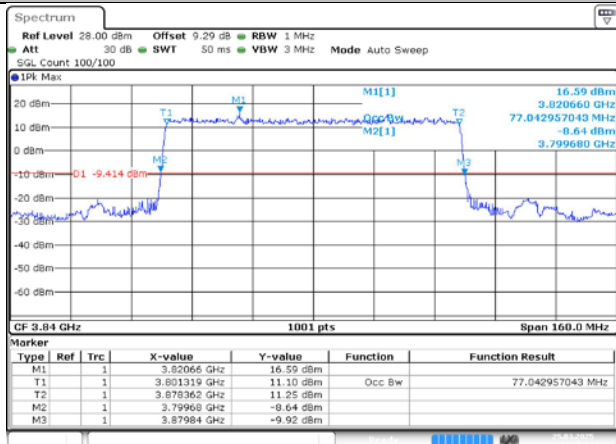
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:41:41

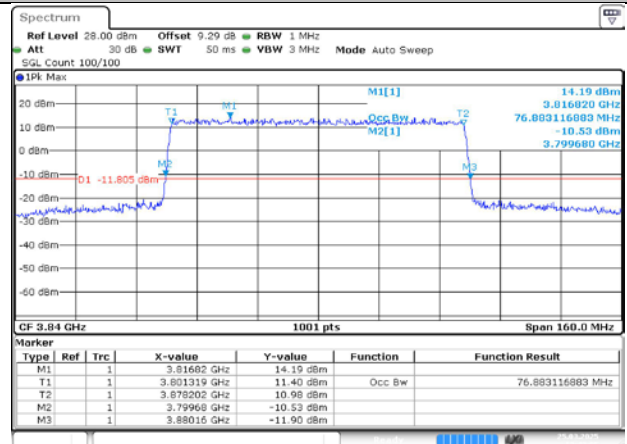
n77A / 30KHz / 80MHz

MCH / DFT-Pi2BPSK / Outer_Full



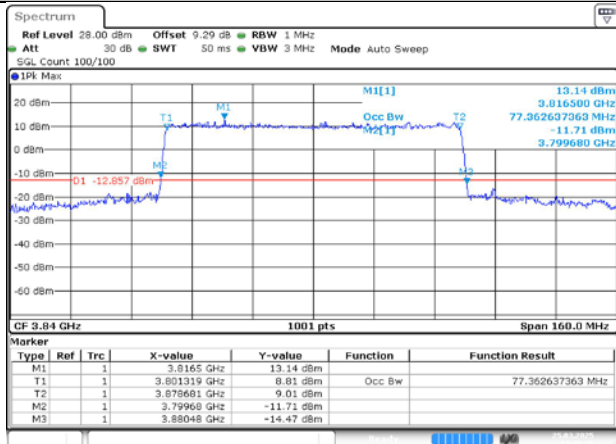
Date: 25.MAR.2025 13:41:54

MCH / DFT-QPSK / Outer_Full



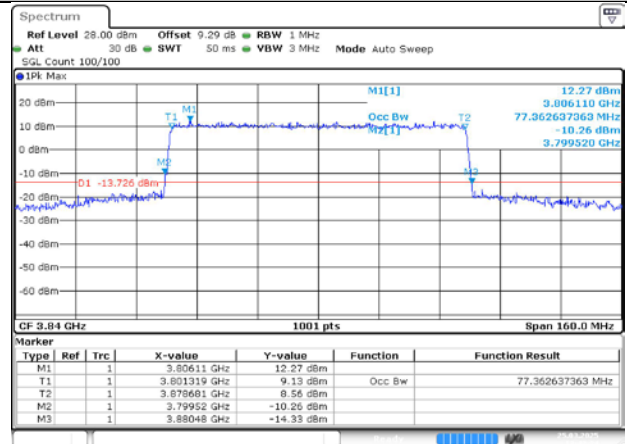
Date: 25.MAR.2025 13:42:05

MCH / CP-QPSK / Outer_Full



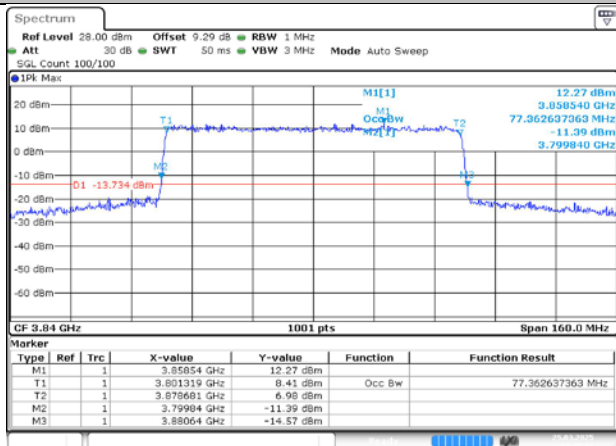
Date: 25.MAR.2025 13:42:15

MCH / CP-16QAM / Outer_Full



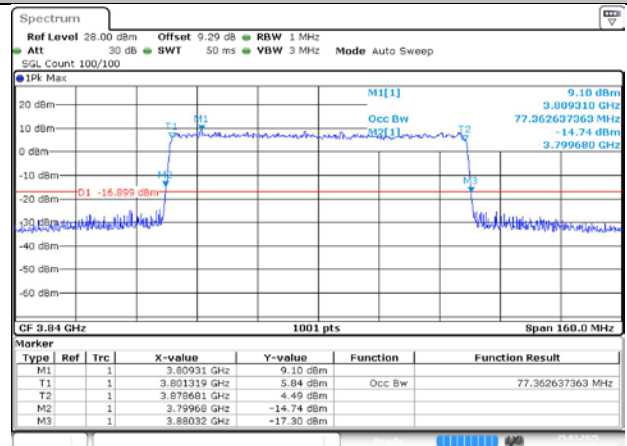
Date: 25.MAR.2025 13:42:24

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:42:33

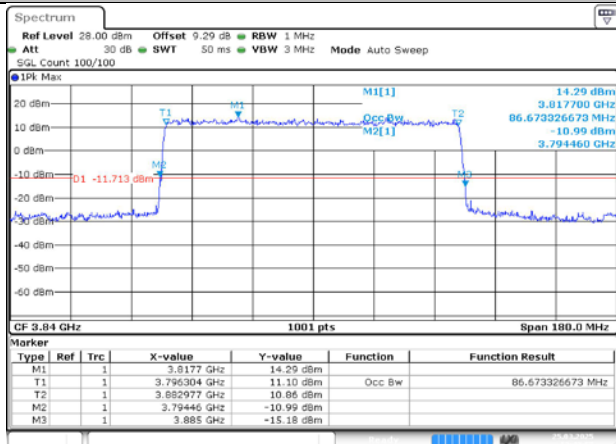
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:42:44

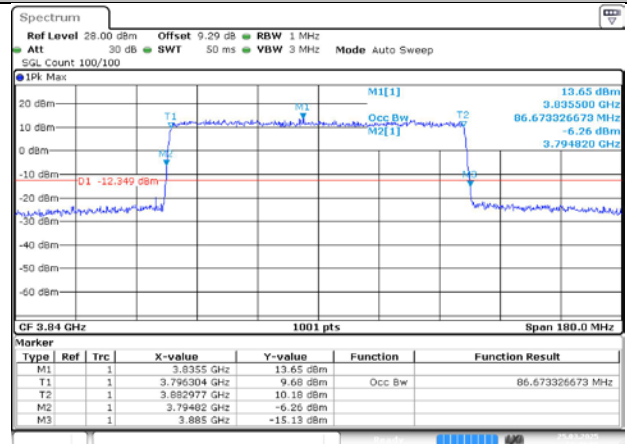
n77A / 30KHz / 90MHz

MCH / DFT-Pi2BPSK / Outer_Full



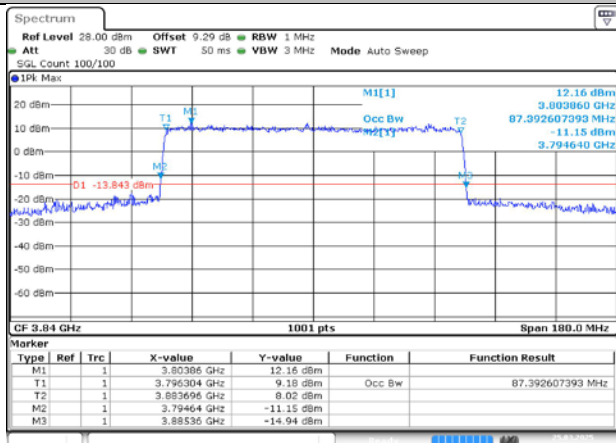
Date: 25.MAR.2025 13:42:57

MCH / DFT-QPSK / Outer_Full



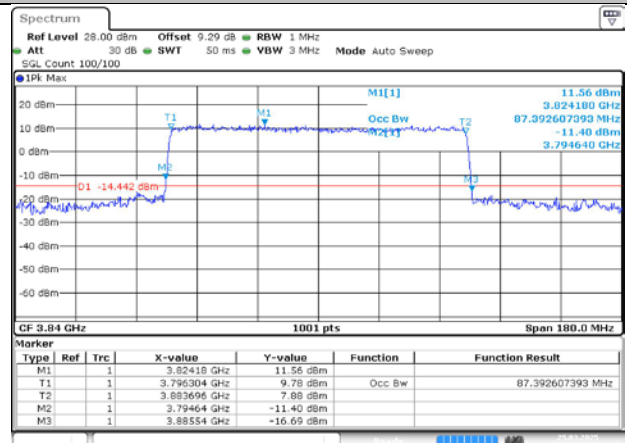
Date: 25.MAR.2025 13:43:08

MCH / CP-QPSK / Outer_Full



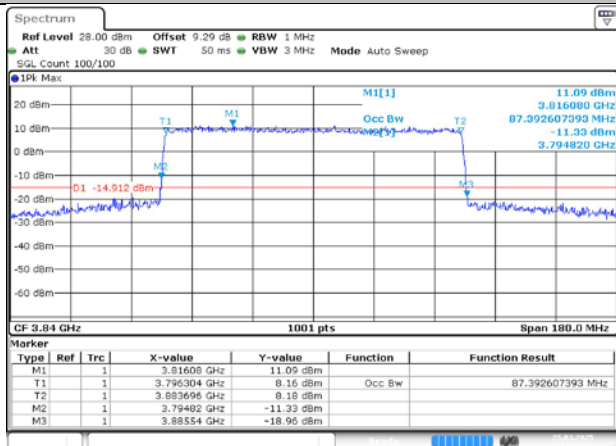
Date: 25.MAR.2025 13:43:18

MCH / CP-16QAM / Outer_Full



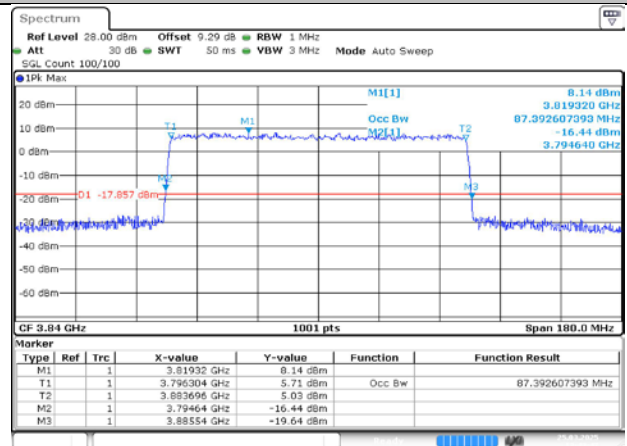
Date: 25.MAR.2025 13:43:27

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:43:36

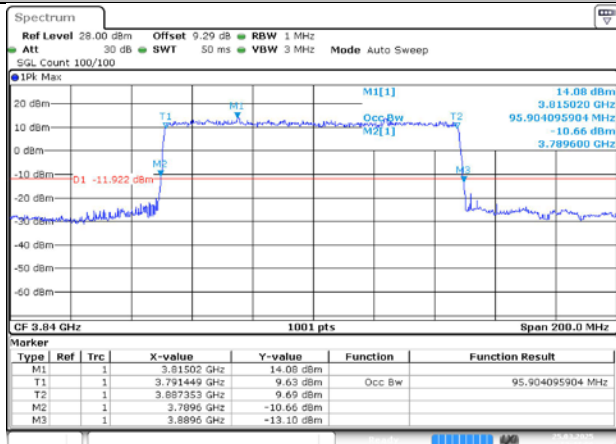
MCH / CP-256QAM / Outer_Full



Date: 25.MAR.2025 13:43:47

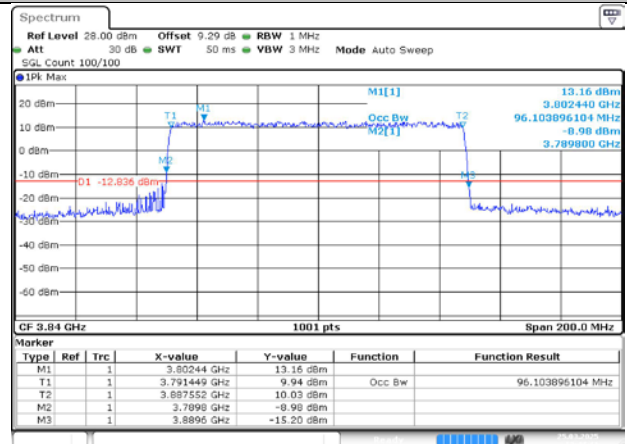
n77A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



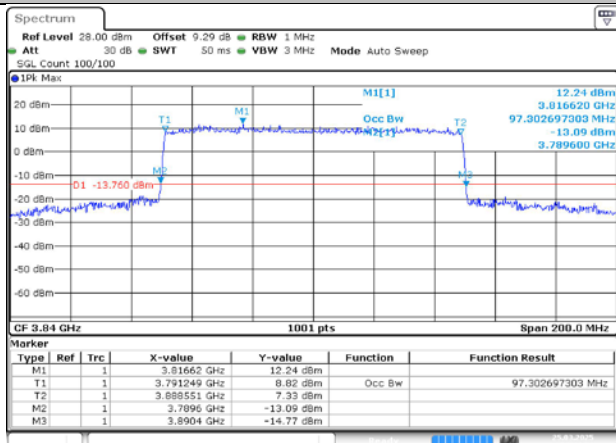
Date: 25.MAR.2025 13:44:00

MCH / DFT-QPSK / Outer_Full



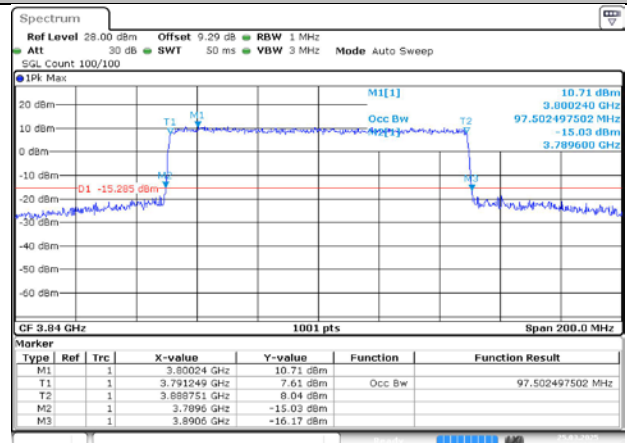
Date: 25.MAR.2025 13:44:11

MCH / CP-QPSK / Outer_Full



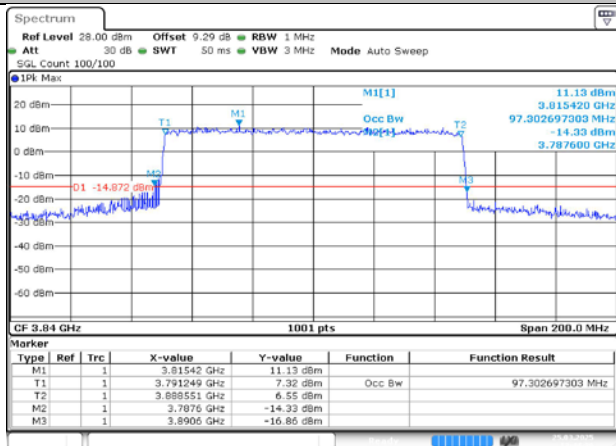
Date: 25.MAR.2025 13:44:21

MCH / CP-16QAM / Outer_Full



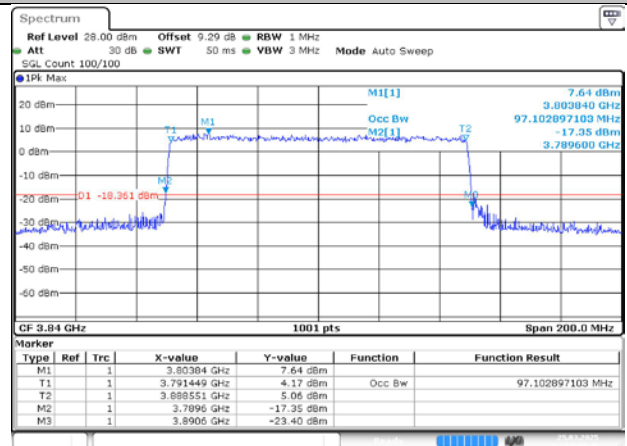
Date: 25.MAR.2025 13:44:30

MCH / CP-64QAM / Outer_Full



Date: 25.MAR.2025 13:44:39

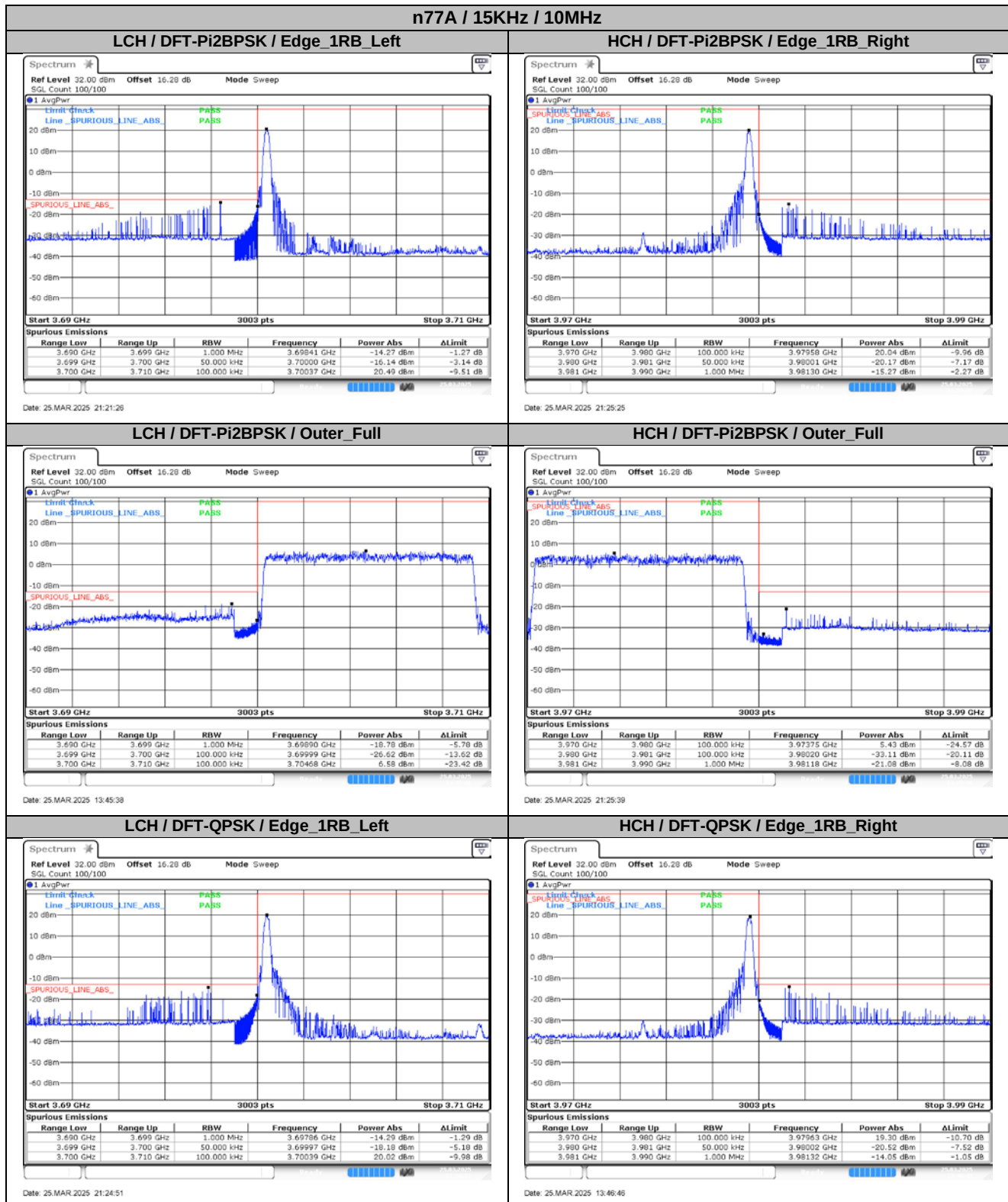
MCH / CP-256QAM / Outer_Full

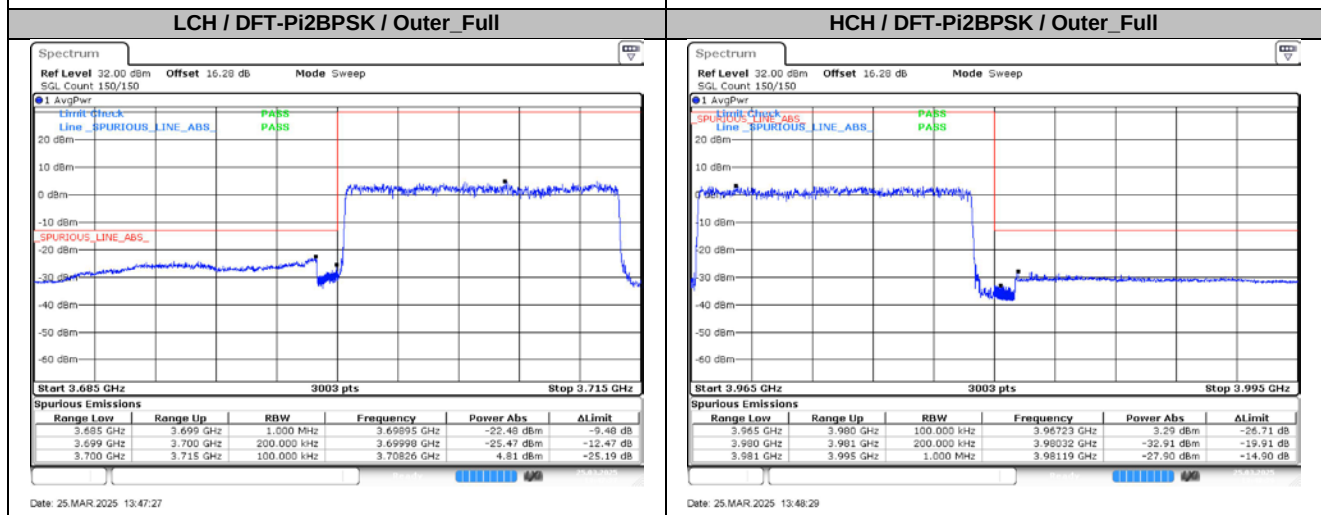
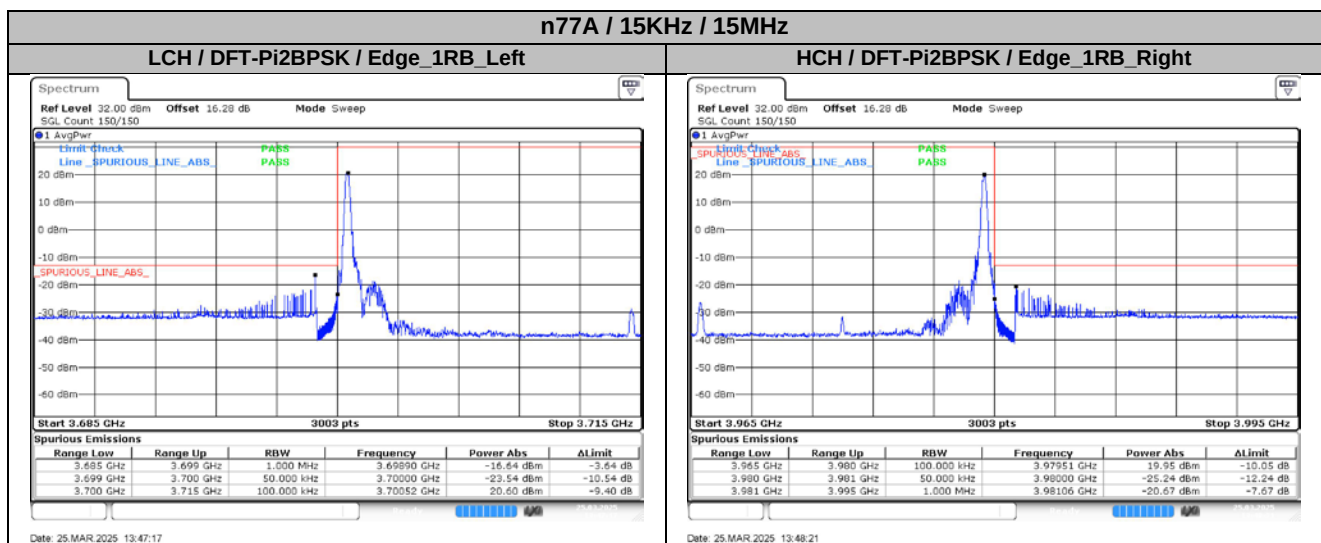
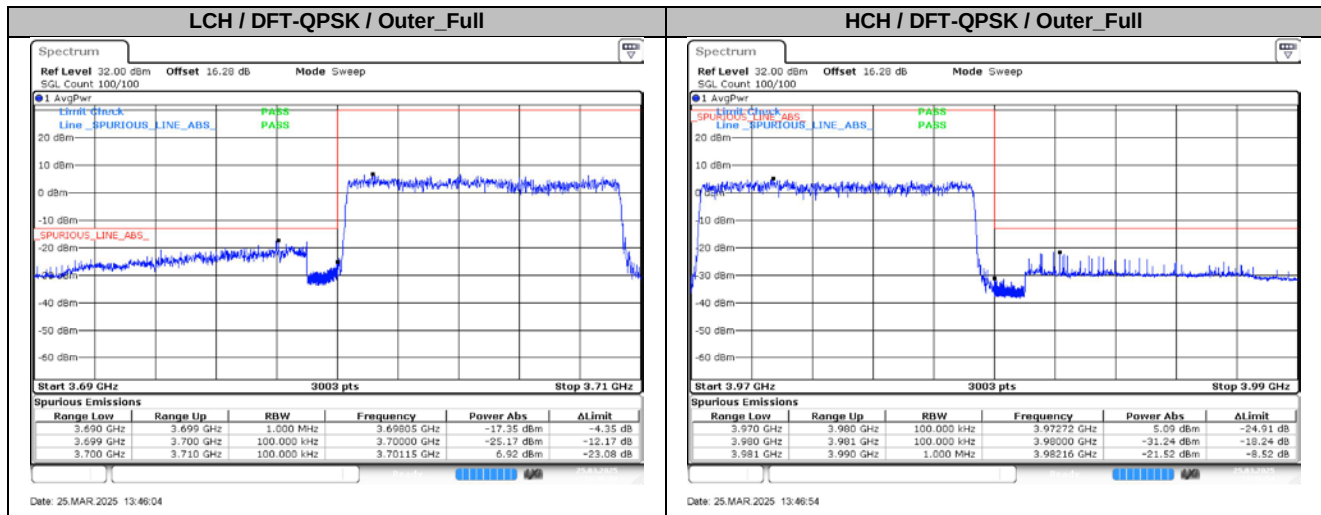


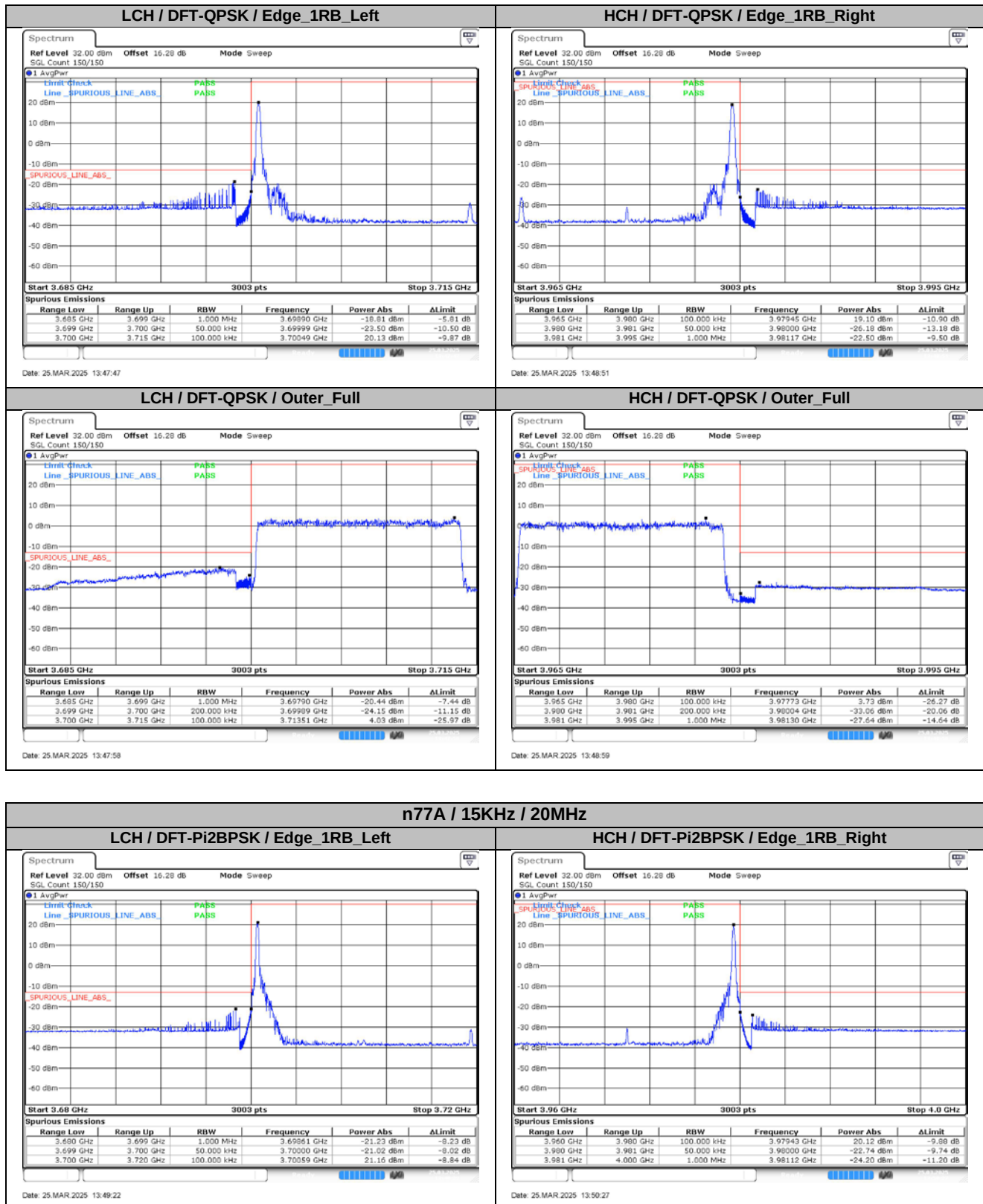
Date: 25.MAR.2025 13:44:49

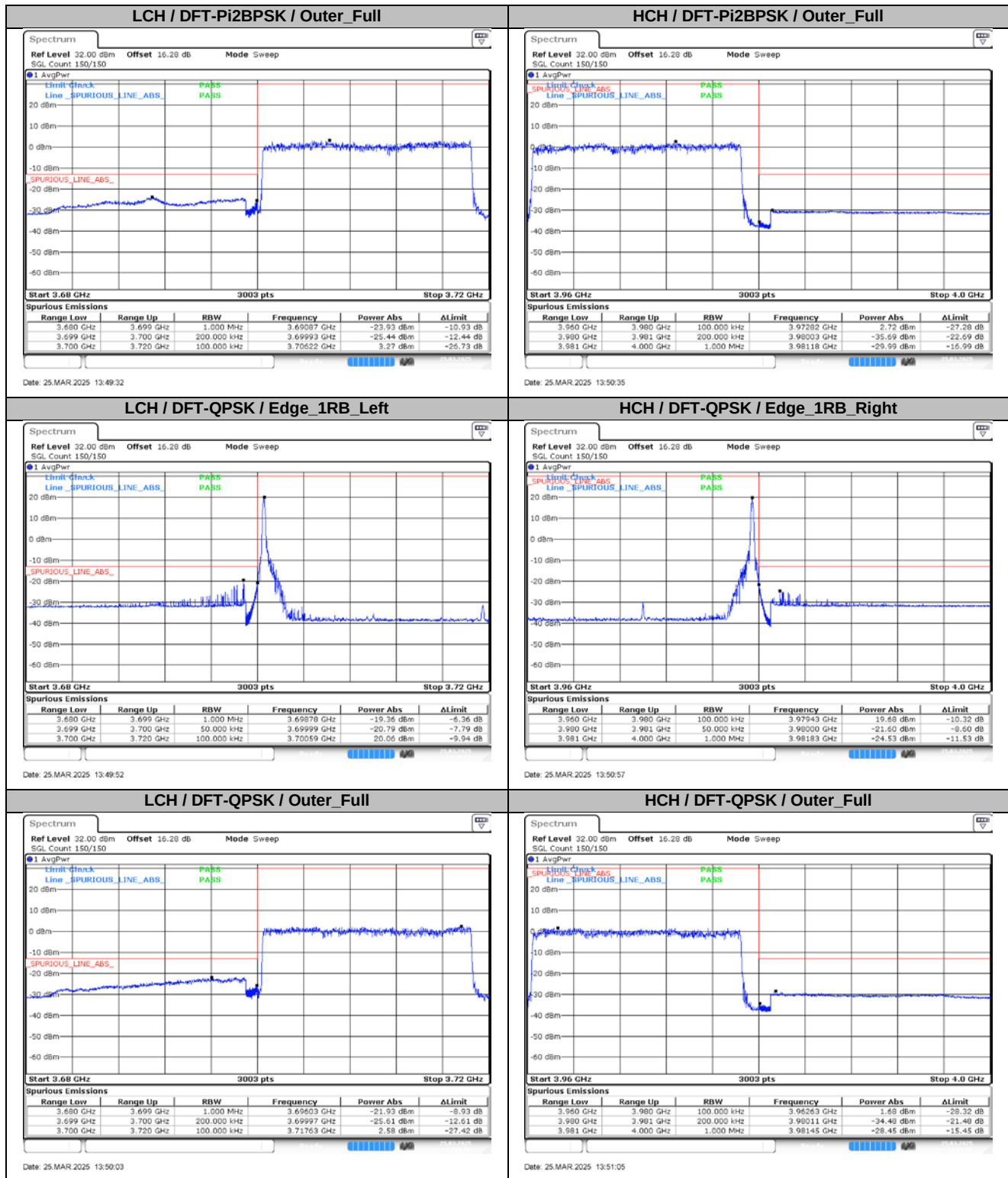
5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz



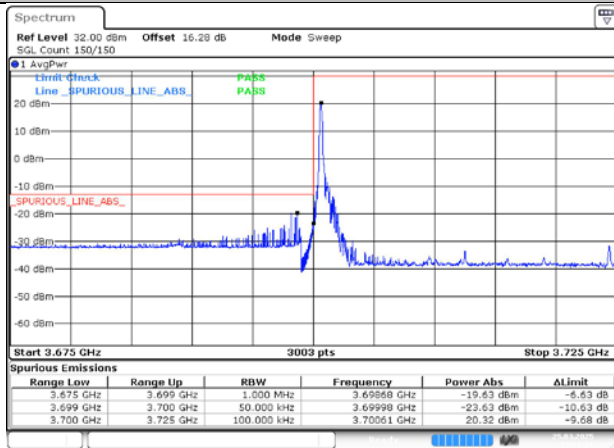




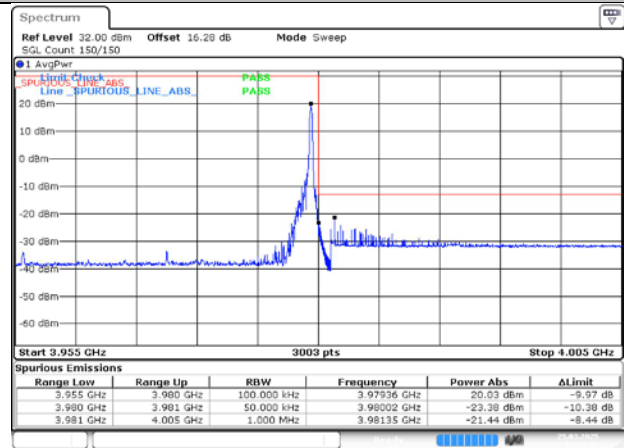


n77A / 15KHz / 25MHz

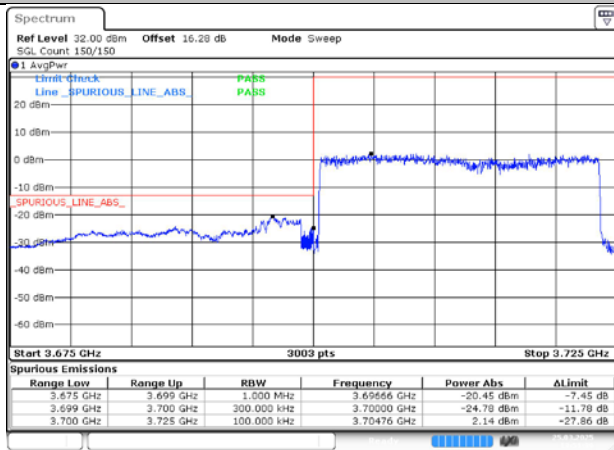
LCH / DFT-Pi2BPSK / Edge_1RB_Left



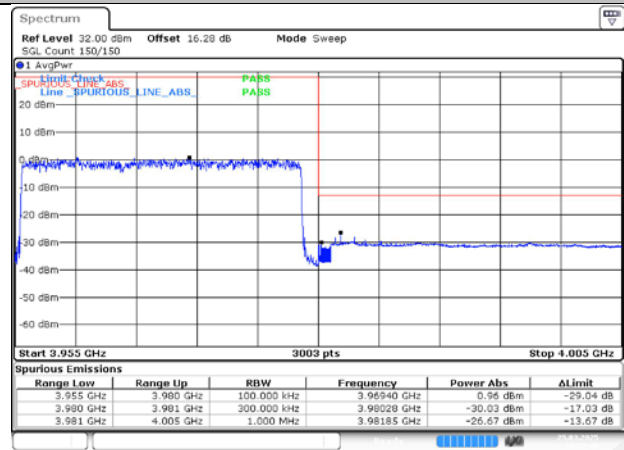
HCH / DFT-Pi2BPSK / Edge_1RB_Right



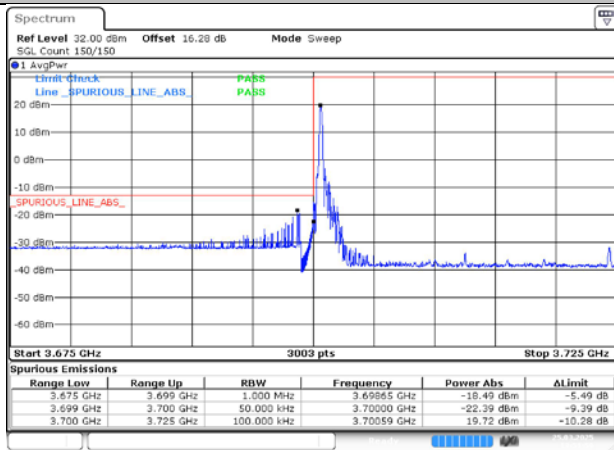
LCH / DFT-Pi2BPSK / Outer_Full



HCH / DFT-Pi2BPSK / Outer_Full



LCH / DFT-QPSK / Edge_1RB_Left



HCH / DFT-QPSK / Edge_1RB_Right

