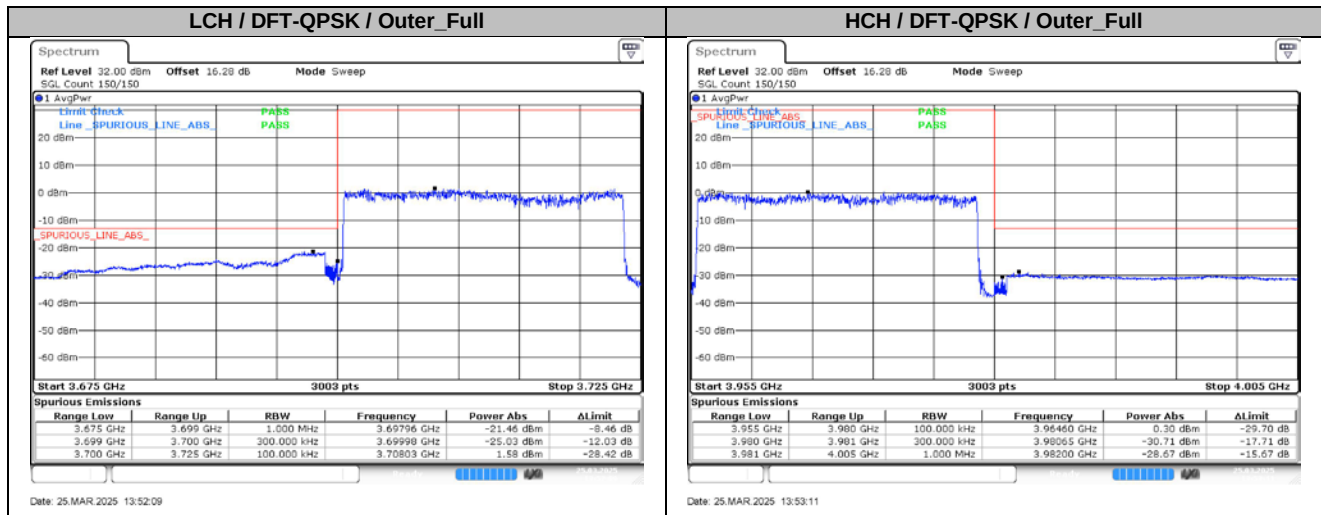
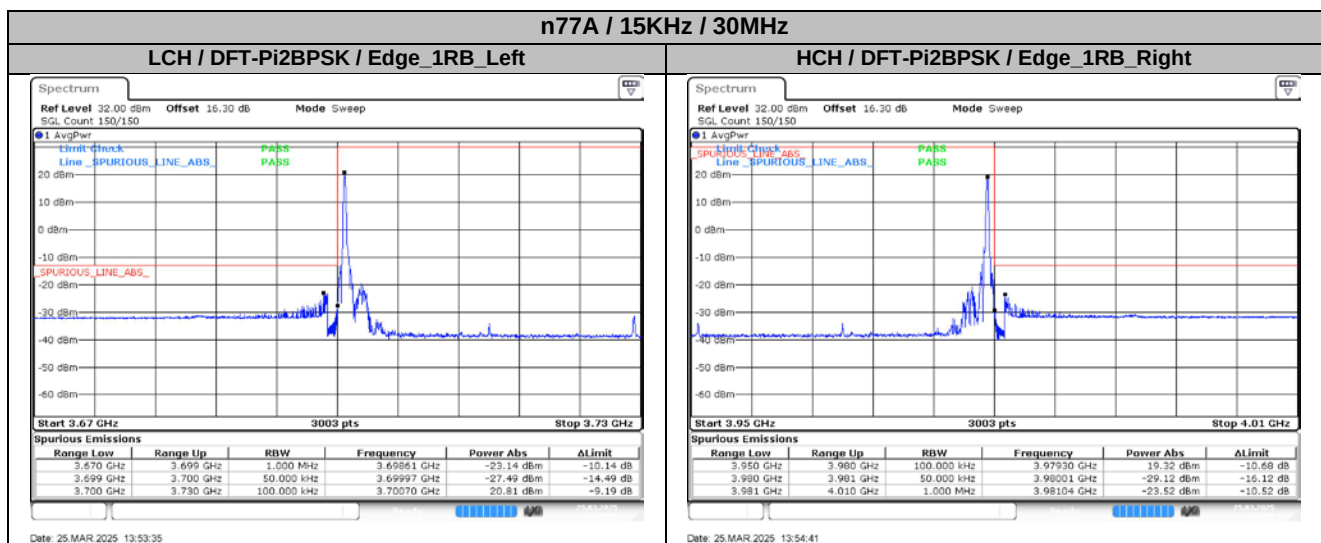
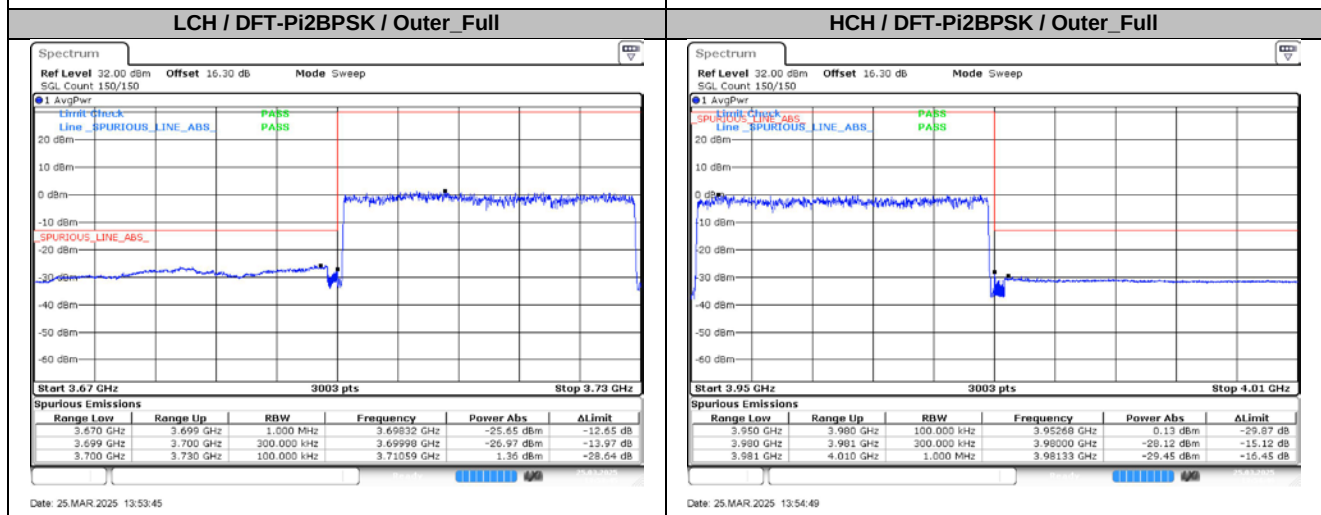
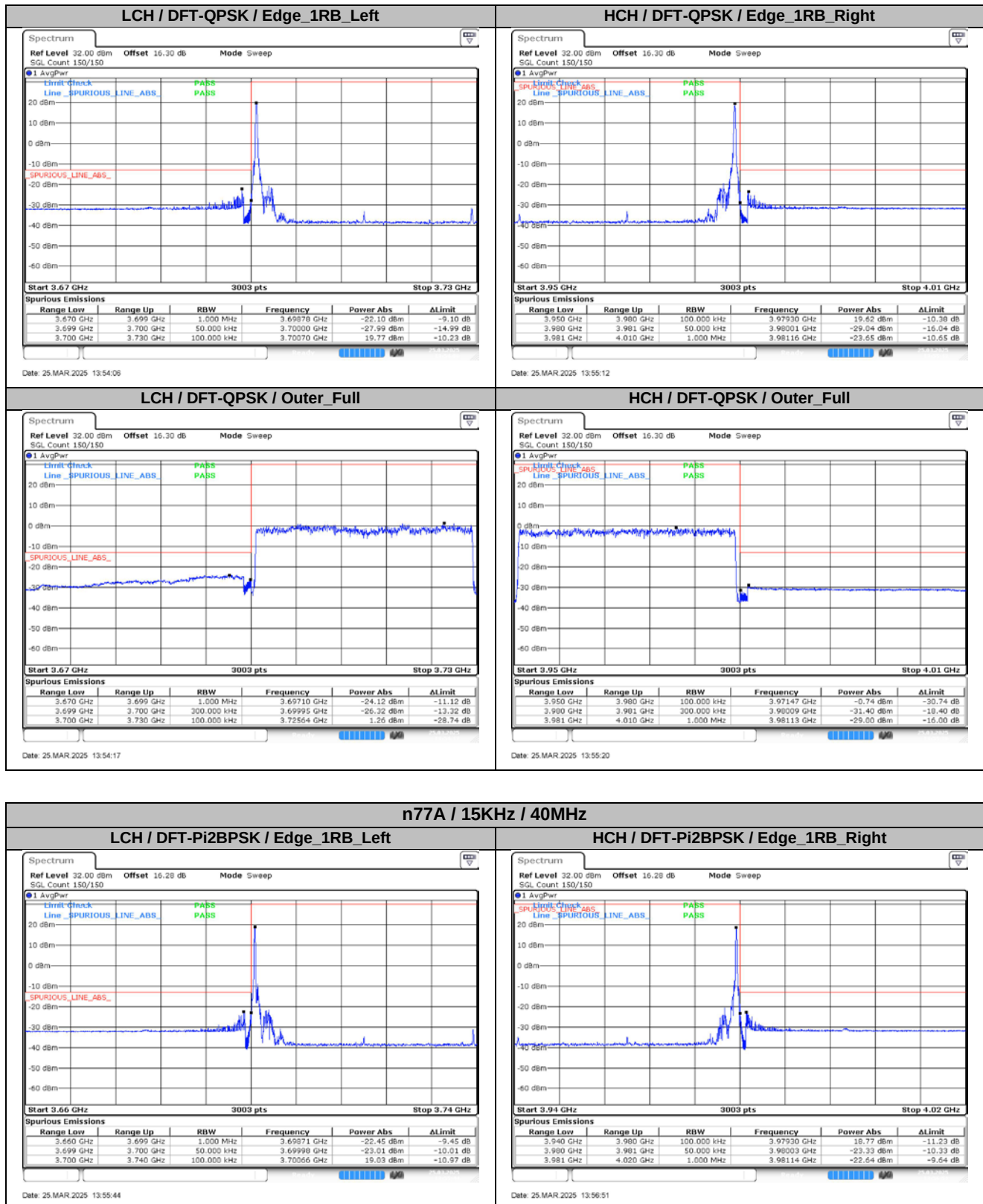
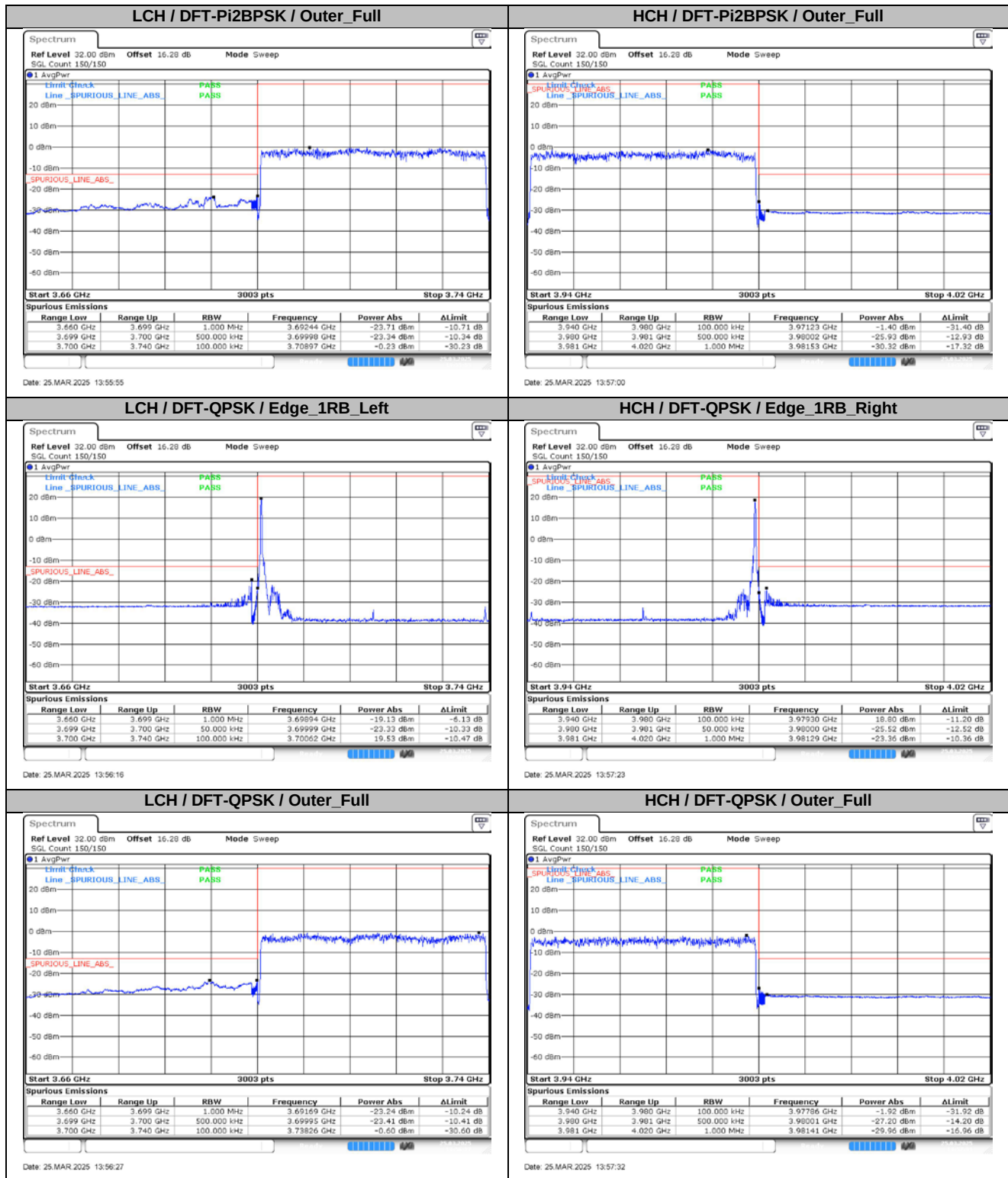


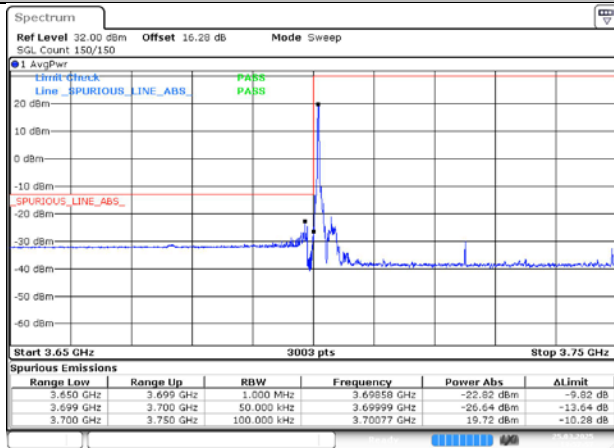




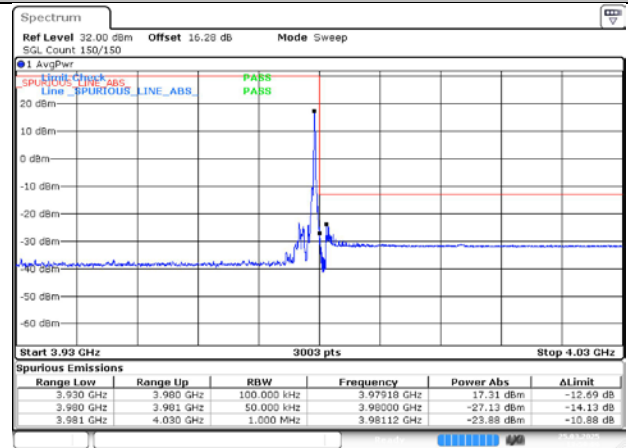


n77A / 15KHz / 50MHz

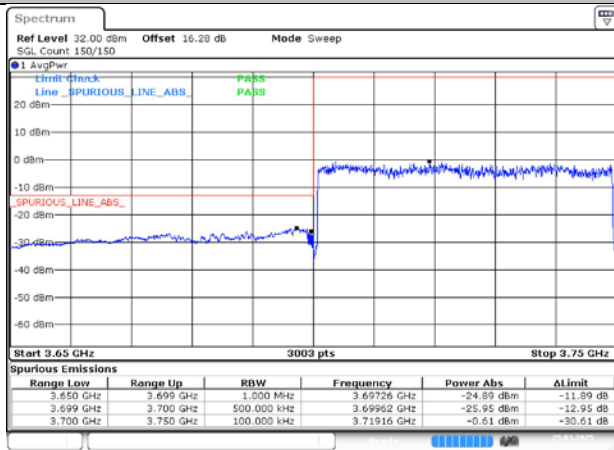
LCH / DFT-Pi2BPSK / Edge_1RB_Left



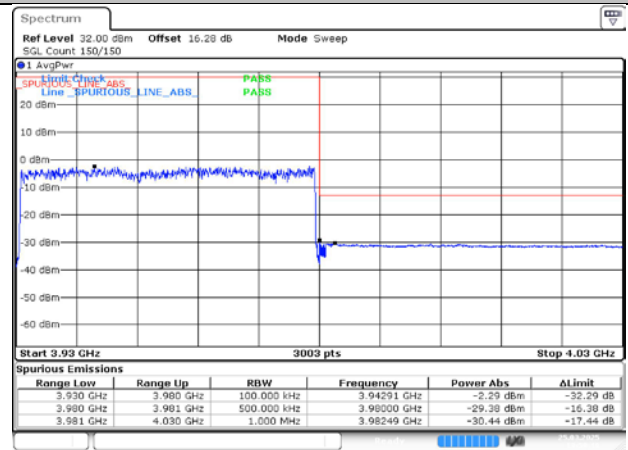
HCH / DFT-Pi2BPSK / Edge_1RB_Right



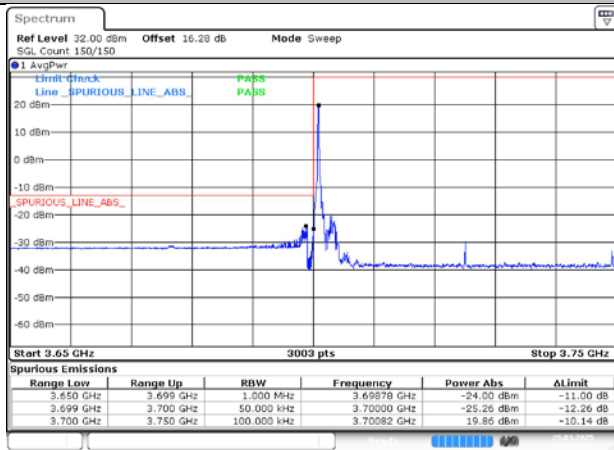
LCH / DFT-Pi2BPSK / Outer_Full



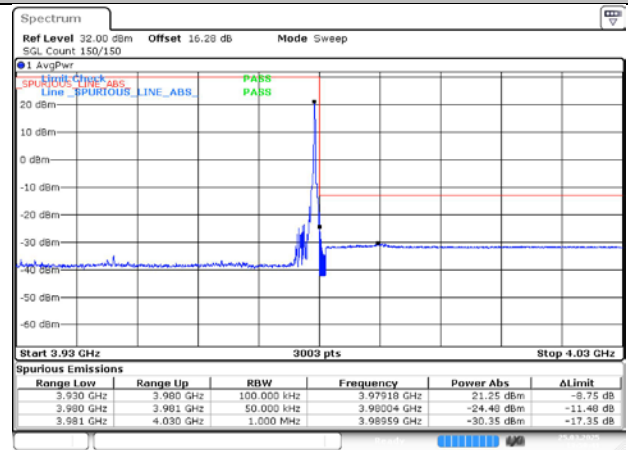
HCH / DFT-Pi2BPSK / Outer_Full

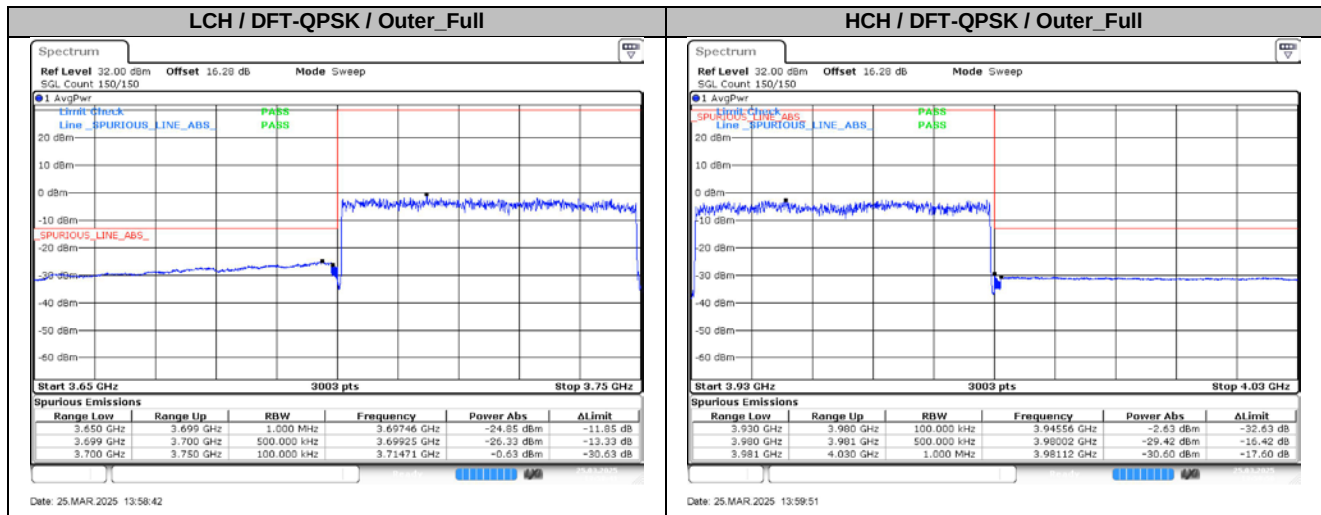


LCH / DFT-QPSK / Edge_1RB_Left

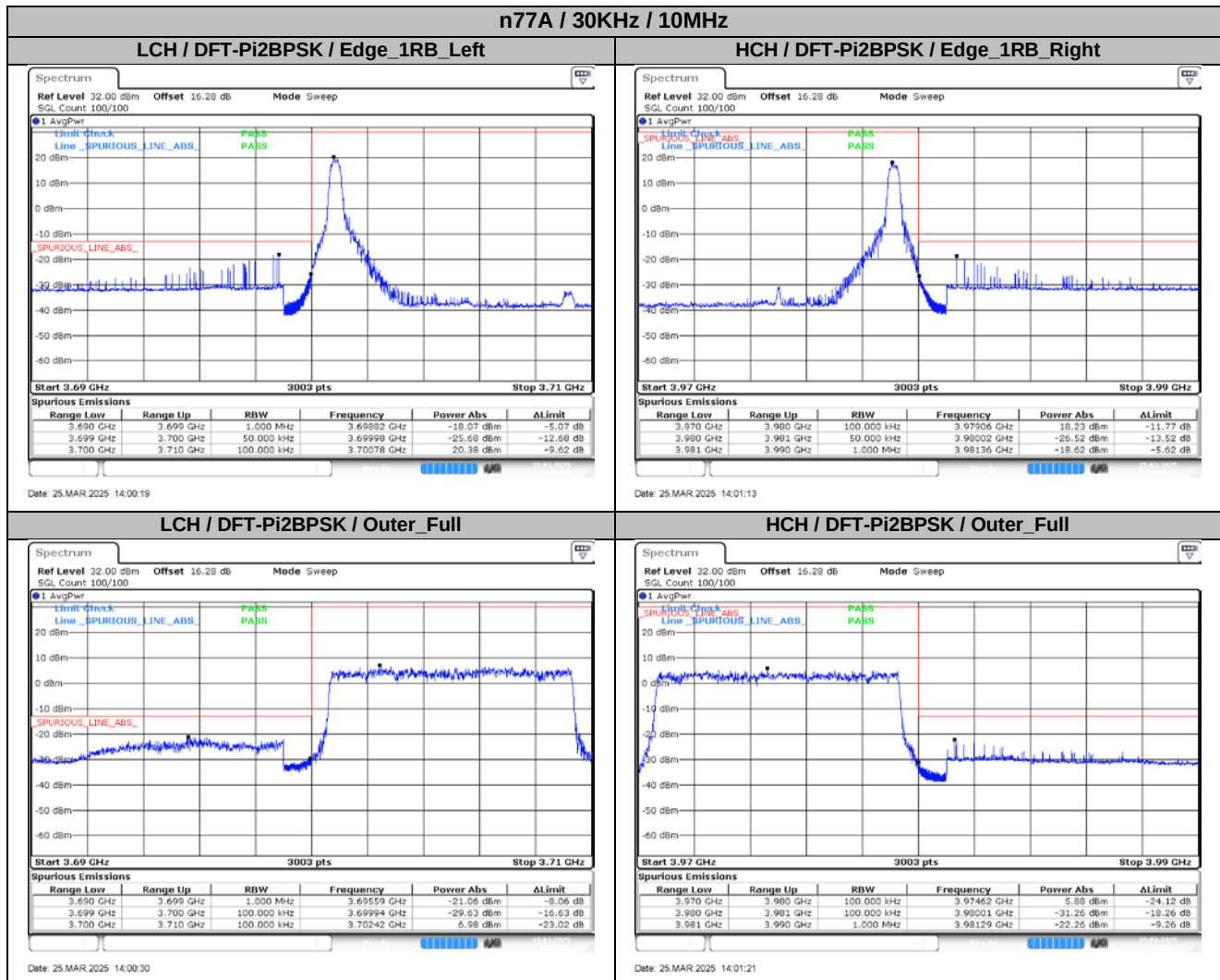


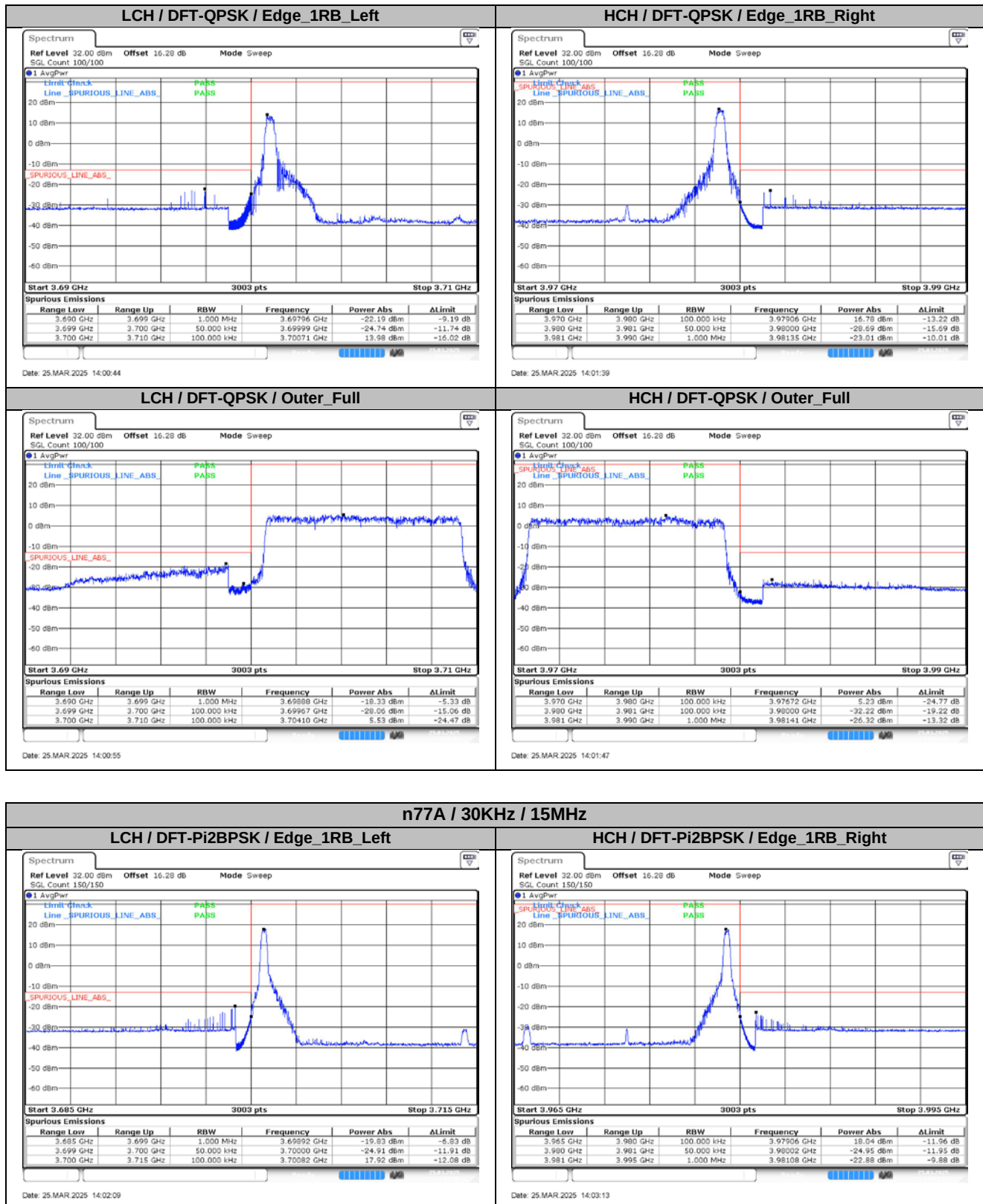
HCH / DFT-QPSK / Edge_1RB_Right

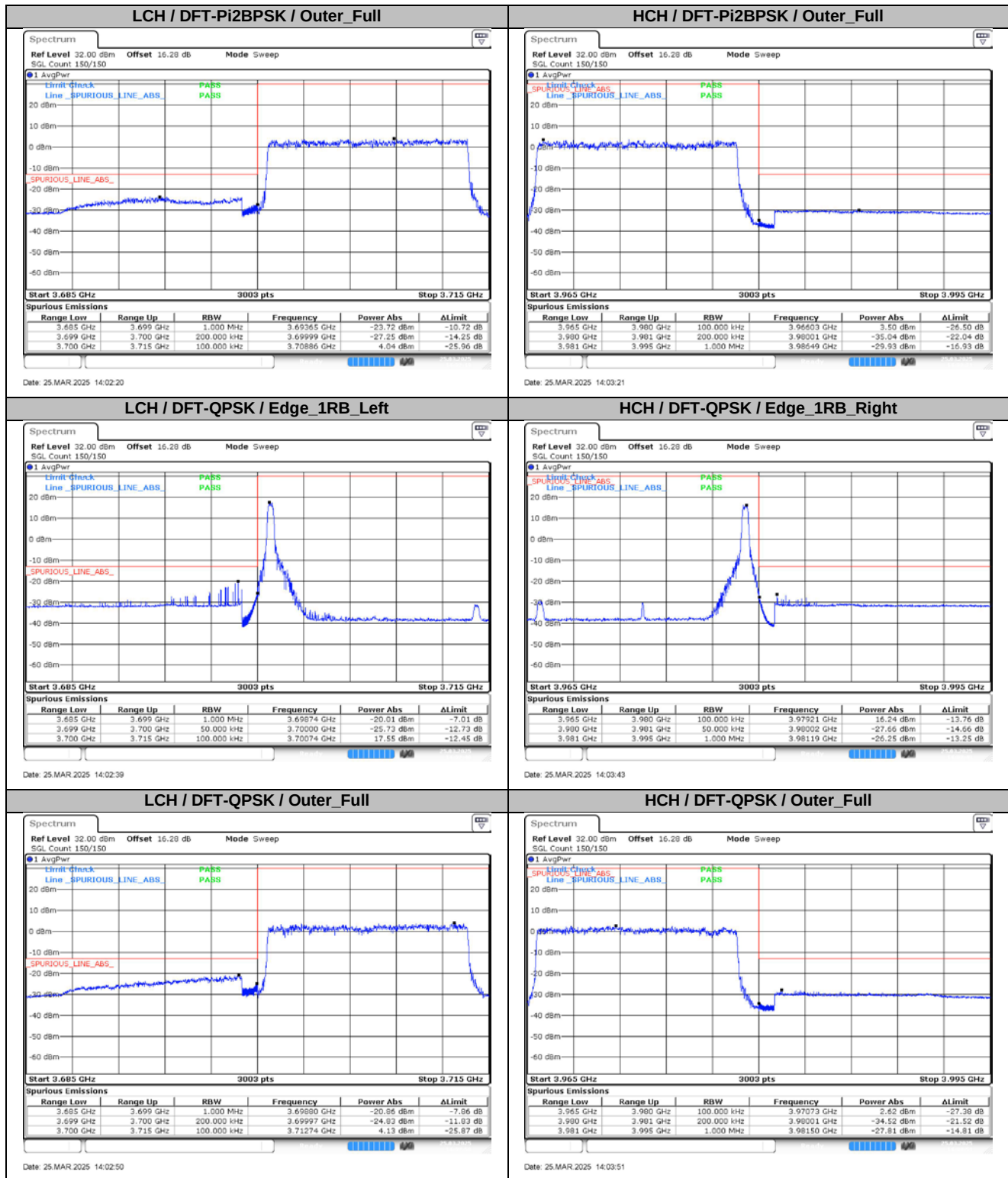




5.2. Test Plots for SCS=30KHz

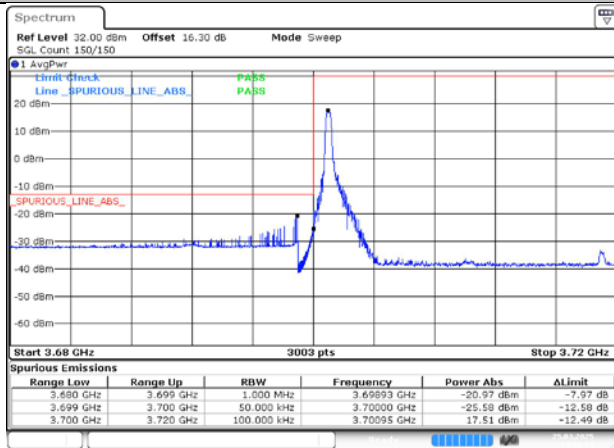




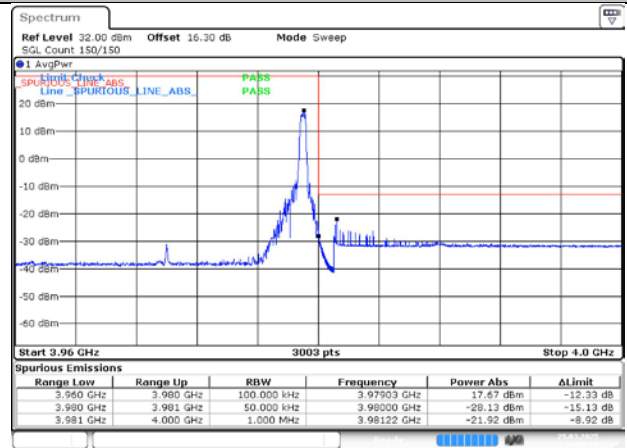


n77A / 30KHz / 20MHz

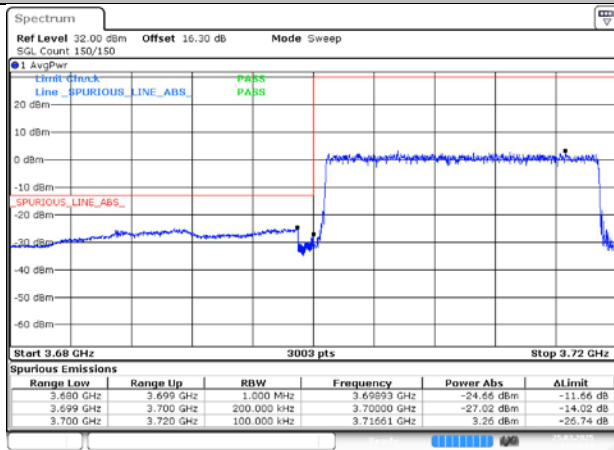
LCH / DFT-Pi2BPSK / Edge_1RB_Left



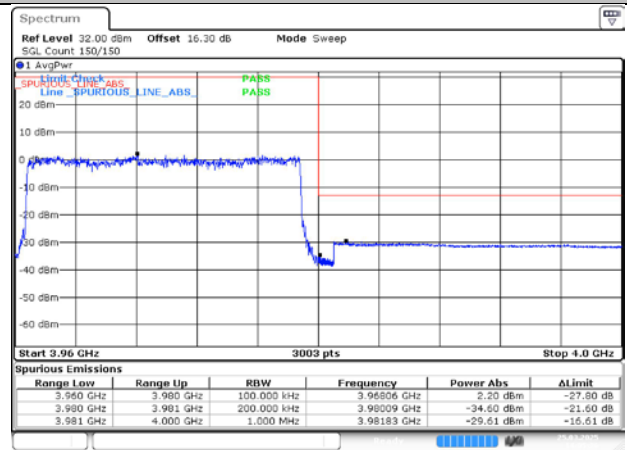
HCH / DFT-Pi2BPSK / Edge_1RB_Right



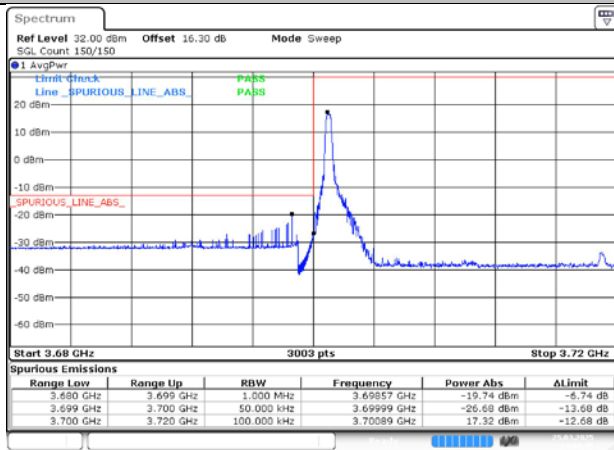
LCH / DFT-Pi2BPSK / Outer_Full



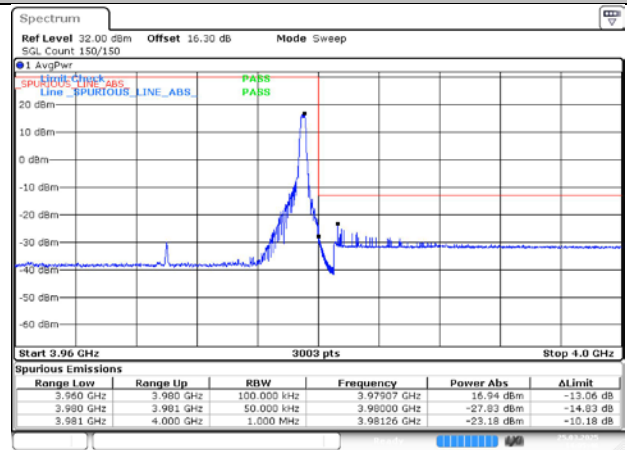
HCH / DFT-Pi2BPSK / Outer_Full

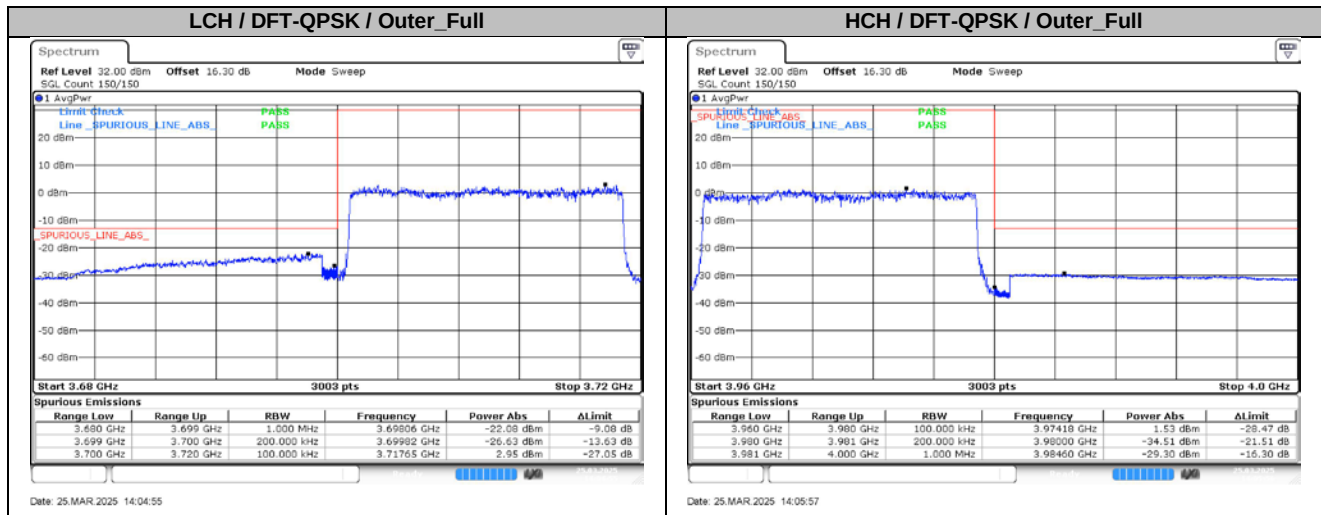


LCH / DFT-QPSK / Edge_1RB_Left

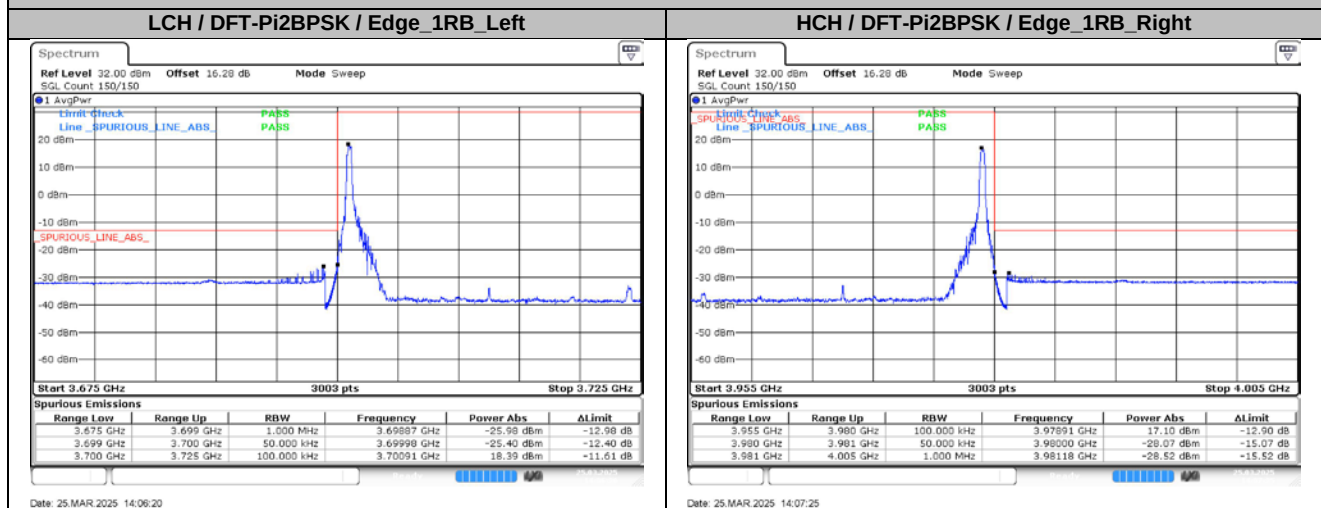
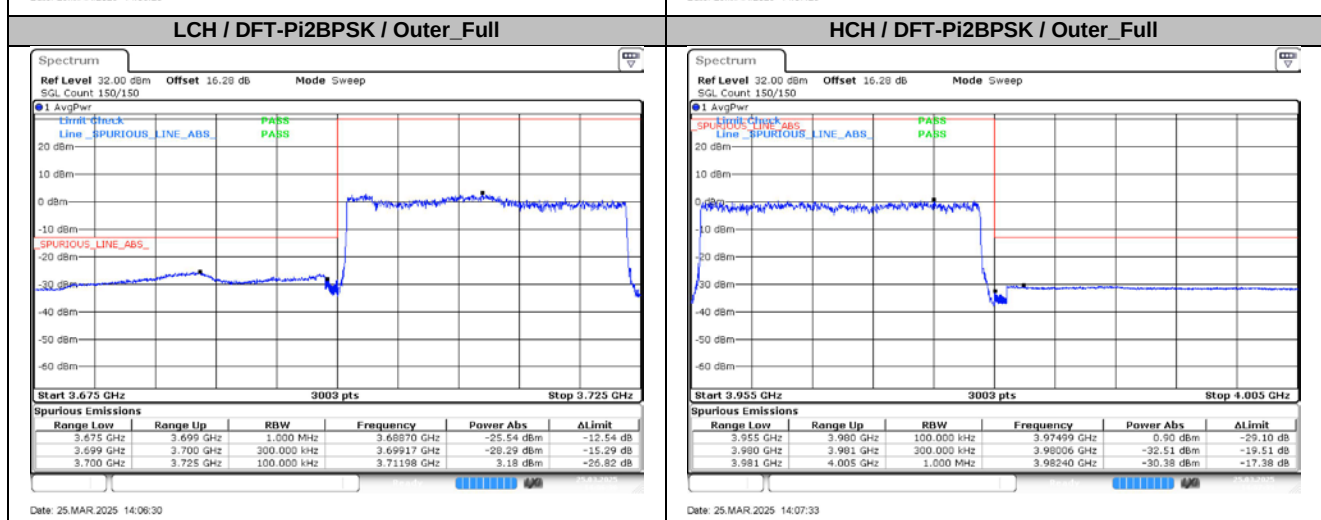


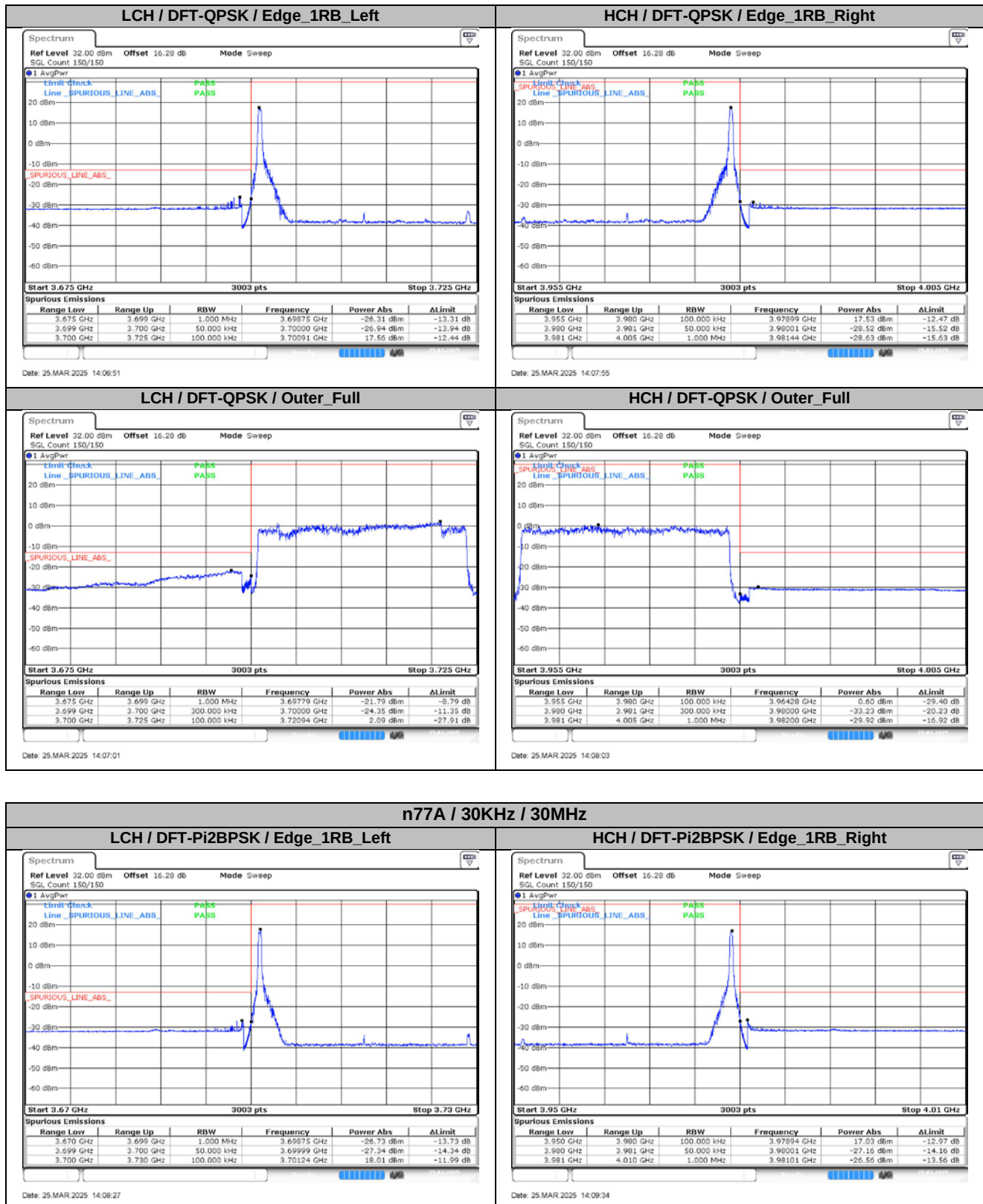
HCH / DFT-QPSK / Edge_1RB_Right

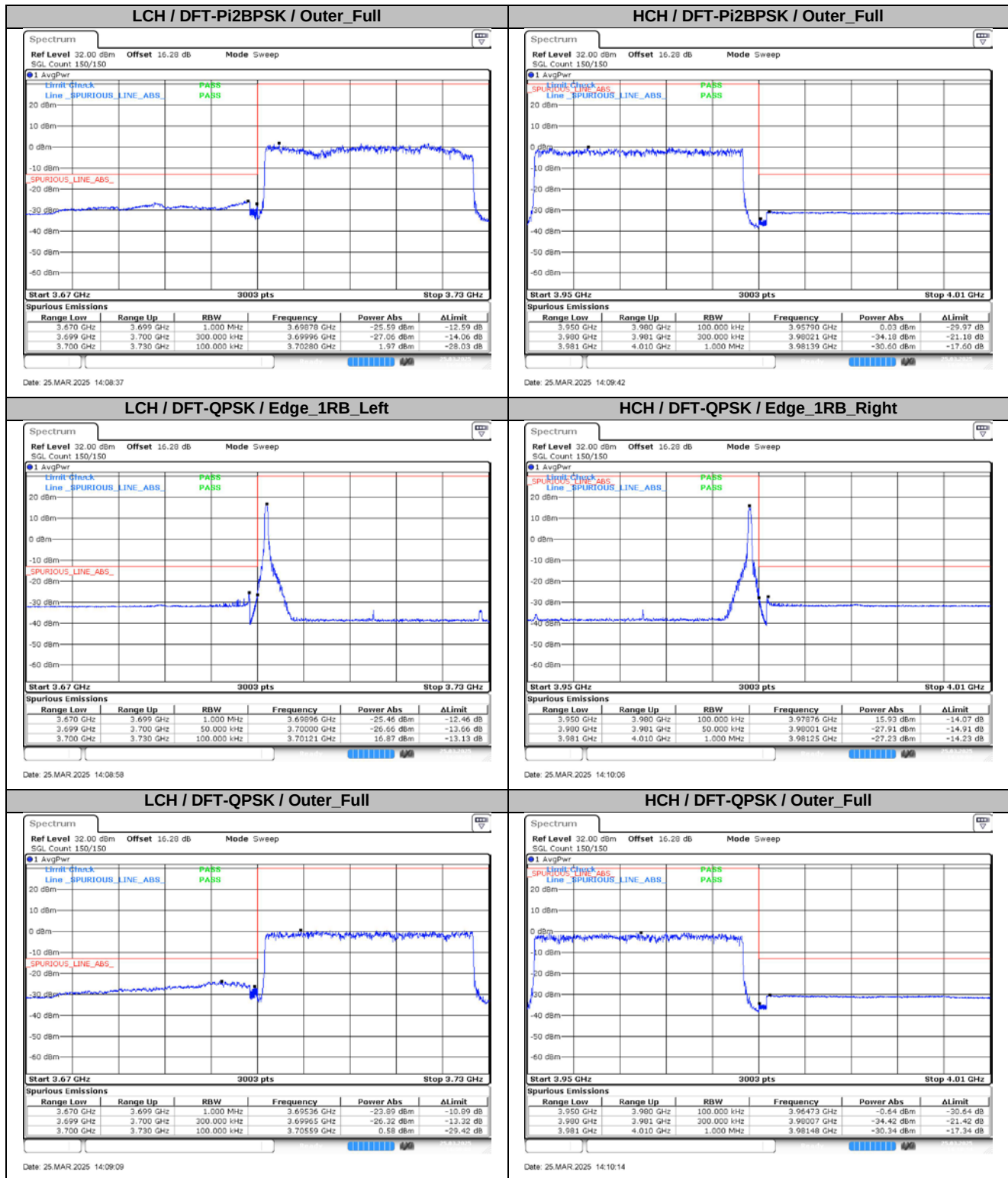




n77A / 30KHz / 25MHz

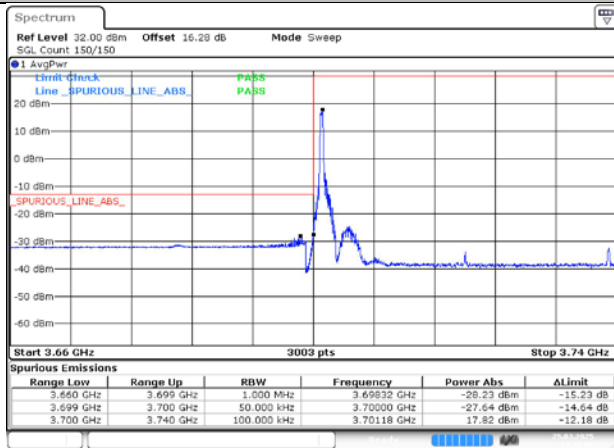




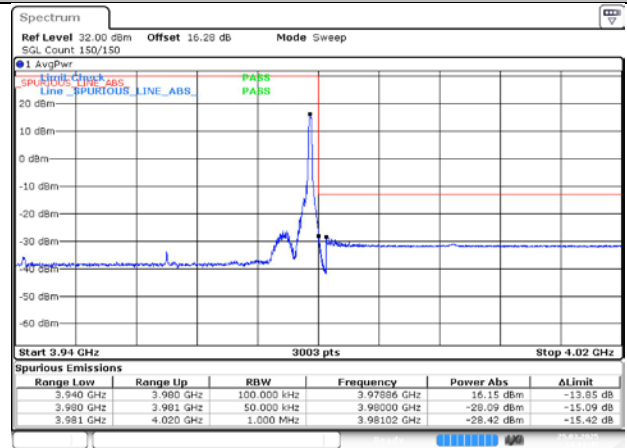


n77A / 30KHz / 40MHz

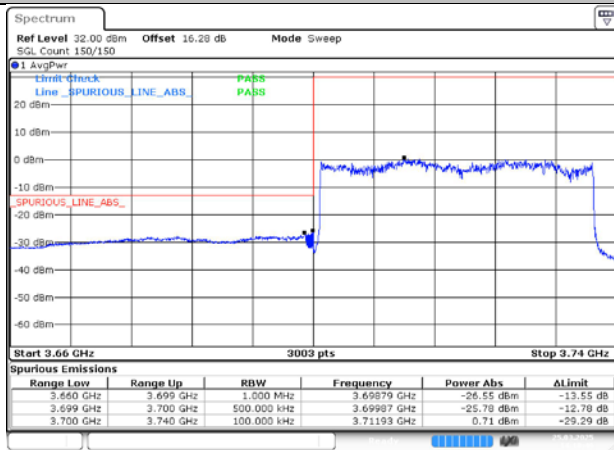
LCH / DFT-Pi2BPSK / Edge_1RB_Left



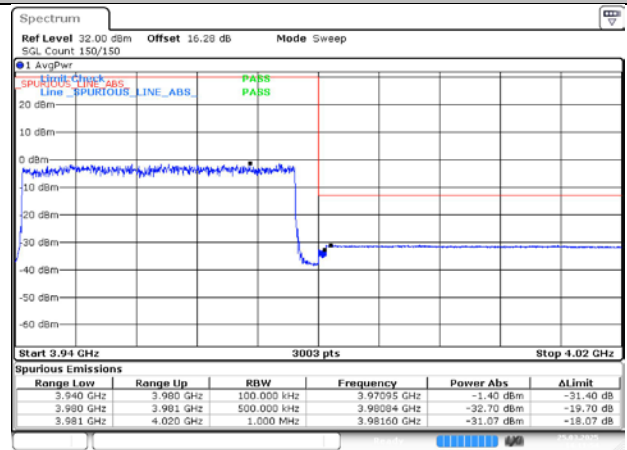
HCH / DFT-Pi2BPSK / Edge_1RB_Right



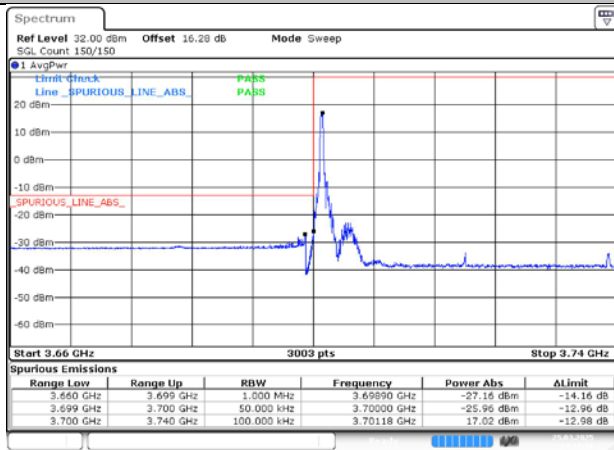
LCH / DFT-Pi2BPSK / Outer_Full



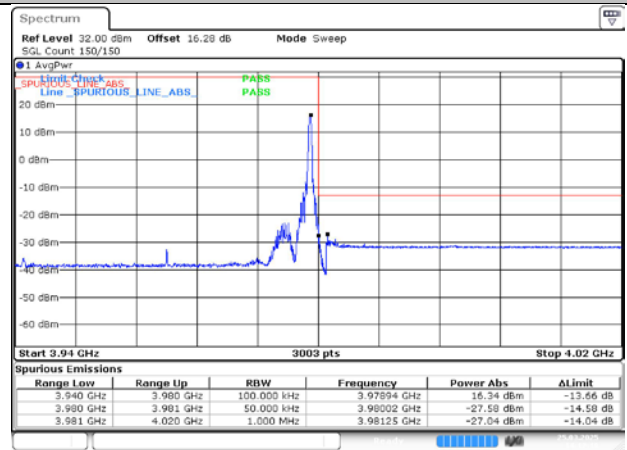
HCH / DFT-Pi2BPSK / Outer_Full

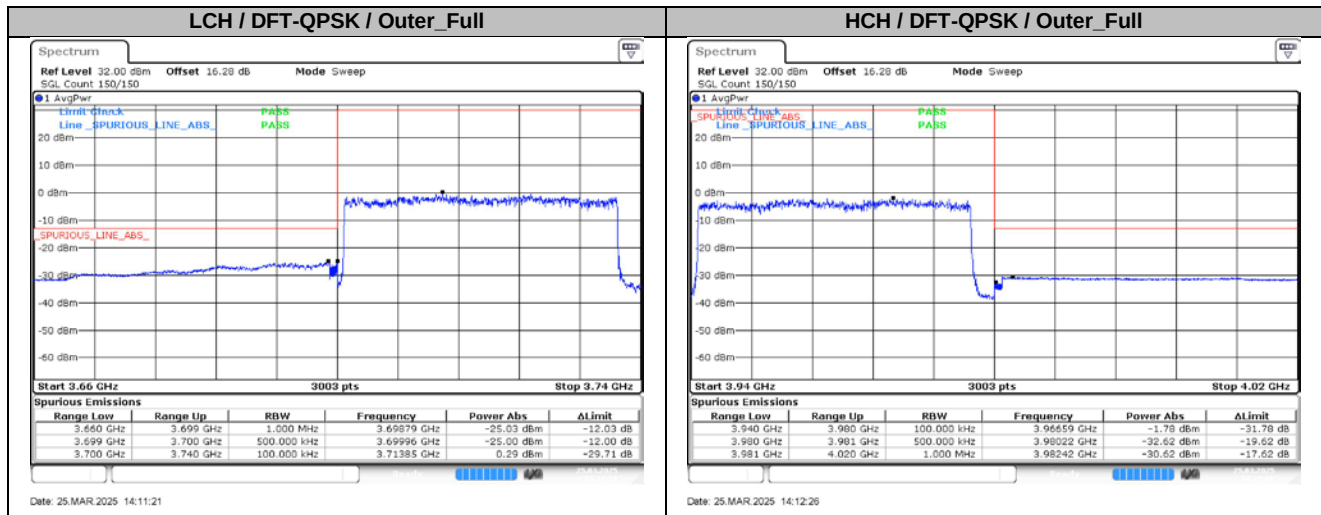


LCH / DFT-QPSK / Edge_1RB_Left



HCH / DFT-QPSK / Edge_1RB_Right





n77A / 30KHz / 50MHz

